

IO Pad_Signal Count	Design Signal Pad Name	Direction	Operating Voltage	IO Power Group (15x15)	IO Power Group (13x13)	PUPD State During HHV	PUPD State After HHV	Default Pinmux Mode	Default Pinmux Mode	Boot Config / Special Pins	LOAD CONDITIONS				13 x 13 BGA	15 x 15 BGA	IO Power Supply Name			Mode0				Mode1					Mode2					Mode3					Mode4					Mode5					Mode6					Mode7					Data Manual Name	Register Control Offset in Control Module	Ball Reset State	Ball Reset Rel. State	Ball Rel. Mode	13x13 Power / 15x15 Power	Hys	Buffer Strength (mA)	Pull Up/Down Type	IO Cell	Note (column #, column #, ...):																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																			
											MIN Load (pF)	MAX Load (pF)	MAX PCB Trace Length (mm)		13x13 BGA Pac	15x15 BGA Package	Mode0 Interface N	Mode0 Signal N	Mode0 Type		Mode1 Interfac	Mode1 Signal N	Mode1 Type		Mode2 Interfac	Mode2 Signal N	Mode2 Type		Mode3 Interfac	Mode3 Signal N	Mode3 Type		Mode4 Interfac	Mode4 Signal N	Mode4 Type		Mode5 Interfac	Mode5 Signal N	Mode5 Type		Mode6 Interfac	Mode6 Signal N	Mode6 Type		Mode7 Interfac	Mode7 Signal Name	Mode7 Type																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																									