



– About RX_ER –

- About RX_ER, the datasheet shows following;
- RX_ER indicates that an invalid symbol has been detected within a received packet in 100 Mb/s mode.
 - Assert high synchronously to X1 when-ever it detects a media error and RXDV is asserted in 100 Mb/s mode.

The Left wave form is what our customer measured.

<Q1>

When RX_CLK stops oscillating, does RX_ER assert high?

As my recognition, regardless of RX_ER high or low, RX_CLK always oscillates.

Is my recognition correct or not?

Please let know us.

– Clock In (X1) Requirements –

<Q2>

Our customer is designed like as right value of capacitances, C229=10pF, c230=12pF. $CL = 2 \times \text{Crystal load spec} - 7 \text{ pF} = 16 - 7 = 9 \text{ pF}$
So, does it have to be used 9pF capacitance for C229, and c230?

<Q3>

AT cut crystal with a minimum drive level of 100μW and a maximum of 500μW. So, the crystal excitation level is 10μW (Max. 200μW), a crystal is specified for a lower drive, should level a current limiting resistor be placed in series between X2 and the crystal?

