

1	GND	NA	NA	Ground	
2	SDA	IO	VIO_Serial	Serial Interface Data I/O	
3	SCL	I	VIO_Serial	Serial Interface Clock Input	
4	DATA 0	I	1.8V ~ 3.3V	Y0	Y0/Cb0/Cr0
5	DATA 1	I	1.8V ~ 3.3V	Y1	Y1/Cb1/Cr1
6	DATA 2	I	1.8V ~ 3.3V	Y2	Y2/Cb2/Cr2
7	DATA 3	I	1.8V ~ 3.3V	Y3	Y3/Cb3/Cr3
8	DATA 4	I	1.8V ~ 3.3V	Y4	Y4/Cb4/Cr4
9	DATA 5	I	1.8V ~ 3.3V	Y5	Y5/Cb5/Cr5
10	DATA 6	I	1.8V ~ 3.3V	Y6	Y6/Cb6/Cr6
11	DATA 7	I	1.8V ~ 3.3V	Y7	Y7/Cb7/Cr7
12	DATA 8	I	1.8V ~ 3.3V	Cb0/Cr0	GND
13	DATA 9	I	1.8V ~ 3.3V	Cb1/Cr1	GND
14	DATA 10	I	1.8V ~ 3.3V	Cb2/Cr2	GND
15	DATA 11	I	1.8V ~ 3.3V	Cb3/Cr3	GND
16	DATA 12	I	1.8V ~ 3.3V	Cb4/Cr4	GND
17	DATA 13	I	1.8V ~ 3.3V	Cb5/Cr5	GND
18	DATA 14	I	1.8V ~ 3.3V	Cb6/Cr6	GND
19	DATA 15	I	1.8V ~ 3.3V	Cb7/Cr7	GND
20	NC	NA	NA	Open	
21,22	AVCC	NA	NA	Panel Analog / LED Power Supply (+5V)	
23	VSYNC	I	1.8V ~ 3.3V	Vertical Synch Signal (If non-use : GND)	
24	HSYNC	I	1.8V ~ 3.3V	Horizontal Synch Signal (If non-use : GND)	
25	VIO_Serial	NA	NA	Serial Interface I/O Power Supply	
26,27,28	VCC(*3)	NA	NA	Panel Core Power Supply (+1.8V)	
29	VCCX	NA	NA	Panel / EEPROM Power Supply (+3.3V)	