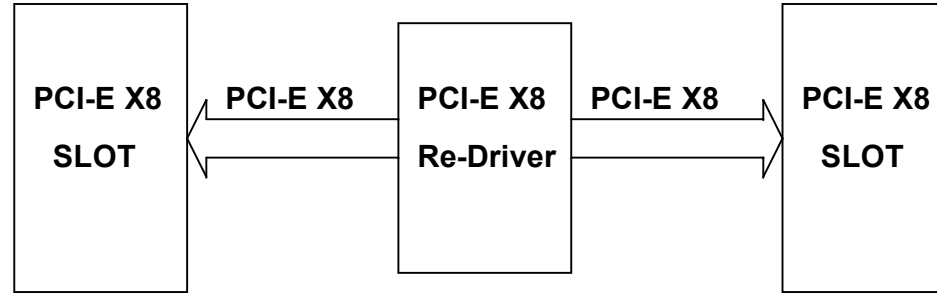
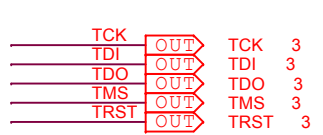


TABLE OF CONTENTS

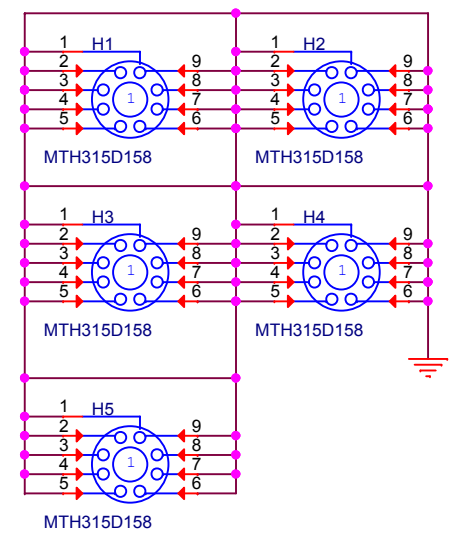
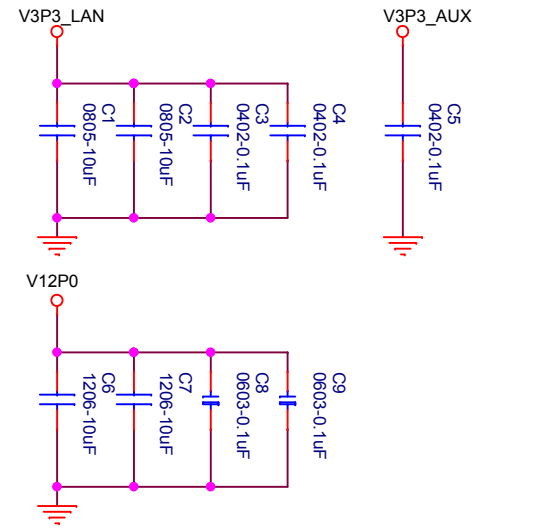
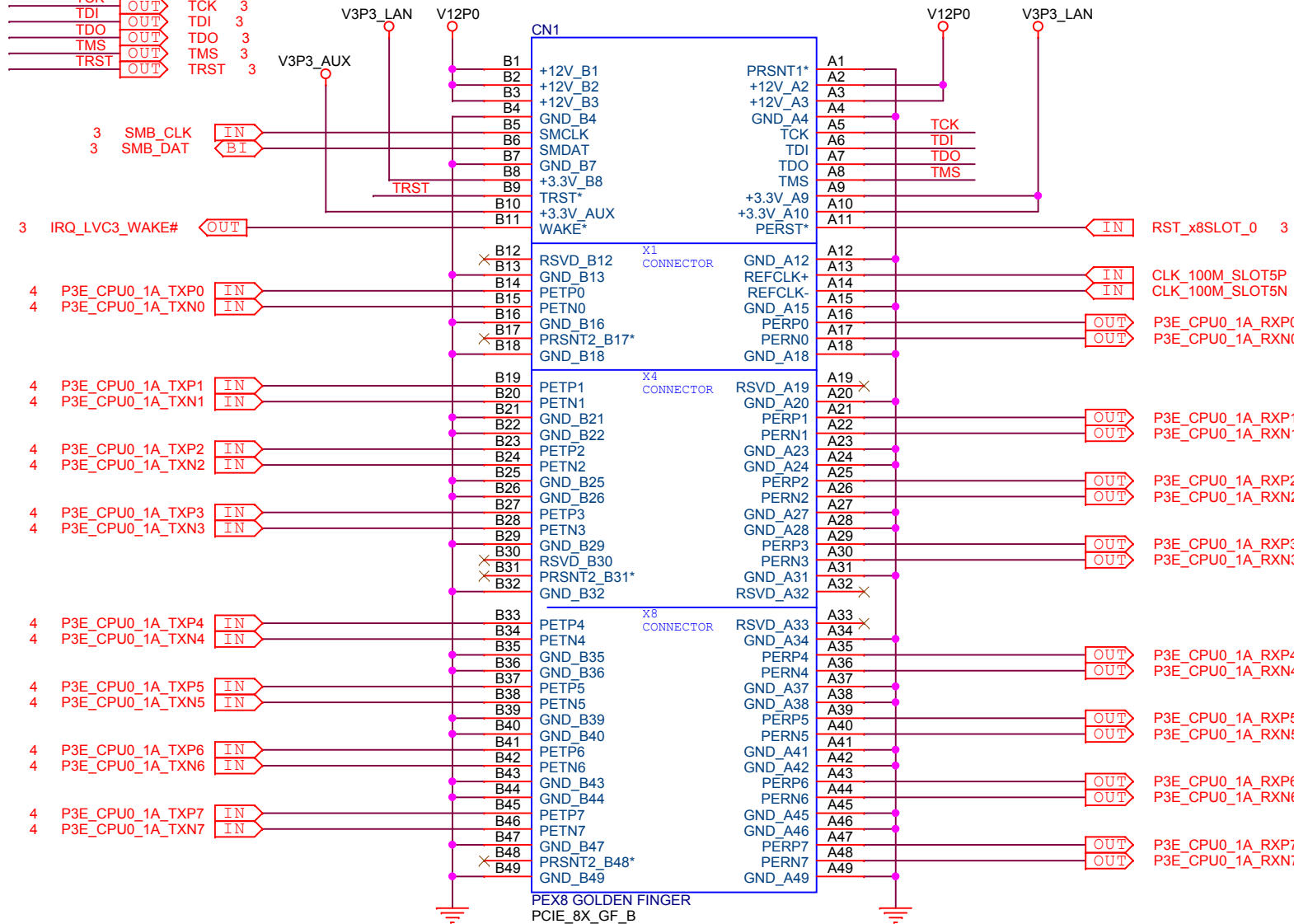
P01. TITLE & BLOCK
P02. MB PCI-E x8 GOLD FINGER
P03. SLO \bar{T} PCI-E x8 GOLD FING
P04. TI RE-DRIVER TX
P05. TI RE-DRIVER RX
P06. STACKUP
P07. HISTORY

BLOCK DIAGRAM

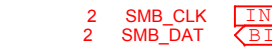




SLOT1

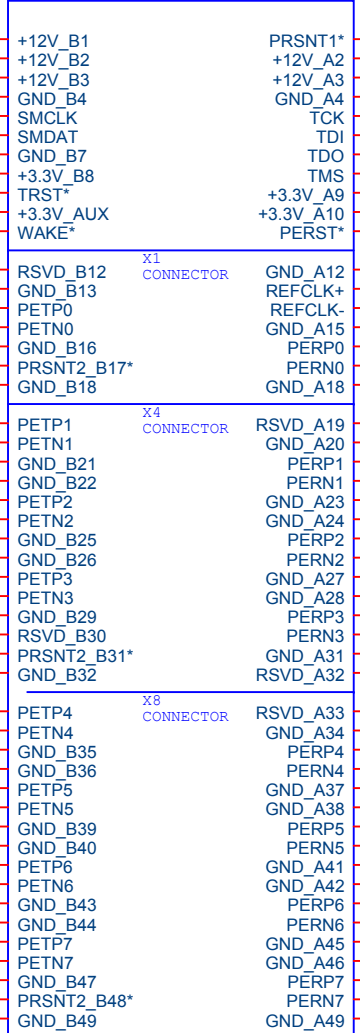


PEX8 GOLDEN FINGER
 PCIE_8X_GF_B



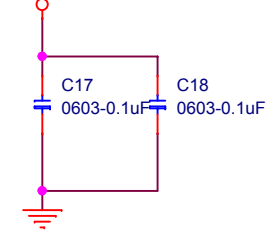
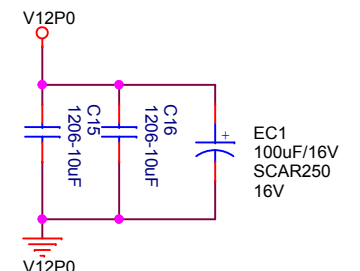
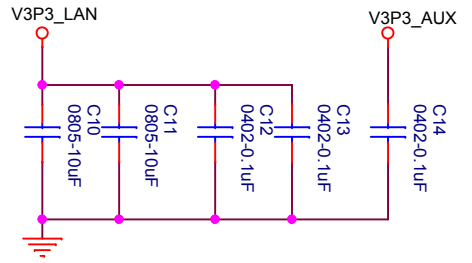
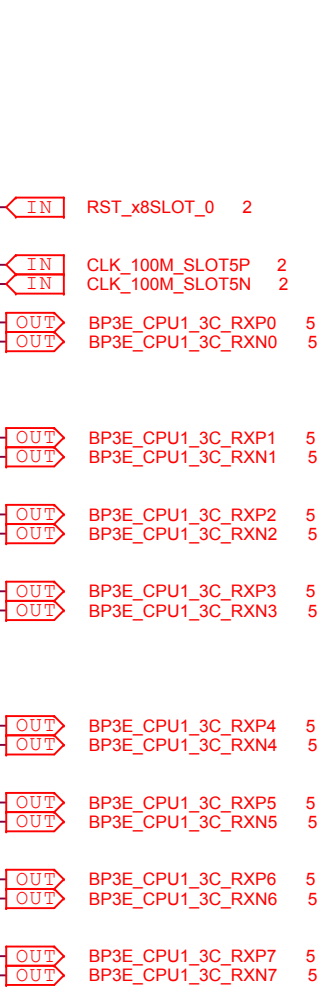
V3P3_LAN V12P0

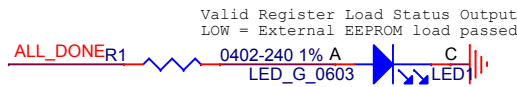
SLOT0



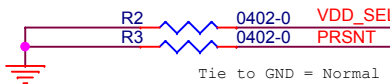
PEX8 GOLDEN FINGER
PCIEX8_GF_B

V12P0 V3P3_LAN





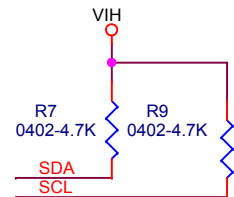
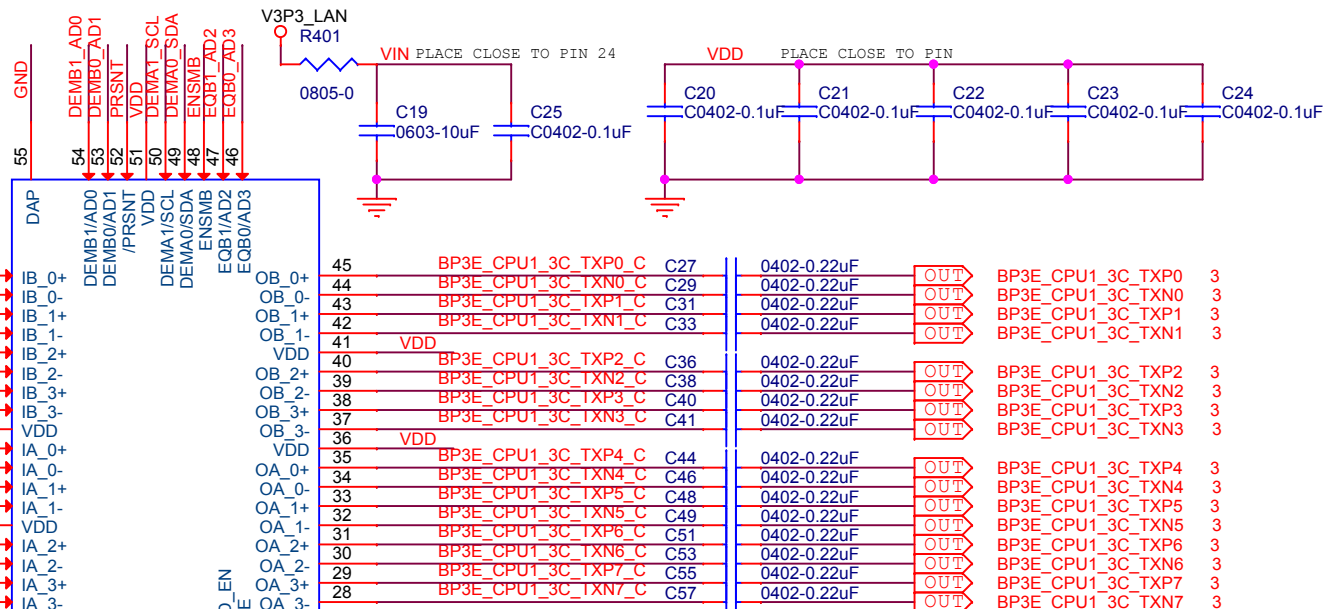
Controls the internal regulator
Tie to GND = 3.3 V mode



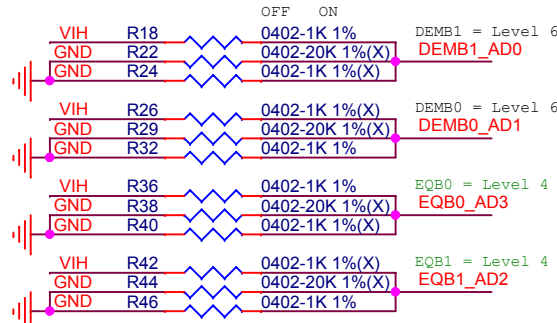
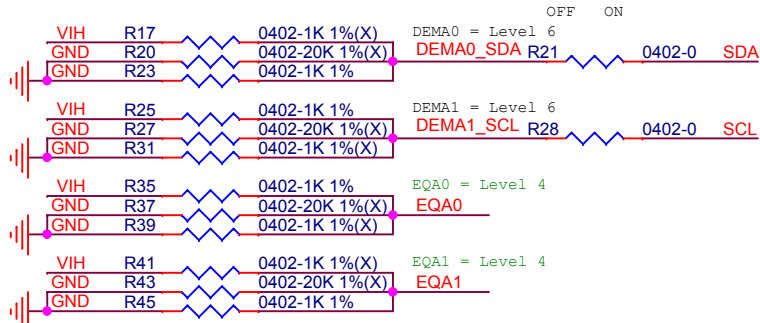
Tie to GND = Normal Operation



U1
DS80PCI800



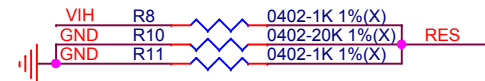
HEADER 1X3(2.54mm)



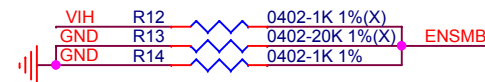
Signal Detect Status Threshold Level
SD_TH(PIN 26) = F (default)



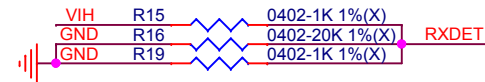
RESERVED1
This input must be left floating.



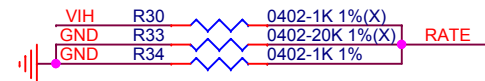
Pin Control Mode
ENSMB (PIN 48) = (ENSMB = 0)

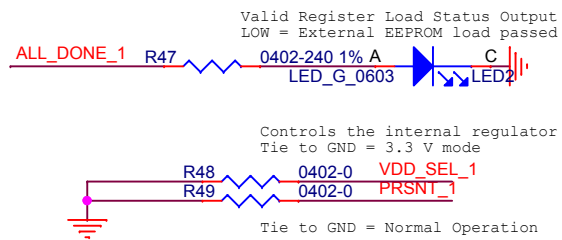


RX Detect Settings
RXDET(PIN 22) = F (default)

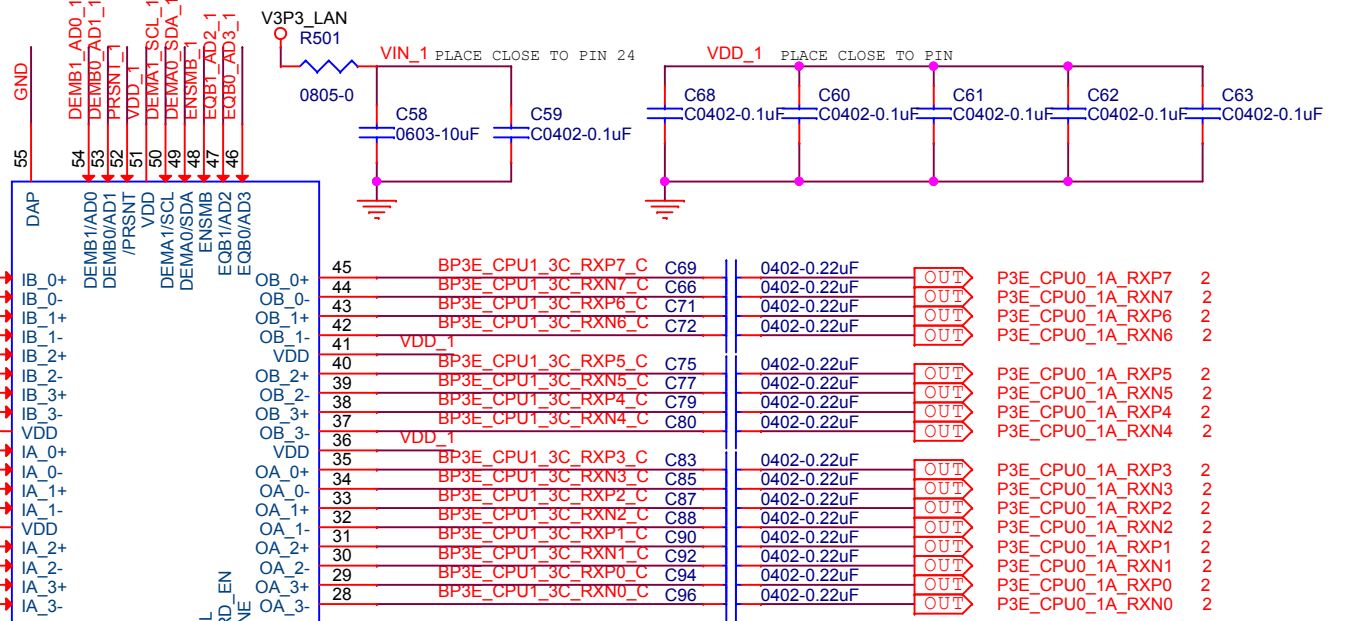


RESERVED2
Tie via external 1 kΩ to GND





U2
DS80PCI800



| | | | | | | | | | | | | | | | |
|---|-------------------|----|-----|-------------|---------------------|----|-------|-------|---------------------|---------------------|-------------|-------------|------------------|------------------|---|
| 3 | BP3E_CPU1_3C_RXP7 | IN | C64 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP7 | 1 | IB_0+ | 45 | BP3E_CPU1_3C_RXP7_C | C69 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP7 | 2 | |
| 3 | BP3E_CPU1_3C_RXN7 | IN | C65 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN7 | 2 | IB_0- | 44 | BP3E_CPU1_3C_RXN7_C | C66 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN7 | 2 | |
| 3 | BP3E_CPU1_3C_RXP6 | IN | C70 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP6 | 3 | IB_1+ | 43 | BP3E_CPU1_3C_RXP6_C | C71 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP6 | 2 | |
| 3 | BP3E_CPU1_3C_RXN6 | IN | C67 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN6 | 4 | IB_1- | 42 | BP3E_CPU1_3C_RXN6_C | C72 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN6 | 2 | |
| 3 | BP3E_CPU1_3C_RXP5 | IN | C73 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP5 | 5 | IB_2+ | VDD_1 | 40 | BP3E_CPU1_3C_RXP5_C | C75 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP5 | 2 |
| 3 | BP3E_CPU1_3C_RXN5 | IN | C74 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN5 | 6 | IB_2- | 39 | BP3E_CPU1_3C_RXN5_C | C77 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN5 | 2 | |
| 3 | BP3E_CPU1_3C_RXP4 | IN | C76 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP4 | 7 | IB_3+ | 38 | BP3E_CPU1_3C_RXP4_C | C79 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP4 | 2 | |
| 3 | BP3E_CPU1_3C_RXN4 | IN | C78 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN4 | 8 | IB_3- | 37 | BP3E_CPU1_3C_RXN4_C | C80 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN4 | 2 | |
| 3 | BP3E_CPU1_3C_RXP3 | IN | C81 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP3 | 10 | IA_0+ | VDD_1 | 36 | BP3E_CPU1_3C_RXP3_C | C83 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP3 | 2 |
| 3 | BP3E_CPU1_3C_RXN3 | IN | C82 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN3 | 11 | IA_0- | VDD_1 | 35 | BP3E_CPU1_3C_RXN3_C | C85 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN3 | 2 |
| 3 | BP3E_CPU1_3C_RXP2 | IN | C84 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP2 | 12 | IA_1+ | 34 | BP3E_CPU1_3C_RXP2_C | C87 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP2 | 2 | |
| 3 | BP3E_CPU1_3C_RXN2 | IN | C86 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN2 | 13 | IA_1- | 33 | BP3E_CPU1_3C_RXN2_C | C88 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN2 | 2 | |
| 3 | BP3E_CPU1_3C_RXP1 | IN | C89 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP1 | 15 | IA_2+ | 32 | BP3E_CPU1_3C_RXP1_C | C90 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP1 | 2 | |
| 3 | BP3E_CPU1_3C_RXN1 | IN | C91 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN1 | 16 | IA_2- | 31 | BP3E_CPU1_3C_RXN1_C | C92 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN1 | 2 | |
| 3 | BP3E_CPU1_3C_RXP0 | IN | C93 | 0402-0.22uF | C_BP3E_CPU1_3C_RXP0 | 17 | IA_3+ | 30 | BP3E_CPU1_3C_RXP0_C | C94 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXP0 | 2 | |
| 3 | BP3E_CPU1_3C_RXN0 | IN | C95 | 0402-0.22uF | C_BP3E_CPU1_3C_RXN0 | 18 | IA_3- | 29 | BP3E_CPU1_3C_RXN0_C | C96 | 0402-0.22uF | OUT | P3E_CPU0_1A_RXN0 | 2 | |

