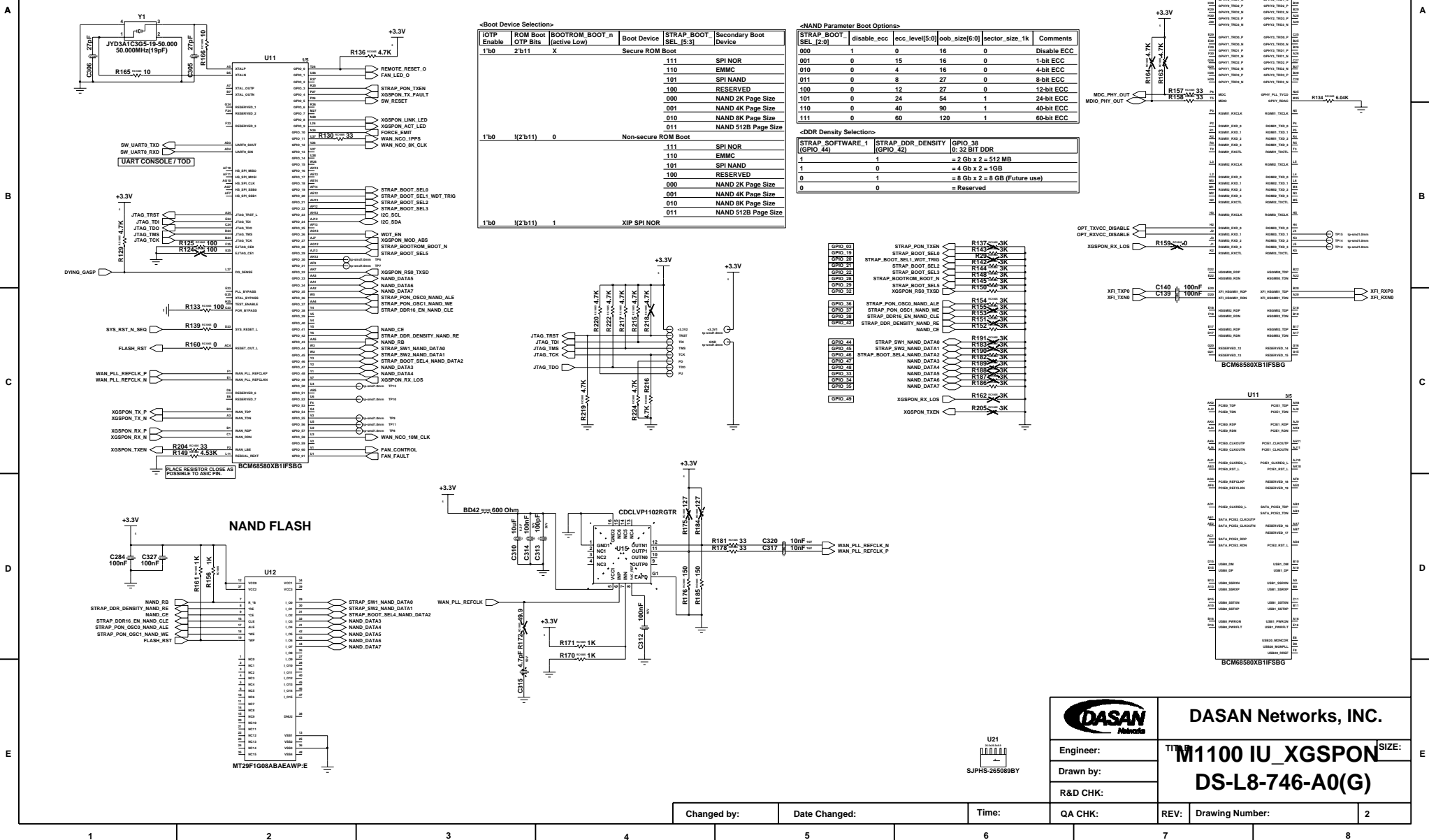




# BCM68580 GPIO / Ref.Clk / NAND Flash / JTAG



**<Boot Device Selection>**

OTFP Enable	ROM BOOT OTP Bls	BOOTFROM BOOT_n (active Low)	Boot Device	STRAP BOOT_SEL [5:3]	Secondary Boot Device
1'b0	2'b11	X	Secure ROM Boot		
			111	SPI NOR	
			110	EMMC	
			101	SPI NAND	
			100	RESERVED	
			000	NAND 2K Page Size	
			001	NAND 4K Page Size	
			010	NAND 8K Page Size	
			011	NAND 512B Page Size	
1'b0	(2'b11)	0	Non-secure ROM Boot		
			111	SPI NOR	
			110	EMMC	
			101	SPI NAND	
			100	RESERVED	
			000	NAND 2K Page Size	
			001	NAND 4K Page Size	
			010	NAND 8K Page Size	
			011	NAND 512B Page Size	
1'b0	(2'b11)	1	XIP SPI NOR		

**<NAND Parameter Boot Options>**

STRAP BOOT_SEL [2:0]	disable_ecc	ecc_level[5:0]	oob_size[6:0]	sector_size_1k	Comments
000	1	0	16	0	Disable ECC
001	0	15	16	0	1-bit ECC
010	0	4	16	0	4-bit ECC
011	0	8	27	0	8-bit ECC
100	0	12	0	0	12-bit ECC
101	0	24	54	1	24-bit ECC
110	0	40	90	1	40-bit ECC
111	0	60	120	1	60-bit ECC

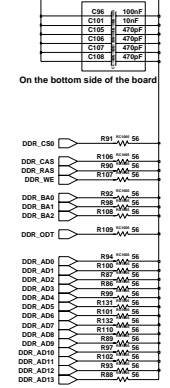
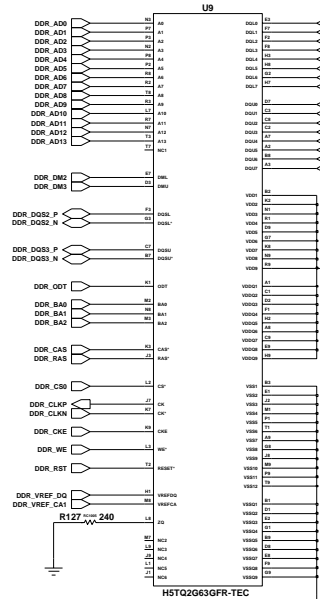
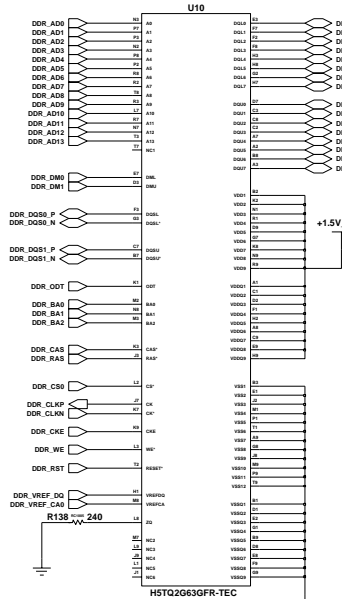
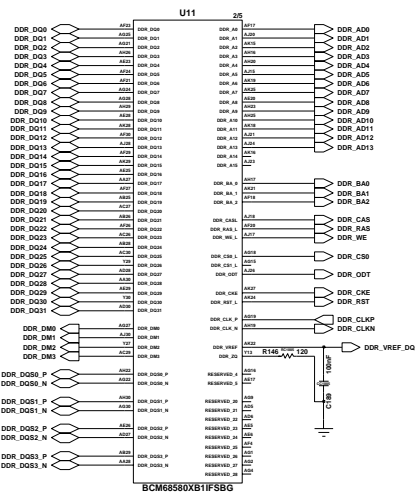
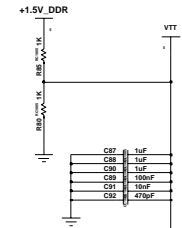
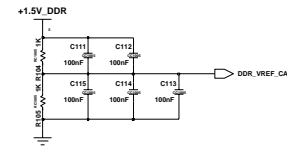
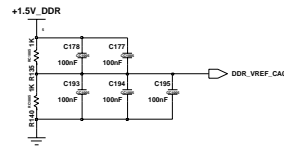
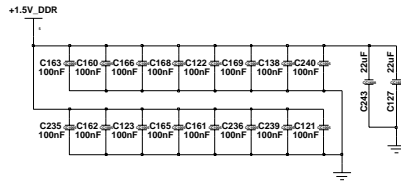
**<DDR Density Selection>**

STRAP SOFTWARE_1 (GPIO 44)	STRAP DDR DENSITY (GPIO 42)	GPIO 38 (0: 32 BIT DDR)	Comments
1	1		= 2 Gb x 2 = 512 MB
1	0		= 4 Gb x 2 = 1GB
0	1		= 8 Gb x 2 = 8 GB (Future use)
0	0		= Reserved

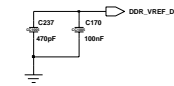
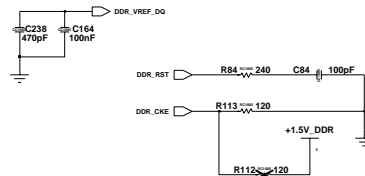
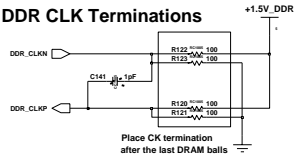
		<b>DASAN Networks, INC.</b> M1100 IU_XGSPON DS-L8-746-A0(G)		SIZE:
Engineer:	Drawn by:	QA CHK:	REV:	Drawing Number:
R&D CHK:				2

Changed by: Date Changed: Time: 2

# BCM68580 DDR3

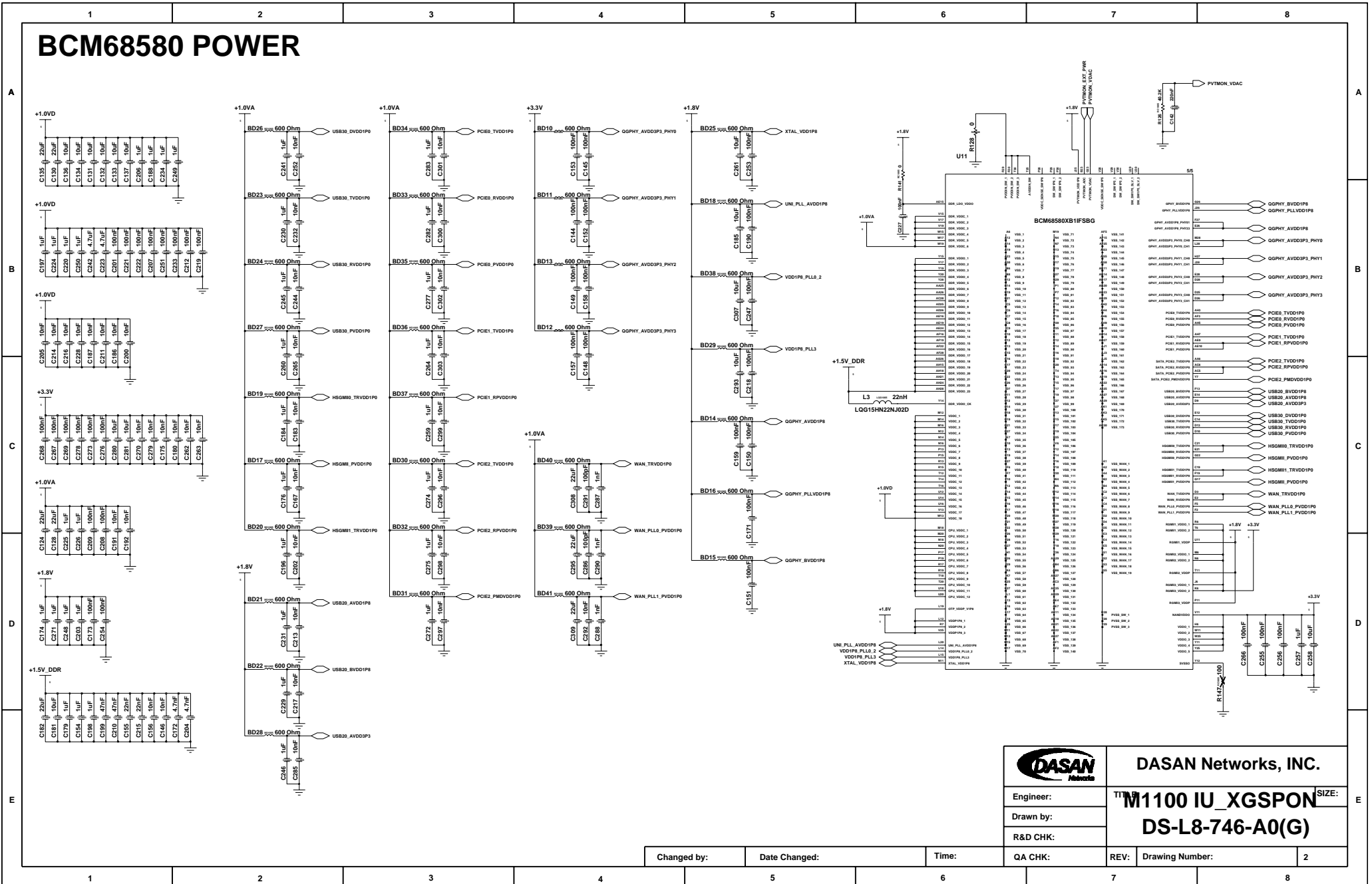


## DDR CLK Terminations



		<b>DASAN Networks, INC.</b>		
		Engineer:	TIV	SIZE:
Drawn by:		<b>M1100 IU_XGSPON</b>		
R&D CHK:		<b>DS-L8-746-A0(G)</b>		
Changed by:	Date Changed:	Time:	QA CHK:	REV: Drawing Number:
				2

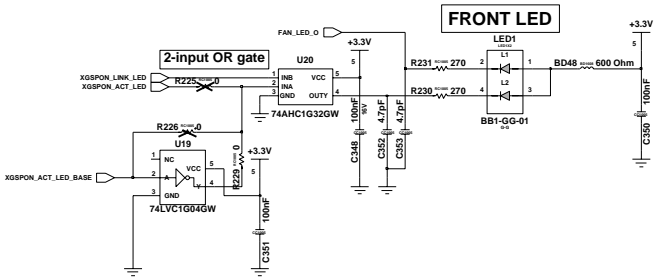
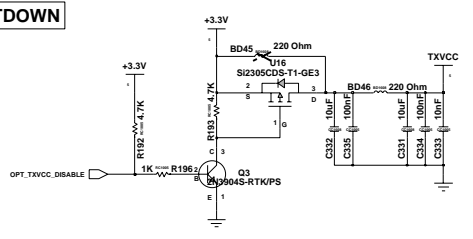
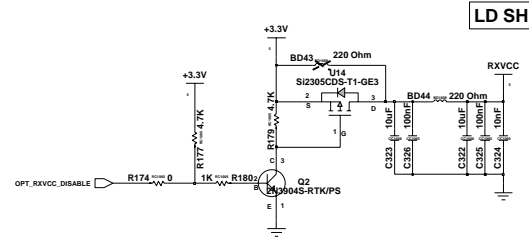
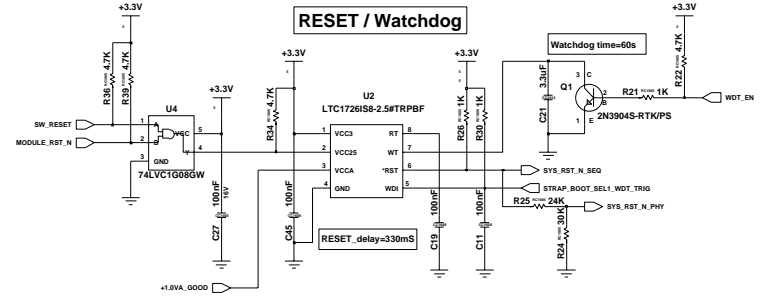
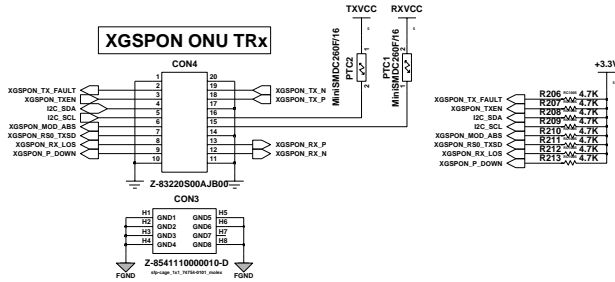
# BCM68580 POWER



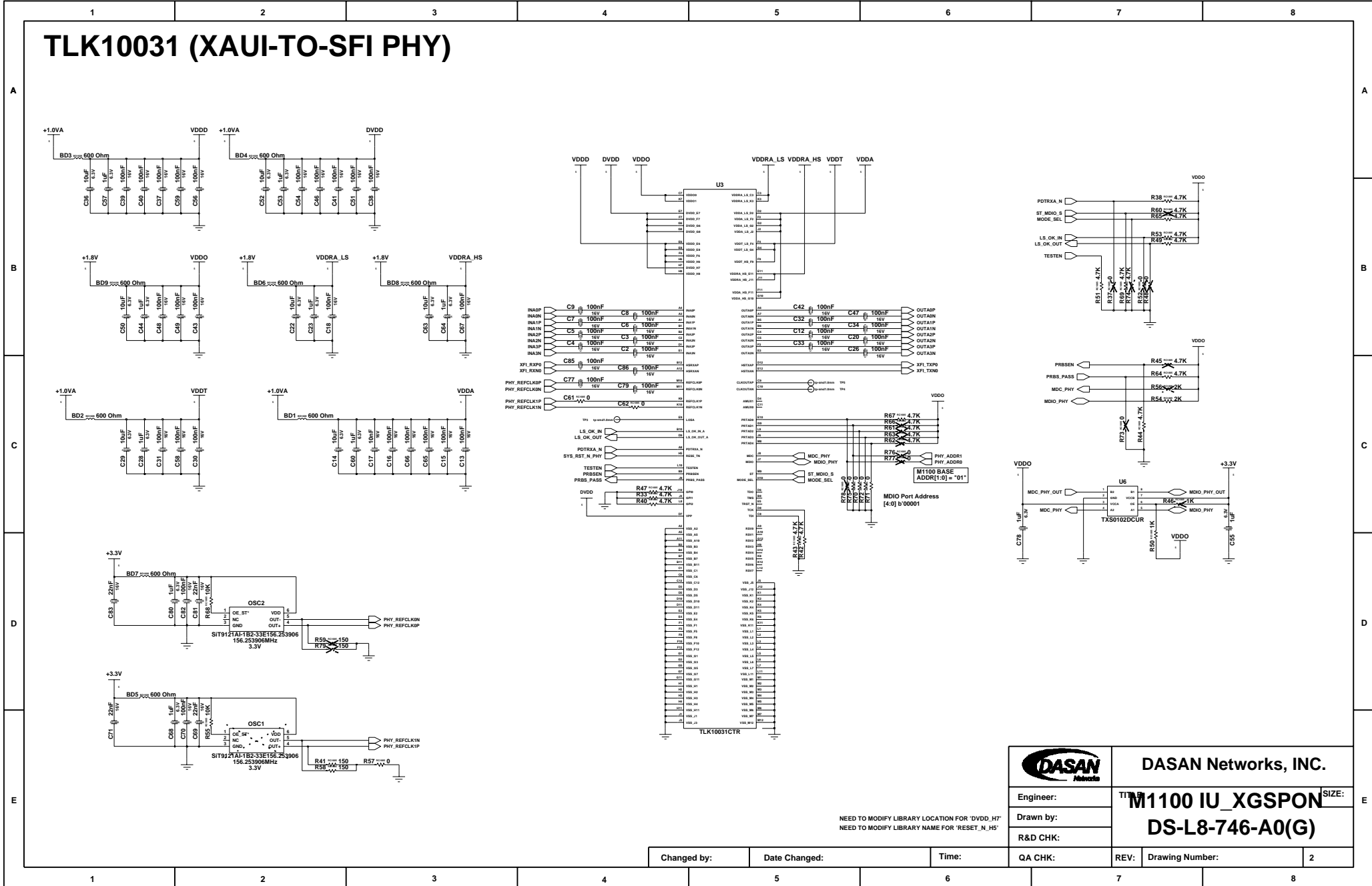
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Drawn by:		<b>M1100 IU_XGSPON</b>	
R&D CHK:		<b>DS-L8-746-A0(G)</b>	
QA CHK:	REV:	Drawing Number:	2

Changed by:	Date Changed:	Time:
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# XGSPON TRx / RESET / LED



# TLK10031 (XAUI-TO-SFI PHY)

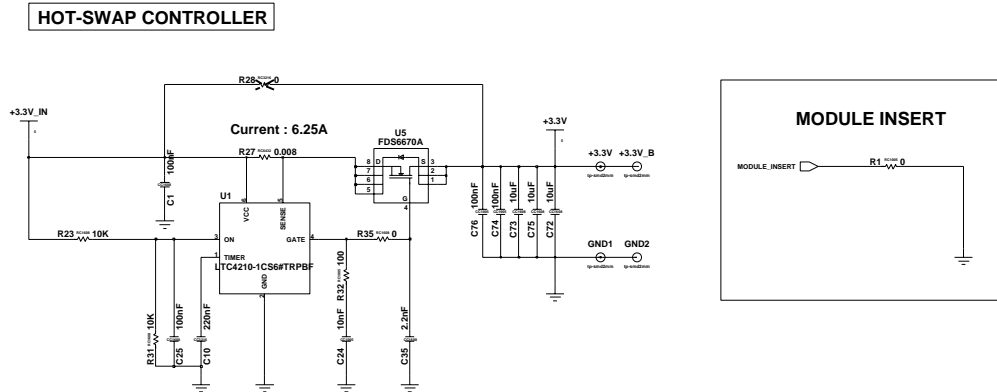
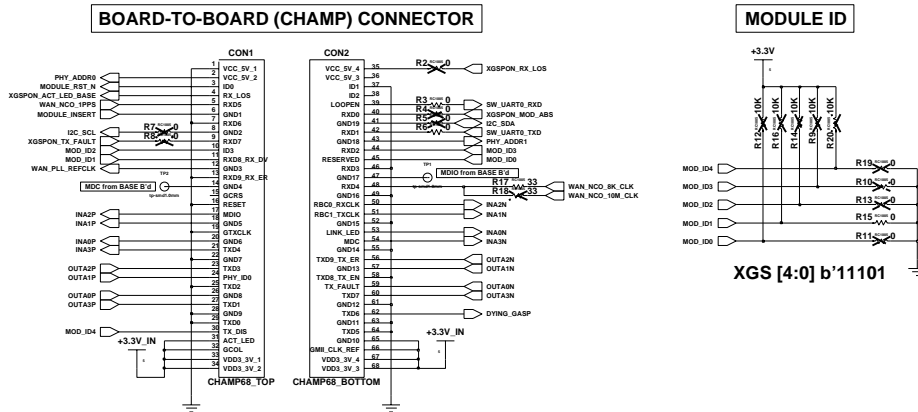


		<b>DASAN Networks, INC.</b>	
Drawn by:		<b>M1100 IU_XGSPON</b>	
R&D CHK:		<b>DS-L8-746-A0(G)</b>	
QA CHK:	REV:	Drawing Number:	2

NEED TO MODIFY LIBRARY LOCATION FOR 'DVDD\_H7'  
NEED TO MODIFY LIBRARY NAME FOR 'RESET\_N\_H5'

Changed by:	Date Changed:	Time:
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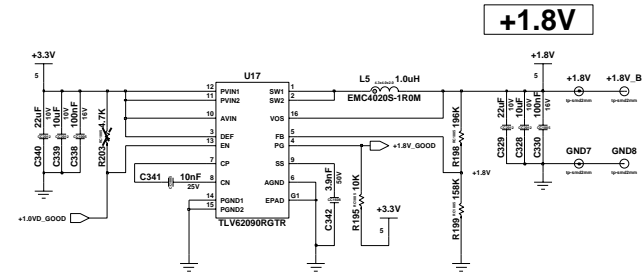
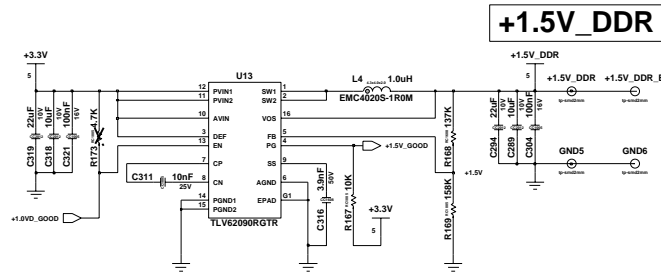
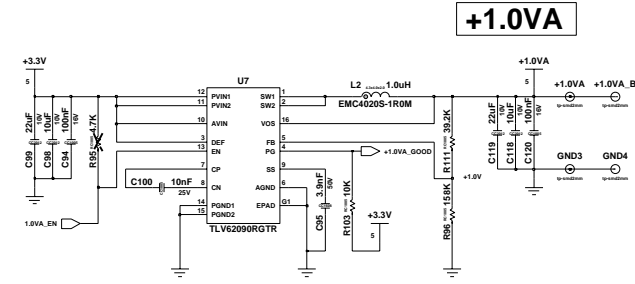
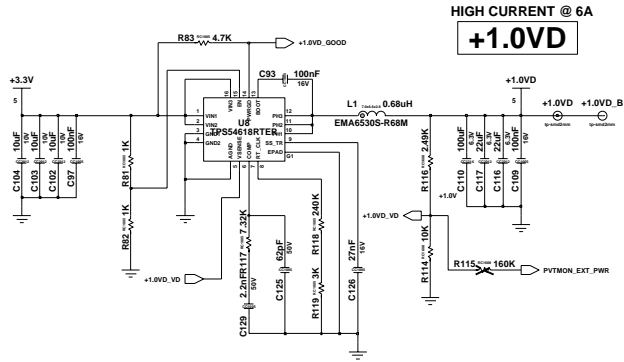
# BASE B'd INTERFACE / HOTSWAP CONTROLLER



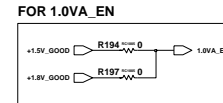
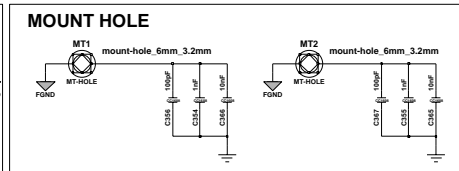
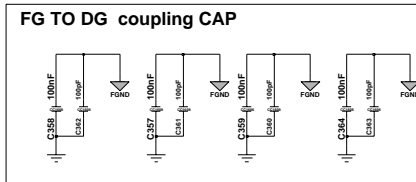
		DASAN Networks, INC.	
		Engineer:	TIT
Drawn by:		<b>M1100 IU_XGSPON</b>	
R&D CHK:			
QA CHK:		REV:	2
Date Changed:		Drawing Number:	2

Changed by:	Date Changed:	Time:	QA CHK:	REV:	Drawing Number:	2
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# DC/DC BLOCK



**POWER SEQUENCE: 3.3V & 1.0VD > 1.8V & 1.5V > 1.0VA**



		<b>DASAN Networks, INC.</b>	
Engineer:	TMM		SIZE:
Drawn by:	<b>M1100 IU_XGSPON</b>		<b>DS-L8-746-A0(G)</b>
R&D CHK:			
QA CHK:	REV:	Drawing Number:	2

Changed by:	Date Changed:	Time:
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	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
	1	2	3	4	5	6	7	8	



DASAN Networks, INC.

Engineer:

Drawn by:

R&D CHK:

QA CHK:

TITLE: M1100 IU\_XGSPON

SIZE:

DS-L8-746-A0(G)

Changed by:

Date Changed:

Time:

REV:

Drawing Number:

2