===Init seq===

0x1e.0x00 = 0x8610 /\* global reset \*/

0x07.0x00 = 0x2000 /\* disable auto nego \*/

0x1e.0x96 = 0x0000 /\* disable link training \*/

0x01.0xab = 0x0001 /\* enable 10G-KR FEC \*/

0x1e.0x0e = 0x0008 /\* channel data path reset \*/

0x1e.0x9000= 0x024d /\* magic register 0x9000 \*/

0x1e.0x8101= 0x0004 /\* enable default tx trigger\*/

0x1e.0x8100= 0x0004 /\* trigger loading default HS TX setting value \*/

0x1e.0x8100= 0x0000 /\* normal op for tx trigger \*/

0x1e.0x9001= 0x0200 /\* magic register 0x9001 \*/

0x1e.0x96 = 0x0002 /\* magic registe 0x96 \*/

0x1e.0x9005= 0x1c00 /\* magic register 0x9005 \*/

0x1e.0x3 = 0xe888 /\* force attenuator off, HS swing change from 1020 to 1340 mVdfpp \*/

0x1e.0x4 = 0x5252 /\* HS\_TWCRF : cursor reduction to 22%, default is 0%;

 \* HS serdes PEAK\_disable;

 \* HS clock data recovery alg -Sixteen vote threshold ->

 \* four vote threshold ;

 \* HS clock data recovert alg freq from 2x mode to 1x mode

 \*/

0x07.0x12 |= 0x8000 /\* using 'or' , bit15 = 1, auto neg FEC req \*/

0x1e.0x8100= 0x0001 /\* bit0 for TI use only \*/

0x07.0x0 = 0x3000 /\* enable auto nego \*/

0x07.0x0 |= 0x0200 /\* using 'or' , bit9 = 1, restart auto nego \*/

0x01.0x96 = 0x0003 /\* link training enable and restart \*/

===setup loopback on same port===

0x1E.0x18 |= 0x4000 /\* using 'or', bit14, same channel HS input \*/

0x1E.0xb |= 0x0008 /\* using 'or', bit3, deep remote loopback \*/

===1G-KX setting===

0x1E.0x1 = 0x0300

0x7.0x0 = 0x2000

0x1E.0xE = 0x0008