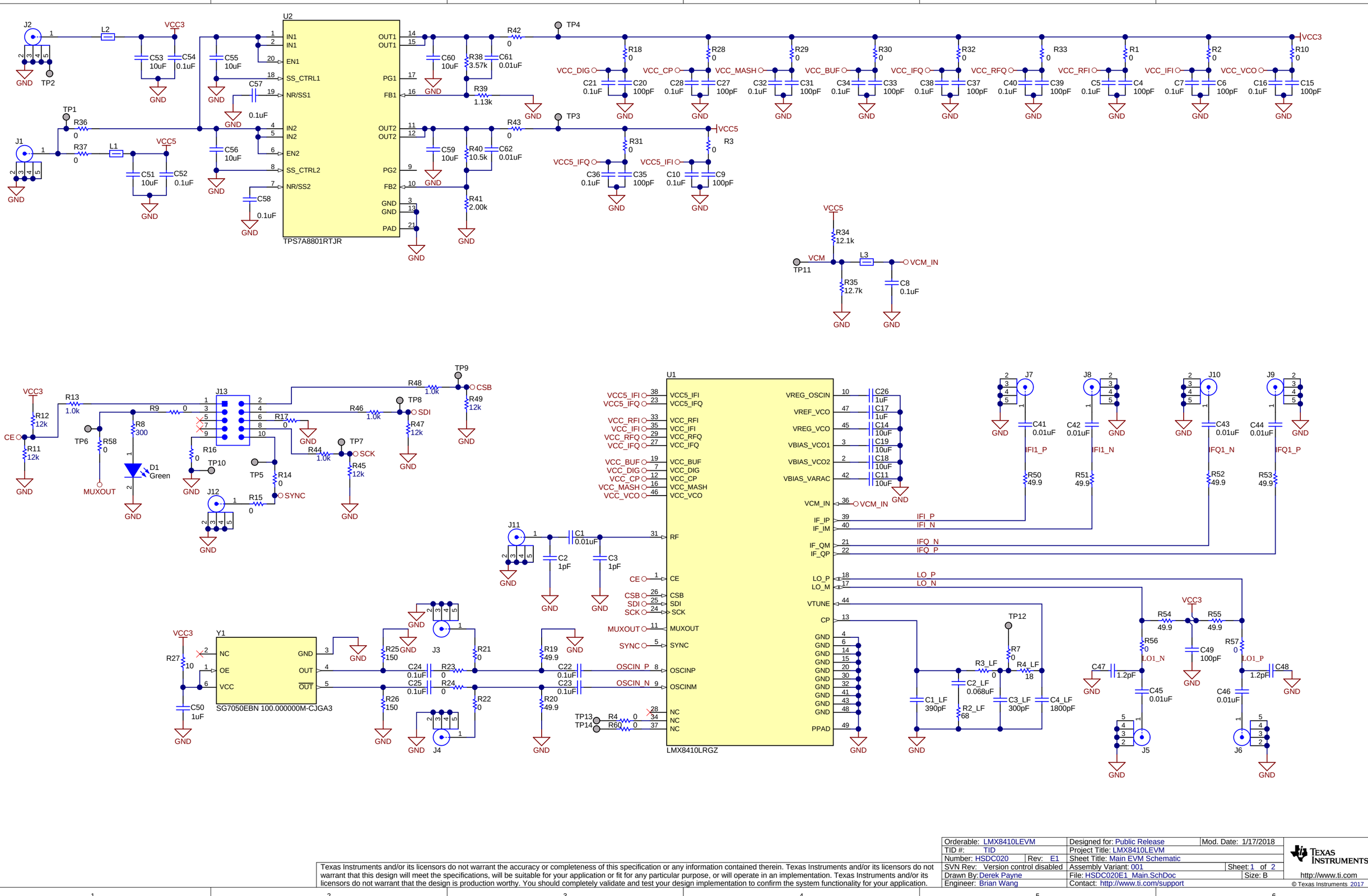

Orderable: LMX8410LEVM	Designed for: Public Release	Mod. Date: 1/17/2018	
TID #: TID	Project Title: LMX8410LEVM		
Number: HSDC020 Rev: E1	Sheet Title: Main EVM Schematic		
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2	
Drawn By: Derek Payne	File: HSDC020E1_Main.SchDoc	Size: B	
Engineer: Brian Wang	Contact: http://www.ti.com/support		

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!PCB

PCB Number: HSDC020
PCB Rev: E1

Printed Circuit Board

PCB LOGO
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PCB LOGO
ESD Susceptible

PCB LOGO
FCC disclaimer

PCB LOGO
WEEE logo

CE Mark

LBL1

PCB Label

Label

Variant/Label Table	
Variant	Label Text
001	LMX8410EVM

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: LMX8410LEVM	Designed for: Public Release	Mod. Date: 1/17/2018
TID #: TID	Project Title: LMX8410LEVM	
Number: HSDC020 Rev: E1	Sheet Title: Hardware and Assembly Notes	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2
Drawn By: Derek Payne	File: HSDC020E1_EVM_Hardware.SchDoc	Size: B
Engineer: Brian Wang	Contact: http://www.ti.com/support	

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