

**National Semiconductor**  
LM98618 Evaluation Board  
Rev. 1

National Semiconductor

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Notes:

FR4 1oz. 0.062" board.  
PCB to be plated hard body gold.  
0.010" via holes may be plated shut.  
Soldermask LPI both board sides.  
No soldermask between the pins of U4.  
Silkscreen white both board sides.

Please stack up PCB as such:

7.8 mils	Layer 1
	Layer 2
7.8 mils	Layer 3
	Layer 4

Layer 1 Component side.  
Layer 2 Ground Plane.  
Layer 3 Power Plane.  
Layer 4 Solder side.

