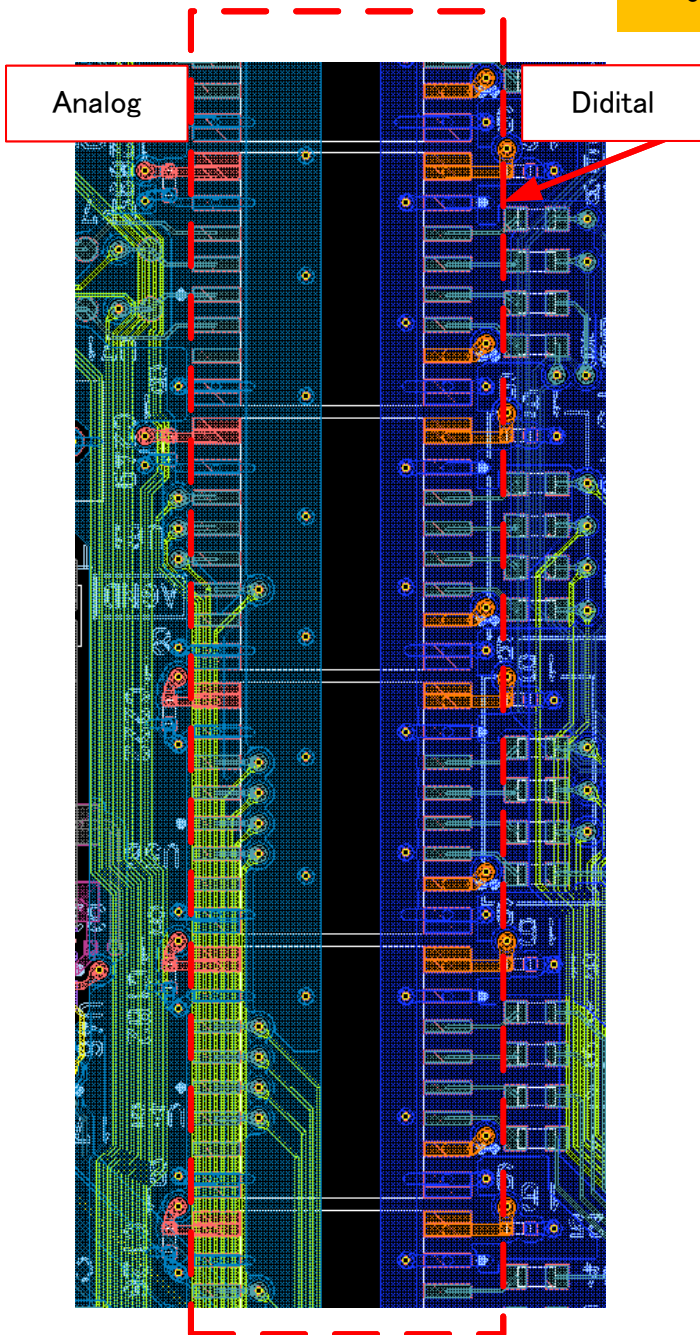
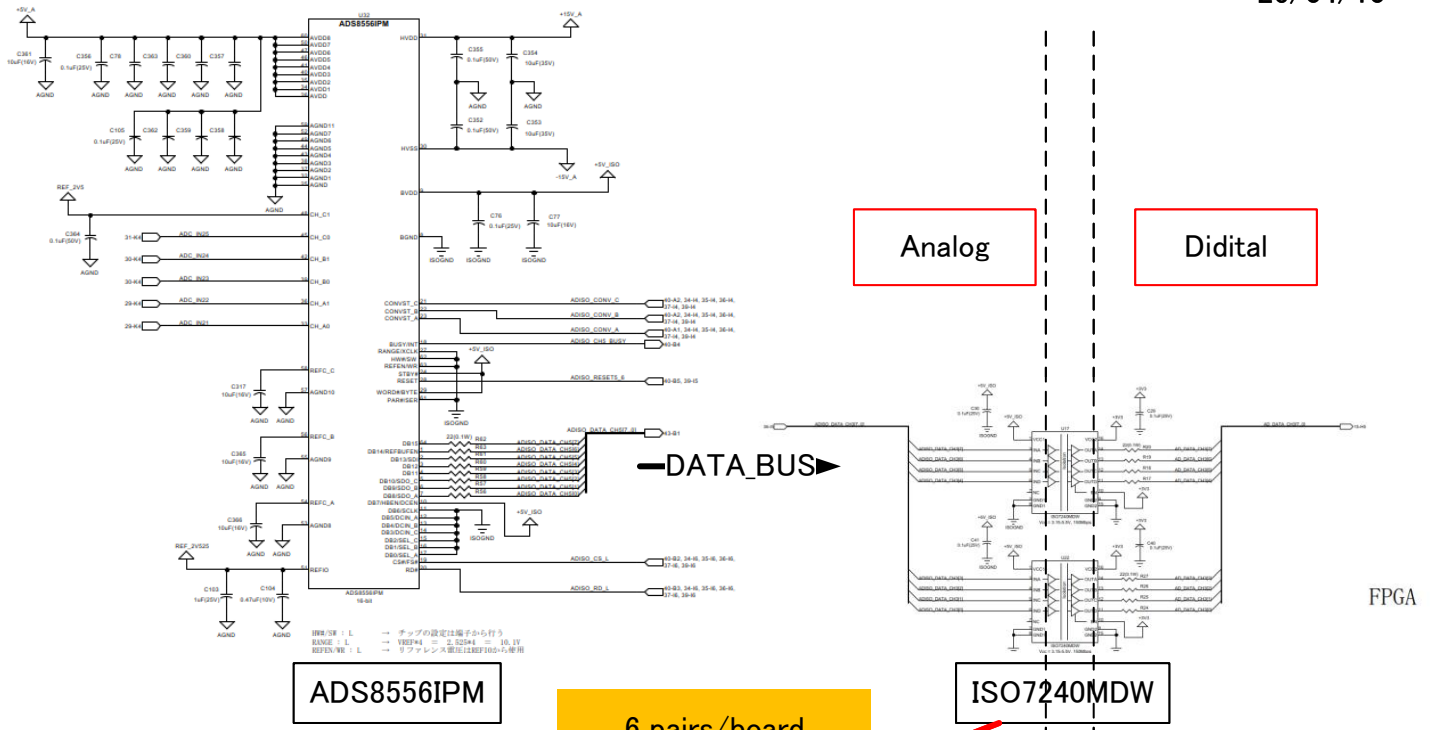
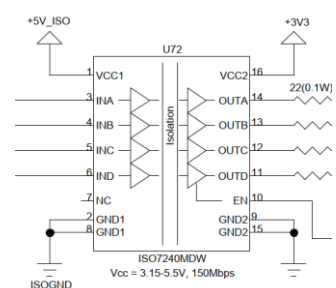
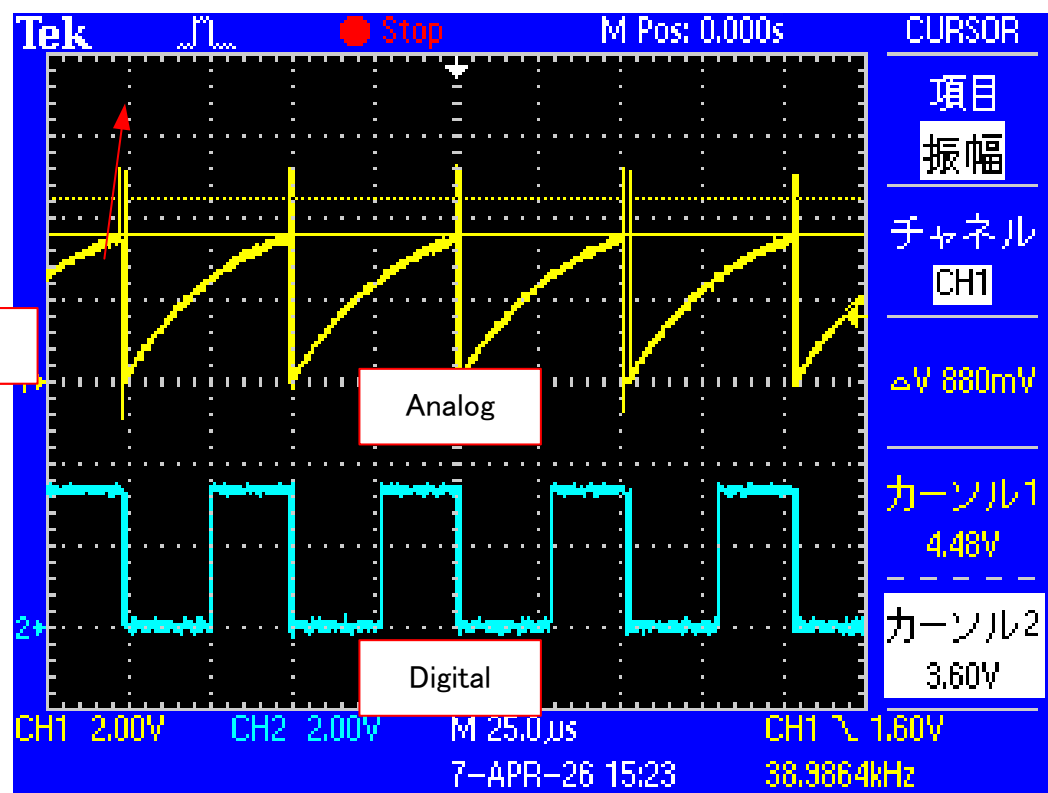




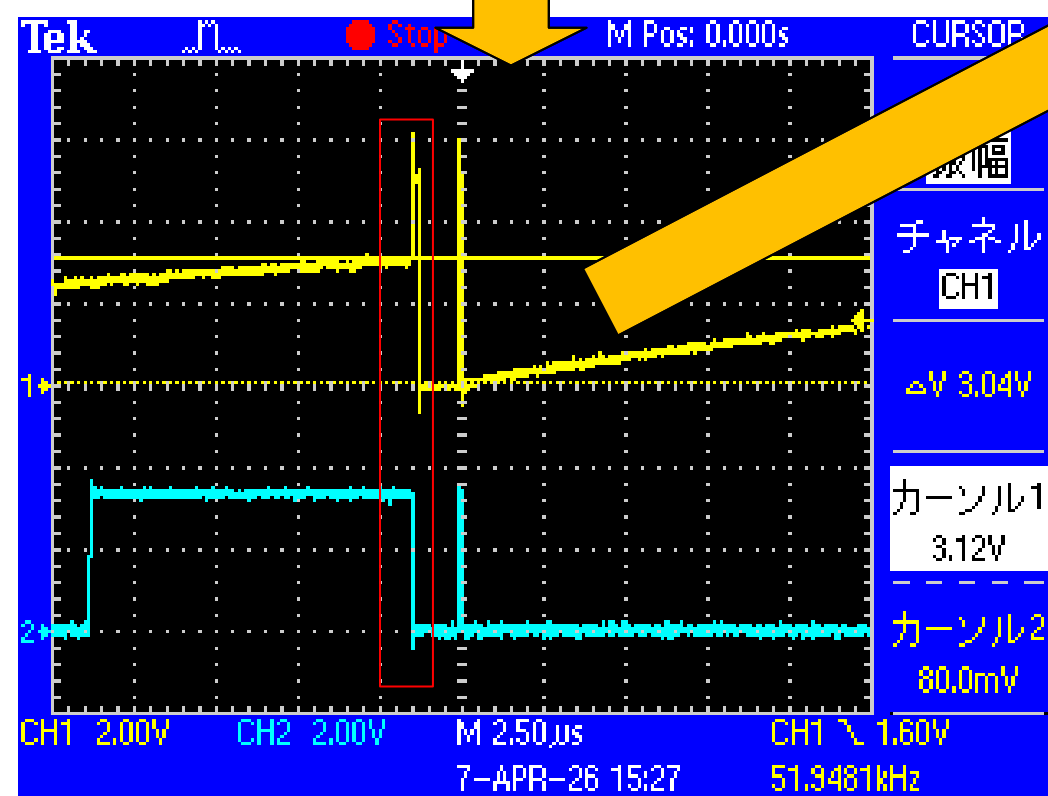


Analog

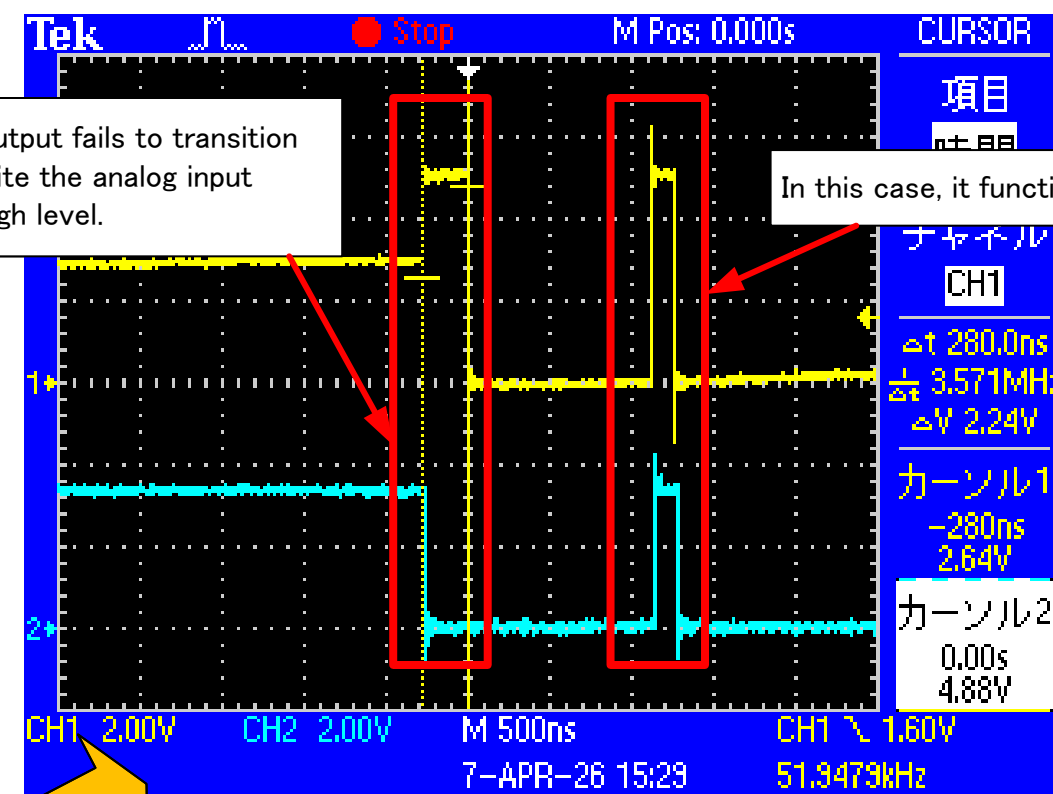
Digital



No issues occurred when the ADC started driving while the voltage on the analog side was around 3.6V.



However, an issue occurred when the ADC started driving while the analog-side voltage was around 3.1V.



The digital output fails to transition to High despite the analog input being at a High level.

In this case, it functions as expected.

