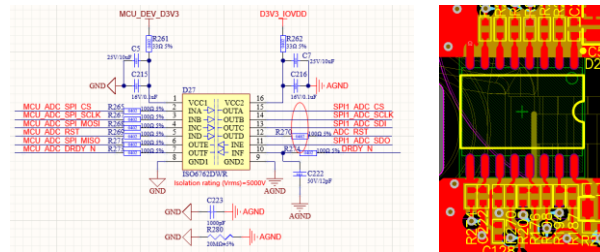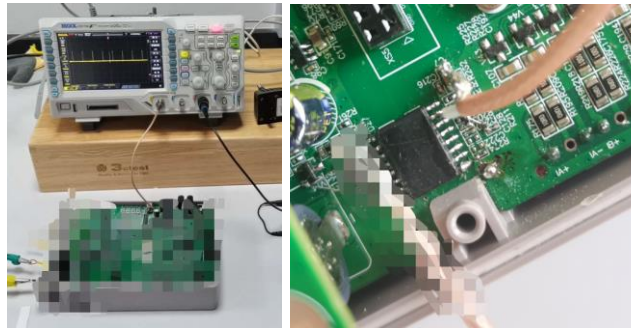# Input and output waveforms of ISO6762 and IS3762

Using a power-isolated oscilloscope, the waveform differences between the input and output signals of the digital isolator are tested under ESD HCP, and the test is performed using coaxial cables to act as probes to reduce the loop area.
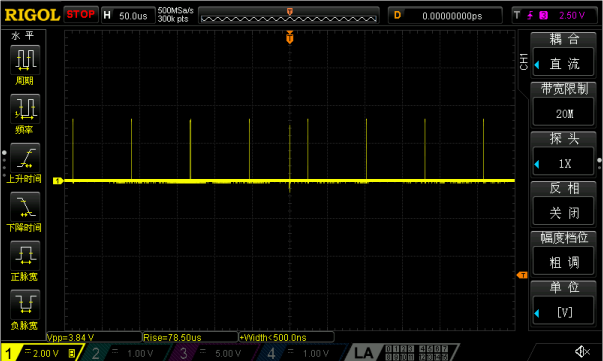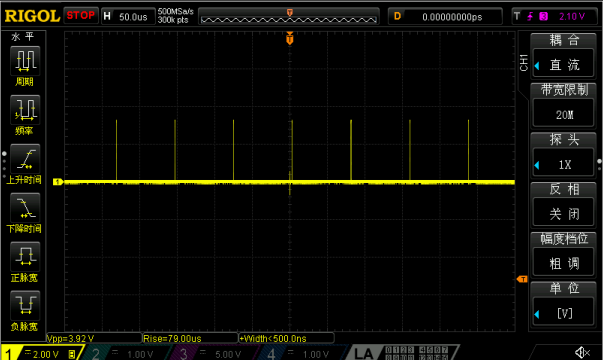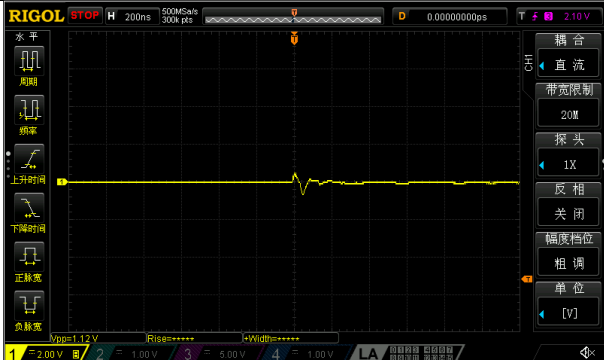
Isolator Circuit：

Probe connection： Test points are performed on channels 7-10 of the isolator. Removed the filter capacitor connected to the IO.

ISO6762

Test waveform: DRDY signal, all test points are taken from the isolator pins.

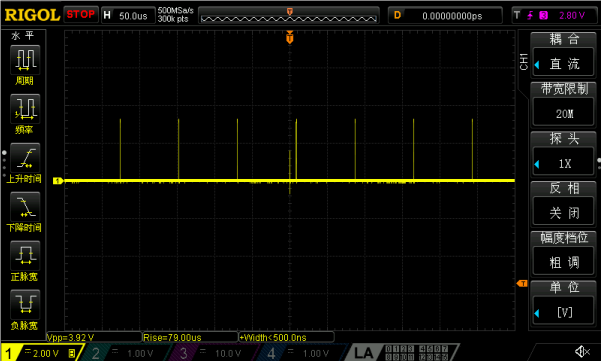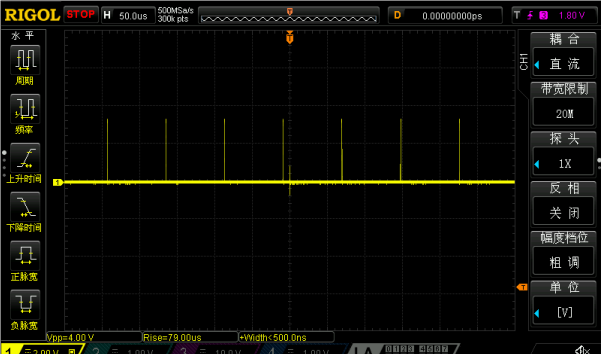| Test point	Wavefrom	Interference waveform amplification
CH1-MCU This is the floating GND end of the isolation ISO6762-7pin		
Test point	Wavefrom	Interference waveform amplification
CH1-ADC This is the non-isolated terminal connected to the power grid N ISO6762-10pin		

IS3762

Test waveform: DRDY signal, all test points are taken from the isolator pins.

Test point	Wavefrom	Interference waveform amplification
CH1-MCU This is the floating GND end of the isolation ISO6762-7pin		
Test point	Wavefrom	Interference waveform amplification
CH1-ADC This is the non-isolated terminal connected to the power grid N ISO6762-10pin		

This is the DRDY waveform when the two isolators are communicating normally

	ISO6762	IS3762
CH1-ADC This is the non-isolated terminal connected to the power grid N IC-10pin		
CH1-MCU This is the floating GND end of the isolation IC-7pin		