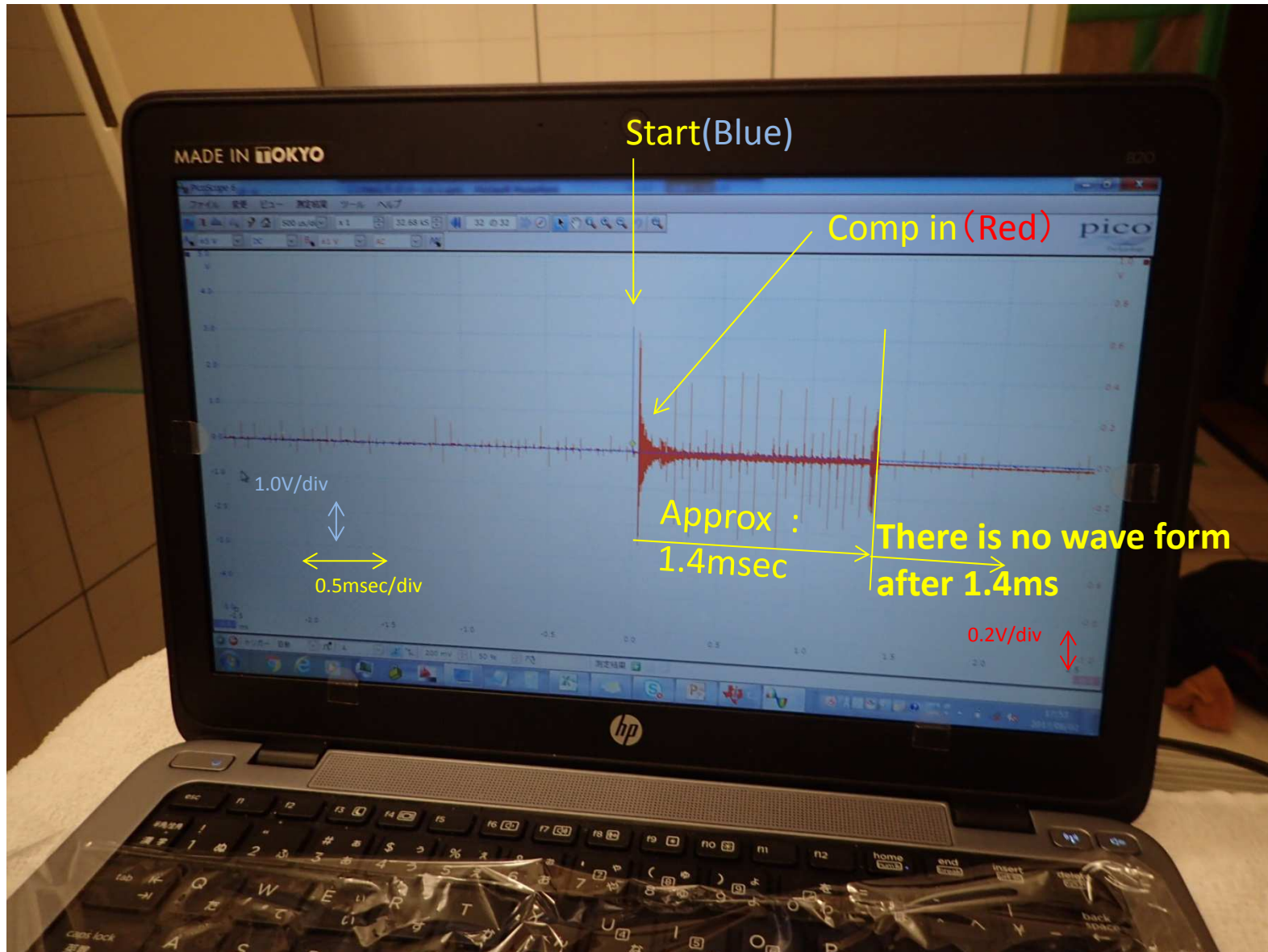




SETUP

TDC1000

TDC7200

TOF_ONE_SHOT

GRAPH

TANK LEVEL

TEMPERATURE

DEBUG

FW REVISION

2.01

GUI REVISION

1.2.7.1

CONFIG0 (0x00)

TX_FREQ_DIV

Divide by 8

NUM_TX

4 Pulses

R

CONFIG1 (0x01)

NUM_AVG

1 Cycle

NUM_RX

1 STOP

R

CONFIG2 (0x02)

VCOM_SEL

Internal

MEAS_MODE

TOF Measurement

DAMPING

Disabled

CH_SWP

Disabled

EXT_CHSEL

Disabled

CH_SEL

CH1 (TX1)

TOF_MEAS_MODE

Mode 0

R

CONFIG3 (0x03)

TEMP_MODE

REF_RTD1_RTD2

TEMP_RTD_SEL

Pt1000

TEMP_CLK_DIV

Divide by 8

BLANKING

Disabled

ECHO_QUAL_THLD

-1500mV

R

CONFIG4 (0x04)

RECEIVE_MODE

Multi Echo

TRIG_EDGE_POL

Rising

TX_PH_SHIFT_POS

31

R

CONTINUOUS TRIGGER

READ ALL

TOF-1 (0x05)

PGA_GAIN

21dB

PGA_CTRL

Active

LNA_CTRL

Active

LNA_FB

Capacitive

TIMING_REG[9:8]

3

R

TOF-0 (0x06)

TIMING_REG[7:0]

4 255

us

1.99E+3

R

Blank Period = (TIMING_REG - 30) x 8 x T0

ERROR FLAGS (0x07)

ERR_SIG_WEAK

0

ERR_NO_SIG

0

ERR_SIG_HIGH

0

R

TIMEOUT (0x08)

FORCE_SHORT_TOF

Disabled

SHRT_TOF_BLNK_PRD

128 x T0

us

32

ECHO_TIMEOUT

Enabled

TOF_TIMEOUT_CTRL

1024 x T0

us

256

R

CLOCK RATE (0x09)

CLOCKIN_DIV

Divide by 2

AUTOZERO_PERIOD

512 x T0

us

128

R

LOAD CONFIG

SAVE CONFIG

Note: T0 = CLOCKIN_DIV / CLK_FREQ

FW REVISION: 2.01						GUI REVISION: 1.2.7.1							
SETUP													
TDC1000		TDC7200		TOF_ONE_SHOT		GRAPH		TANK LEVEL		TEMPERATURE		DEBUG	
SERIAL PORT COM33 CONNECT DISCONNECT				TRIGGER UPDATE FREQ 200ms R FORWARD TO REVERSE FLOW DELAY 1% of Trig Update Freq R				TDC_CLK_SOURCE_SEL ON BOARD OSC (8MHz) CLK FREQ (1-16MHz) 8.000000 CPU_CLK_EN CPU_CLK FREQ 1.2632MHz R					
TDC1000-HV Driver EN1 EN1 Period (us) 30				MSP430_SPI_CLK_CONFIG Clk Phase Low, Polarity Low MSP430_SPI_CLK_DIVIDER (>=1) x 0000 R W				TDC1000-HV Boost Power Enable EN EN Period (us) Always ON					
TDC1000-HV Driver EN2 EN2 Period (us) 30				ENABLE_MSP430TIMER_TDC									