

1 2 3 4 5 6

H1 NY PMS 440 0025 PH
H2 NY PMS 440 0025 PH
H3 NY PMS 440 0025 PH
H4 NY PMS 440 0025 PH

H5 1902C
H6 1902C
H7 1902C
H8 1902C

LOGO

PCB LOGO
Texas Instruments

PCB LOGO
Pb-Free Symbol

LOGO

1 2 3 4 5 6

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Designed for:		Mod. Date: 25/04/1399	
Project Title:			
Number:	Rev:	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: [No Variations]	Sheet: 1 of 5
Drawn By:		File: Hardware_ULTRASONIC_TDC.SchDoc	Size: B
Engineer:		Contact:	



A

A

B

B

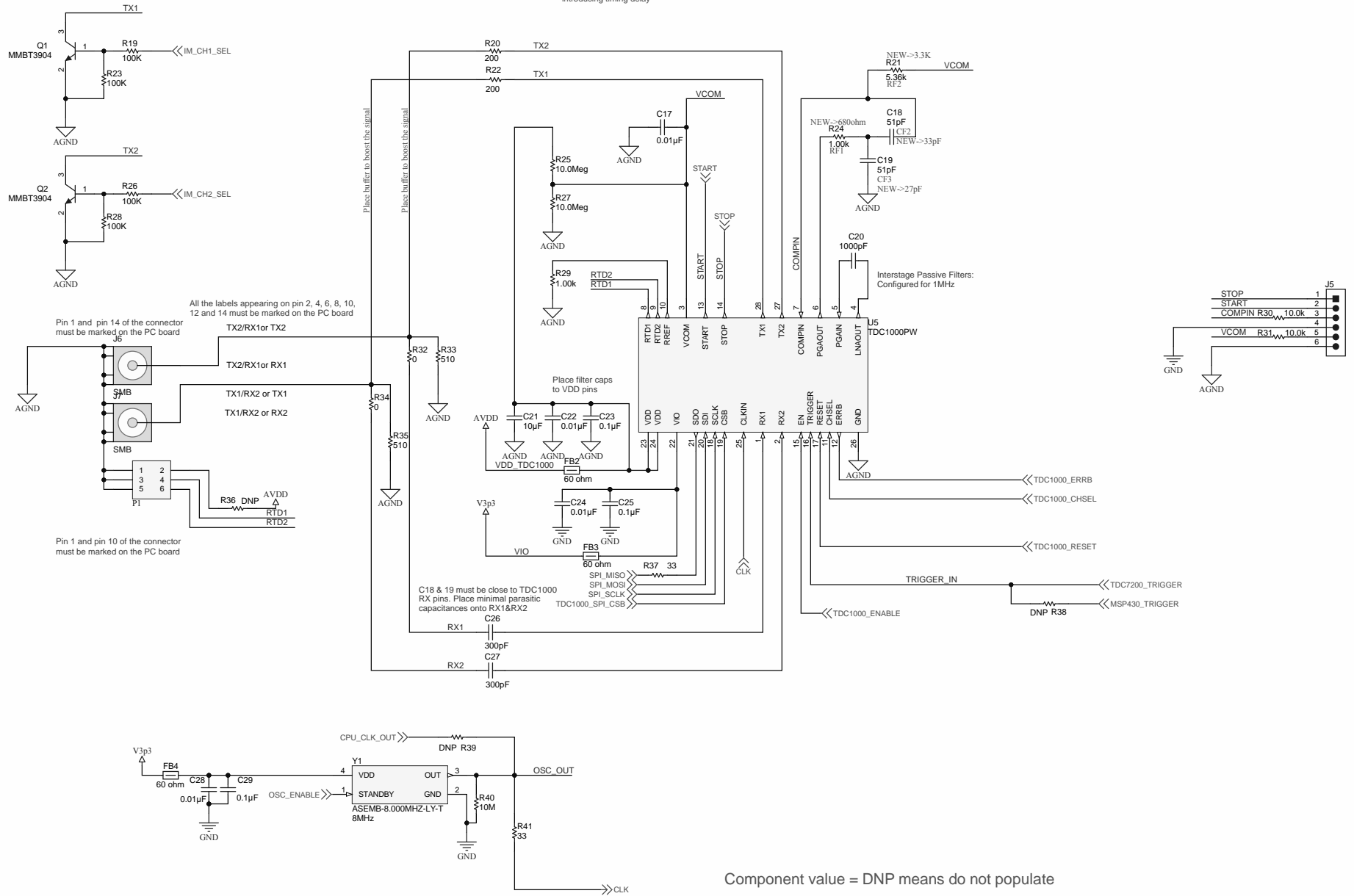
C

C

D

D

Directly connected STOP and START traces from TDC71000 to TDC7200 must be completely symmetrical and as short as possible to avoid introducing timing delay



Pin 1 and pin 14 of the connector must be marked on the PC board

All the labels appearing on pin 2, 4, 6, 8, 10, 12 and 14 must be marked on the PC board

Pin 1 and pin 10 of the connector must be marked on the PC board

Place buffer to boost the signal

Place filter caps to VDD pins

C18 & 19 must be close to TDC1000 RX pins. Place minimal parasitic capacitances onto RX1&RX2

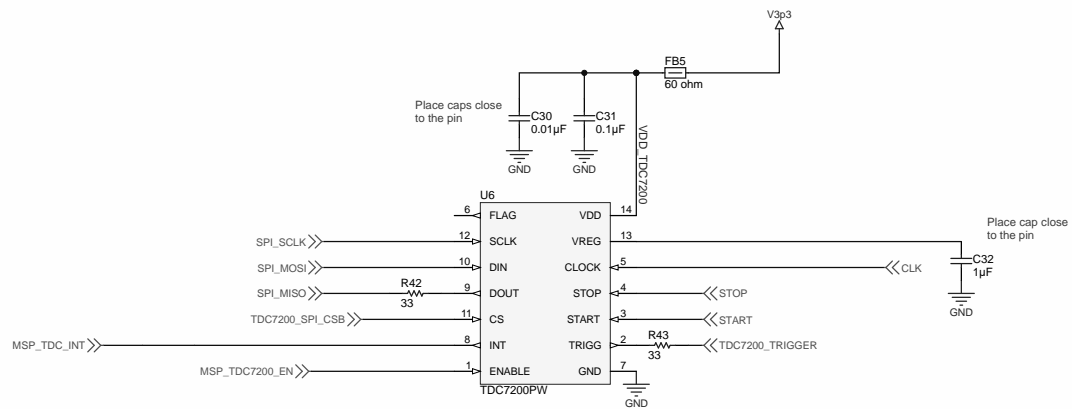
Interstage Passive Filters: Configured for 1MHz

Component value = DNP means do not populate

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Number:	Rev:	Designed for:	Mod. Date:
SVN Rev: Not in version control		Project Title: TDC 1000	08/04/1400
Drawn By:	Engineer:	Sheet Title: Assembly Variant: [No Variations]	Sheet: 2 of 5
		File: TDC_flowmeter_TDC1000.SchDoc	Size: B
		Contact:	

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Component value = DNP means do not populate

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Designed for:		Mod. Date: 25/04/1399	
Project Title:		Sheet Title: TDC7200	
Number:	Rev:	Assembly Variant: [No Variations]	Sheet: 3 of 5
SVN Rev: Not in version control		File: TDC_flowmeter_TDC7200.SchDoc	Size: B
Drawn By:		http://www.ti.com	
Engineer:		Contact:	



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