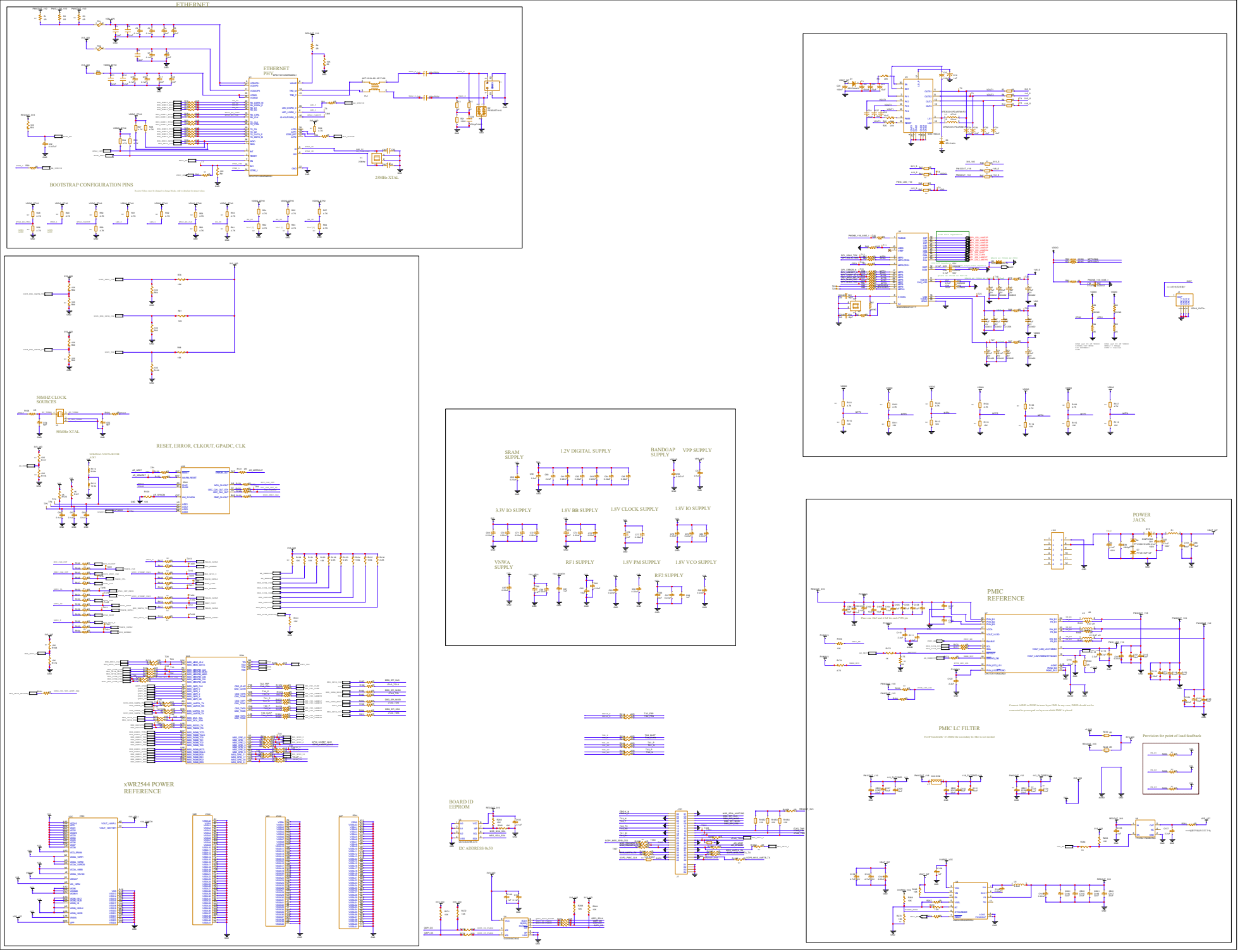
ETHERNET
ETHERNET PHY
BOOTSTRAP CONFIGURATION PINS
25MHz XTAL
50MHZ CLOCK SOURCES
50MHz XTAL
RESET, ERROR, CLKOUT, GPADC, CLK
xWR2544 POWER REFERENCE
SRAM SUPPLY
1.2V DIGITAL SUPPLY
BANDGAP SUPPLY
VPP SUPPLY
3.3V IO SUPPLY
1.8V BB SUPPLY
1.8V CLOCK SUPPLY
1.8V IO SUPPLY
VNWA SUPPLY
RF1 SUPPLY
1.8V PM SUPPLY
1.8V VCO SUPPLY
RF2 SUPPLY
BOARD ID EEPROM
I2C ADDRESS 0x50
POWER JACK
PMIC REFERENCE
PMIC LC FILTER
Provision for point of load feedback