Delivery & Release Note

| Project Name | RF DATA CAPTURE CARD |
| --- | --- |
| Project ID | MFY18026 |
| Project Manager Responsible for the Delivery / Release | ALAGAPPAN R |
| Date of Release | 21st JULY 2022 |
| Against Payment Mile Stone | NO |
|  | |
| ***Ship To:*** | Kind Attn: Mr. Dham Vivek  Texas Instruments Incorporated |
| ***Bill To:***  ***(if applicable)*** | NA |
| Mode of Release | TI BOX – <https://app.box.com/folder/42398137061> |

# Purpose of this Delivery or Release

Purpose of this release is to deliver DCA1000EVM FPGA binaries to Texas Instruments

# What are Delivered / Released (all items shall be in Configuration Management system as Configuration Items (CI))

DCA1000EVM\_FPGA\_v2.9\_21JUL2022 folder contains the following binaries

* DCA EVM FPGA binary release for Record and Playback features

DCA1000\_FPGA\_RECORD\_V2.9\_21JUL2022.bit

DCA1000\_FPGA\_PLAYBACK\_V2.9\_21JUL2022.bit

* Release Notes

MS\_TI\_RF\_DATA\_CAPTURE\_CARD\_FPGA\_DRN\_V2.9\_21\_07\_2022.doc

* Test Procedure

MS\_TI\_RF\_DATA\_CAPTURE\_CARD\_TEST\_PROCEDURE.pdf

Version: 1.4

Configuration Item version: 2.11

SVN version: 846

# Important features about the Delivery / Release

* This release is only for FPGA binaries to fix the Ethernet connectivity issue.
* There is no change in GUI and DLL Versions(earlier GUI version 2.7 and DLL Version 2.7.1 can be used as is)

# Bug Fixes implemented in this Delivery / Release

* Ethernet connectivity issue:

The cause of Ethernet disconnecting is due to the absence of DDR3 controller and TSMAC IP Licenses in FPGA, which induced a hardware timer that limited the operation of hardware to a limited period of time (approximately four hours). FPGA IP Core licenses are required to enable the unrestricted use of both TSMAC and DDR3 Controller IPs. The Latest FPGA binaries are generated with the required licenses.

# Known Issues/Caveats in this release

* NA

# Installation Guidelines

- Refer MS\_TI\_RF\_DATA\_CAPTURE\_CARD\_TEST\_PROCEDURE.pdf Section 3 for FPGA tool installation

- FPGA Programmer tool link got updated as recommended by Lattice

# Target Requirements (if any) required for testing at Customer site

* Refer MS\_TI\_RF\_DATA\_CAPTURE\_CARD\_TEST\_PROCEDURE.pdf

# Important note about the Delivery / Release

-NA