

IIC Write/Read access

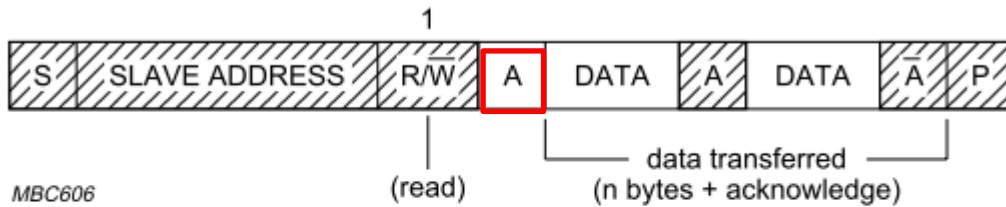
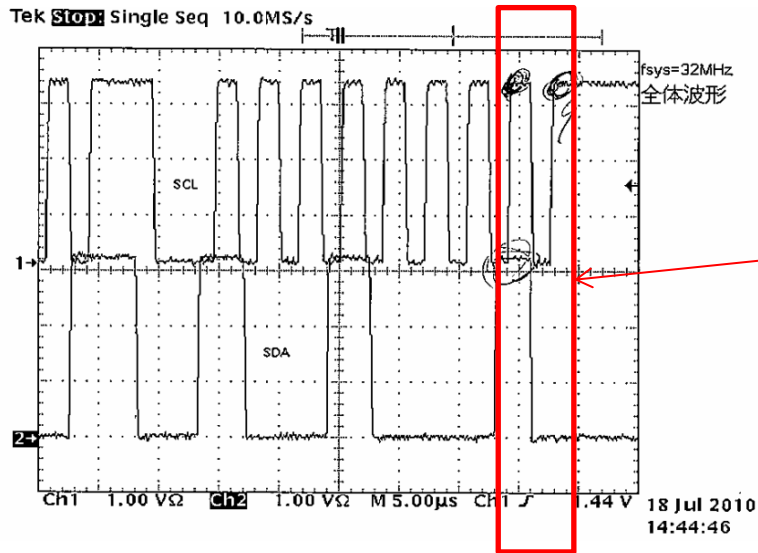


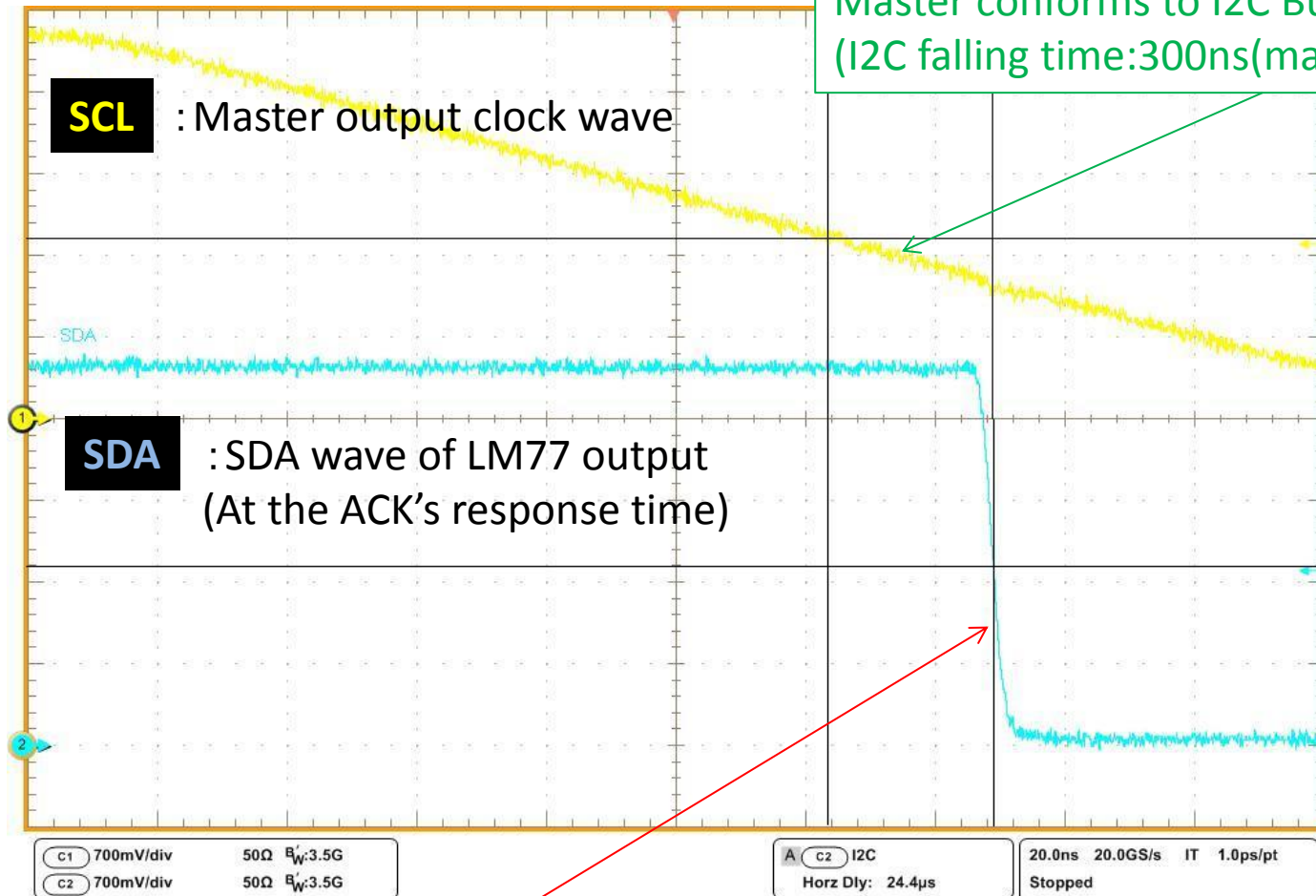
Fig.12 A master reads a slave immediately after the first byte.



The arbitration lost happened often in communications between LM77 and microcomputer.

As a cause, I understood that the falling of SDA signal is early at the ACK's response time.

I2C Write/Read access



【The cause】

In the standard of I2C Bus, it is NG to change SDA during the SCL falling(max:300ns) .
At the ACK's response time, I am understanding that becomes the I2C Bus error
(an arbitration) because a slave device works in this way.
(The temperature sensor of the slave infringes I2C Bus standard like an above figure.)

【Consultation】

At the ACK's response time in EPROM which is same slave device, it is popular to output ACK/transmission data
after delaying (about 300ns) inside(after SCL falling).