

# Hall-Effect-Based Current Sensor with Common-Mode Field Rejection and Overcurrent protection

Datasheet (EN)

#### **Product Overview**

NSM2117 is an integrated path current sensor with a very low on-resistance of  $0.85m\Omega$ , reducing heat loss on the chip.

NOVOSENSE innovative isolation technology and signal conditioning design can meet high isolation levels while sensing the current flowing through the internal Busbar. A differential Hall pair is used internally, so it has a strong immunity to external stray magnetic fields.

NSM2117 senses the magnetic field generated by the Busbar current flowing under the chip to indirectly detect the current. Compared with the current sampling method of the Shunt+ isolated op-amp, NSM2117 eliminates the need for the primary side power supply and has a simple and convenient layout. At the same time, it has extremely high isolation withstand voltage and Lifetime stability.

In high-side current monitoring applications, NSM2117 can reach a working voltage of 1550Vpk, and it can withstand 10kV surge voltage and 13kA surge current without adding any protection devices.

Due to NSM2117 internal accurate temperature compensation algorithm and factory accuracy calibration, this current sensor can maintain good accuracy in the full temperature working range, and the customer does not need to do secondary programming or calibration.

NSM2117 Provides overcurrent protect function with VOC setting.

Support 5V /3.3V power supply (different version)

#### **Key Features**

- High bandwidth and ultrafast response time
- 1MHz bandwidth with -3dB

- 300ns response time
- · High-precision current measurement
- Differential Hall sets can immune stray field
- High isolation level that meets UL standards
- Working Voltage for Basic Isolation (VWVBI): 1550Vpk / 1097Vrms
- Withstand isolation voltage (VISO): 5000Vrms
- Maximum surge isolation withstand voltage (VIOSM): 10kV
- Maximum surge current (Isurge): 13kA
- CMTI > 100V/ns
- CTI (I)
- Creepage distance/Clearance distance: 8mm
- NOVOSENSE innovative 'Spin Current' technology makes offset temperature drift very small
- Fixed output mode with overcurrent fault protection
- Working temperature: -40°C ~ 150°C
- Primary internal resistance: 0.85mΩ
- Wide body SOIC16 package
- UL62368/EN62368 safety certification
- ROHS
- AEC-Q100

#### **Applications**

- Solar system
- Industrial power supply
- Motor control
- OBC/DCDC/PTC Heater
- Charging pile

#### **Device Information**

Part Number	Package	Body Size				
NSM2117	WB SOIC16	10.30mm × 7.50mm				

### **Functional Block Diagrams**

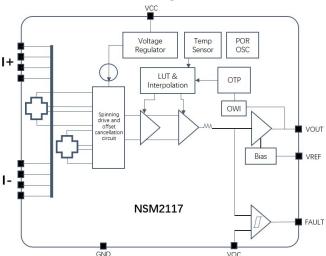


Figure 1. NSM2117 Block Diagram

### **NSM2117**

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### 1. Pin Configuration and Functions

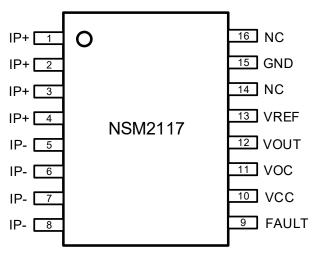


Figure 1.1 NSM2117 Package

Table 1.1 NSM2117 Pin Configuration and Description

NSM2117PIN NO.	SYMBOL	FUNCTION
1-4	IP+	Current flows into the chip, positive direction
5-8	IP-	Current flows out of the chip, negative direction
9	FAULT	Overcurrent Fault, Active low, Open Drain Output
10	VCC	Power supply
11	VOC	Set the output overcurrent protection point
12	VOUT	Output voltage
13	VREF	Reference voltage output
14	NC	Not connection
15	GND	Ground
16	NC	Not connection

### 2. Absolute Maximum Ratings

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
VCC	vcc	-0.3		6.5	V	25℃
Vout		-0.3		VCC+0.3	V	25℃
Others Pin		-0.3		VCC+0.3	V	25℃
Storage temperature	TStorage	-40		150	$^{\circ}$	
Ambient temperature	Toperation	-40		150	$\mathbb{C}$	
Junction temperature	Tj	-40		165	$\mathbb{C}$	
	VHBM		±8		kV	
ESD	VCDM		±2		kV	
	Latch-up		±500		mA	

### 3. Isolation Characteristics

Parameters	Symbol	Rating	Unit	Comments
Surge Voltage	Vsurge	10	kV	Based on IEC61000-4-5 1.2us/50us waveform
Surge Current	Isurge	13	kA	Based on IEC61000-4-5 8us/20us waveform
Dielectric Strength Test Voltage	VISO	5000	Vrms	60s isolation voltage parameters, according to UL62368-1, 6kV/ 1S insulation performance will be tested before delivery, and partial discharge is verified to be less than 5pC
	MANIPI	1097	Vrms	Maximum approved working
Working Voltage for Basic Isolation	VWVBI	1550	Vdc	voltage for basic isolation according to UL60950-1 and UL62368-1
Common-mode transient immunity	СМТІ	>100	V/ns	The criterion for judging the failure is that the output peak is greater than 100mV and the duration is longer than 1us
Creepage	Creepage	8	mm	Minimum Creepage
Clearance	Clearance	8	mm	Minimum Clearance
Comparative Tracking Index	СТІ	>=600		СТІІ

### 4. Specifications

4.1. Common Characteristics (TA= -40°C to 150°C, VCC = 5V or 3.3V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
	.,	3	3.3	3.6	٧	3.3V version
Supply voltage	Vcc	4.5	5	5.5	V	5V version
Supply current	Icc		25		mA	No load,Vcc=5V
Primary conductor resistance	R₽		0.85		mΩ	TA = 25°C
Power-on time	Тро		1		ms	Recommend customer to read output after 1ms power-on time, before 1ms internal OTP is loading, TA = 25 °C
Output capacitance load <sup>[1][2]</sup>	CL			10	nF	
Output resistance load <sup>[1][2]</sup>	R∟	10			kΩ	
Output short current	I <sub>short</sub>		±30		mA	Short to VCC and short to GND, $TA = 25^{\circ}\!\mathrm{C}$
Rail to Rail output voltage <sup>[1][2]</sup>	Vs	0.1		VCC-0.1	V	TA = 25℃, CL=220pF, RL=10K to VCC or GND
Common mode field rejection [1][2]	CMFR		>40		dB	
Rise time [1][2]	Tr		1		us	TA = 25°C, CL=220pF, VCC=5V (Except NSM2117-65B5F/66B3F/40B5F)
Propagation delay [1][2]	$T_{\sf pd}$		300		ns	TA = 25°C, CL=220pF, VCC=5V (Except NSM2117-65B5F/66B3F/40B5F)
Response time [1][2]	T <sub>response</sub>		300		ns	TA = 25°C, CL=220pF, VCC=5V (Except NSM2117-65B5F/66B3F/40B5F)
Bandwidth <sup>[1][2]</sup>	BW		1		MHz	-3dB bandwidth, TA = 25°C, CL=220pF, VCC=5V (Except NSM2117-65B5F/66B3F/40B5F)
Noise density [1][2]	ND		260		uArms/ √Hz	TA = 25 °C , CL=220pF, VCC= 5V
Non-linearity	E <sub>NL</sub>		±0.2		%	
			2.5		V	FB Version,Vcc=5V
Poforonco voltago	Vref		1.65		V	FB Version,Vcc=3.3V
Reference voltage	viei		0.5		V	FU Version,Vcc=5V
			0.33		V	FU Version,Vcc=3.3V
Fault pull-up Resistance	$R_{pu}$	4.7		100	kΩ	

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
Fault Hysteresis	l <sub>hys</sub>		5		%IPR	TA = 25°C, CL=220pF, Ift threshold=100%IPR
Fault Response time	T <sub>fr</sub>		700		ns	The time from Overcurrent happened to Fault pin active low, 4.7 kΩ pull-up Resistance
Fault Error			±5		%IPR	TA = 25°C, CL=220pF, Ift threshold=100%IPR

<sup>[1]:</sup> Guaranteed by design.

<sup>[2]:</sup> Guaranteed by Bench Validation

<sup>[3]:</sup> The increase or decrease of data in 4.X will not send a PCN to the customer if the evaluation does not affect the customer's use.

# 4.2. NSM2117-40B5FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	lpr	-40		40	А	
Sensitivity	Sens		50		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
C 11   12   1   12   13   13   13   13	F	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , lpr=0A
D. C [2]	VRE	-8		8	mV	TA = 25 ℃~150 ℃ , lpr=0A, Vref-2.5V
Reference error <sup>[2]</sup>		-15		15	mV	TA = -40 ℃~25 ℃, Ipr=0A, Vref-2.5V
Table 1. 1. 1	First	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

In the fourth chapter, the increase or decrease of the material number and the tightening of the parameter range, Novosense reserves the right not to send PCN to the customer, unless the expansion of the parameter range affects the customer's use and product performance.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.3. NSM2117-50B5FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
Current sensing range	lpr	-50		50	А	
Sensitivity	Sens		40		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
Canadain da a anna ([1][2]	F	-1.5		1.5	%	TA = 25°C ~150°C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C ~25°C
		-5		5	mV	TA = 25℃~150℃, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40°C~25°C, lpr=0A
D. (	VRE	-8		8	mV	TA = 25 °C ~150 °C , Ipr=0A ,
Reference error <sup>[2]</sup>		-15		15	mV	TA = -40 ℃~25 ℃, lpr=0A, Vref-2.5V
T. (1.)	Fire	-1.5		1.5	%	TA = 25°C ~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C ~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.4. NSM2117-66B3FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 3.3V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	lpr	-66		66	А	
Sensitivity	Sens		20		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		1.65		V	lpr=0A
C : 4: . : 4 [1][2]	F	-1.5		1.5	%	TA = 25 °C ~150 °C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C ,
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , lpr=0A
D. (	VRE	-8		8	mV	TA = 25 °C ~150 °C , lpr=0A ,
Reference error <sup>[2]</sup>		-15		15	mV	TA = -40 °C ~25 °C , Ipr=0A ,
Table 10 10 10 11	Fire	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.5. NSM2117-65B5FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
Current sensing range	lpr	-65		65	Α	
Sensitivity	Sens		30.8		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
Consistivity owner[1][2]	Faana	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C , Ipr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , lpr=0A
D. (	VRE	-8		8	mV	TA = 25 °C ~150 °C , lpr=0A ,
Reference error <sup>[2]</sup>		-15		15	mV	TA = -40 ℃~25 ℃, Ipr=0A, Vref-2.5V
Total autout ama (1)	Ftatal	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.6. NSM2117-20B3FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 3.3V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
Current sensing range	lpr	-20		20	А	
Sensitivity	Sens		66		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		1.65		V	lpr=0A
Consistivity owner[1][2]	Faces	-1.5		1.5	%	TA = 25 ℃~150 ℃
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C ,
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , lpr=0A
D. (	VRE	-8		8	mV	TA = 25 ℃~150 ℃ , Ipr=0A , Vref-1.65V
Reference error <sup>[2]</sup>		-15		15	mV	TA = -40 ℃ ~25 ℃ , Ipr=0A ,
Table 1. 1	First	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.7. NSM2117-65B5F-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

specified)						
Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	Ipr	-65		65	А	
Sensitivity	Sens		30.8		mV/A	prmin< pr< prmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
	F	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25℃~150℃, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , Ipr=0A
D. ( [2]	VDE	-8		8	mV	TA = 25 ℃ ~150 ℃ , lpr=0A , Vref-2.5V
Reference error <sup>[2]</sup>	VRE	-15		15	mV	TA = -40 °C ~25 °C , Ipr=0A ,
T [1]	F I	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = $25^{\circ}$ C
BandWidth	BW		400		kHz	-3dB bandwidth, TA = 25℃, CL=220pF, VCC=5V
Rise time <sup>[1][2]</sup>	Tr		1.2		us	TA = 25℃, CL=220pF, VCC=5V
Propagation delay <sup>[1][2]</sup>	T <sub>pd</sub>		1.2		us	TA = 25℃, CL=220pF, VCC=5V
Response time [1][2]	Tresponse		1.5		us	TA = 25℃, CL=220pF, VCC=5V
r						

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

 $<sup>\</sup>hbox{\it [2]: Min/Max value is the mean value +/-3 sigma; according to the statistical law, 99.73\% of the data is in this range Inside.}$ 

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference. , Is the worst case.

# 4.8. NSM2117-20B5FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Мах	Unit	Comments
Current sensing range	lpr	-20		20	А	
Sensitivity	Sens		100		mV/A	lprmin <lpr<lprmax< td=""></lpr<lprmax<>
Zero current output voltage	VQVO		2.5		V	lpr=0A
Consistivity owner[1][2]	Faces	-1.5		1.5	%	TA = 25 ℃~150 ℃
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C ,
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C ~25 °C , lpr=0A
D. (	VDF	-8		8	mV	TA = 25 °C ~150 °C , lpr=0A, Vref-2.5V
Reference error <sup>[2]</sup>	VRE	-15		15	mV	TA = -40 ℃~25 ℃, Ipr=0A, Vref-2.5V
Table 1. 1	First	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 20A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.9. NSM2117-100B5FH-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	lpr	-100		100	А	
Sensitivity	Sens		20		mV/A	Iprmin<  pr<  prmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
C 11   12   1   12   13   13   13   13	F	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error <sup>[1][2]</sup>	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C ~150°C, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C~25 °C , lpr=0A
D. C [2]	VDF	-8		8	mV	TA = 25 ℃~150 ℃ , lpr=0A, Vref-2.5V
Reference error <sup>[2]</sup>	VRE	-15		15	mV	TA = -40 ℃~25 ℃, Ipr=0A, Vref-2.5V
Table 1. 1. 1	First	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

<sup>[2]:</sup> Min/Max value is the mean value +/-3sigma; according to the statistical law, 99.73% of the data is in this range Inside.

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.10. NSM2117-66B3F-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 3.3V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	lpr	-66		66	А	
Sensitivity	Sens		20		mV/A	prmin< pr< prmax
Zero current output voltage	VQVO		1.65		V	Ipr=0A
Sensitivity error <sup>[1][2]</sup>	Facus	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25℃~150℃, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C ~25 °C , Ipr=0A
	VDF	-8		8	mV	TA = 25 ℃~150 ℃,lpr=0A, Vref-1.65V
Reference error <sup>[2]</sup>	VRE	-15		15	mV	TA = -40 °C ~25 °C , Ipr=0A ,
Tatal autout ausau[1]	Ftatal	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25 ℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = 25℃
BandWidth	BW		400		kHz	-3dB bandwidth, TA = 25 ℃, CL=220pF, VCC=5V
Rise time <sup>[1][2]</sup>	Tr		1.2		us	TA = 25℃, CL=220pF, VCC=5V
Propagation delay <sup>[1][2]</sup>	$T_{pd}$		1.2		us	TA = 25℃, CL=220pF, VCC=5V
Response time [1][2]	$T_{response}$		1.5		us	TA = 25℃, CL=220pF, VCC=5V

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

 $<sup>\</sup>hbox{\it [2]: Min/Max value is the mean value +/-3 sigma; according to the statistical law, 99.73\% of the data is in this range Inside.}$ 

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference., Is the worst case.

# 4.11. NSM2117-40B5F-Q0SWR Characteristics (TA= -40°C to 150°C, VCC = 5V, unless otherwise specified)

Parameters	Symbol	Min	Тур	Max	Unit	Comments
Current sensing range	lpr	-40		40	А	
Sensitivity	Sens		50		mV/A	Iprmin<  Ipr<  Iprmax
Zero current output voltage	VQVO		2.5		V	Ipr=0A
Sensitivity error <sup>[1][2]</sup>	Facus	-1.5		1.5	%	TA = 25°C~150°C
Sensitivity error	Esens	-2		2	%	TA = -40°C~25°C
		-5		5	mV	TA = 25°C~150°C, lpr=0A
Offset error <sup>[2]</sup>	VOE	-5		5	mV	TA = -40 °C ~25 °C , Ipr=0A
(2)		-8		8	mV	TA = 25 °C ~150 °C , Ipr=0A, Vref-2.5V
Reference error <sup>[2]</sup>	VRE	-15		15	mV	TA = -40 ℃~25 ℃, Ipr=0A, Vref-2.5V
Tatal autout ausau[1]	Ftatal	-1.5		1.5	%	TA = 25°C~150°C
Total output error <sup>[1]</sup>	Etotal	-2		2	%	TA = -40°C~25°C
Sensitivity error lifetime drift [3]	Esens_drift		±2.1		%	After reliability test, TA = 25℃
Offset lifetime drift <sup>[3]</sup>	VOE_drift		±8		mV	After reliability test, TA = 25℃
Reference Lifetime drift [3]	VRE_drift		±7		mV	After reliability test, TA = 25℃
Total output error lifetime drift [3]	Etotal_drift		±2.3		%	After reliability test, TA = $25^{\circ}$ C
BandWidth	BW		400		kHz	-3dB bandwidth, TA = 25°C, CL=220pF, VCC=5V
Rise time <sup>[1][2]</sup>	Tr		1.2		us	TA = 25°C, CL=220pF, VCC=5V
Propagation delay <sup>[1][2]</sup>	$T_{pd}$		1.2		us	TA = 25°C, CL=220pF, VCC=5V
Response time [1][2]	$T_{response}$		1.5		us	TA = 25℃, CL=220pF, VCC=5V

<sup>[1]:</sup> In production, total error and sensitivity error are measured and calculated at 30A, A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage.

In the fourth chapter, the increase or decrease of the material number and the tightening of the parameter range, Novosense reserves the right not to send PCN to the customer, unless the expansion of the parameter range affects the customer's use and product performance.

 $<sup>\</sup>hbox{\it [2]: Min/Max value is the mean value +/-3 sigma; according to the statistical law, 99.73\% of the data is in this range Inside.}$ 

<sup>[3]:</sup> The reliability data is implemented in accordance with the AEC-Q100 standard. This item is derived from the experimental results with the largest change after the PC, HTS, HAST, UHAST, HTOL, TC and other test data required by AEC-Q100 Grade0 as a reference. , Is the worst case.

#### 5. Typical Performance Characteristics

#### 5.1. Overview

NSM2117 current sensor can accurately measure AC/DC current while minimizing the overall measurement cost. Current sensors based on the Hall principle can be widely used in all current monitoring applications such as consumption, industry, and automotive. Compared with current transformers, the extremely small size of NSM2117 SOIC16W can help customers reduce the overall PCB area; compared to Shunt + isolated op amps, NSM2117 only needs low-voltage side power supply, reducing the inconvenience of isolated op amps requiring power supply for both high and low voltages. When using NSM2117, you only need to string the primary side pin into the measured current. According to the part of Maxwell equations about electricity and magnetism, a magnetic field will be generated around the energized conductor of the primary side. The Hall and conditioning amplifier circuits in NSM2117 will convert magnetic field into an output voltage, and the output voltage increases or decreases in proportion to the input current.

Benefiting from the typical value of the primary resistance of NSM2117 is only 0.85mohm, as long as the customer conducts a reasonable heat dissipation design, the temperature rise brought by the measurement of large current can be effectively reduced.

At the same time, NSM2117 uses dual Hall sampling internally, the common mode magnetic field brought by the outside world can be effectively reduced. According to the measured typical value, if the 100G common mode magnetic field acts vertically on the chip, it will only bring an error of less than 1G in the output. (Equivalent to input). Because NSM2117 has a good ability to resist common-mode magnetic fields, it can still maintain excellent performance in motor control or some harsh current measurement environments.

#### 5.2. NSM2117 F version(fixed output)

In some applications, the ADC and the current sensor do not share a power rail, so the sensor needs to have absolute sensitivity that does not vary with the power supply voltage. The value of the sensing current can be obtained by (vout-vref)/Sensitivity. For ±50A measurement range, if Vout measures 3.7V and VREF measures 2.5V, then the input current is (3.7V-2.5V)/40mV/A=30A. In practical applications, Vout and VREF can be directly collected by differential ADC to obtain input current, and the measurement accuracy will not be affected by power supply changes.

#### 5.3. Overcurrent Fault Performance

NSM2117 has overcurrent protect function. When the primary current exceeds the overcurrent threshold, the internal error comparator reverses, driving Open Drain Output to work, and the Fault pin is pulled down.

The overcurrent threshold can be adjusted linearly by voltage of VOC pin as shown in Figure 5.1. VOC voltage can be divided by VREF voltage for Bipolar device. The effective input voltage of Voc is between 0.33~1.32V for 3.3V supply and 0.5~2V for 5V supply. When VOC<0.1V, the fault threshold is a default Value -100%FullScale by factory setting.

The corresponding relationship between the overcurrent threshold and Voc voltage is shown in the following table.

VOC/VCC-2 2V/V	VOC(VCC=5V)/V	Fault Thresho	ld/%FullScale	
VOC(VCC=3.3V)/V	VOC(VCC-5V)/V	Bipolar IP	Unipolar IP	
<0.1	<0.1	100%	50%	
0.33	0.5	50%	25%	
0.495	0.75	75%	37.5%	
0.661	1	100%	50%	
0.826	1.25	125%	62.5%	
0.991	1.5	150%	75%	
1.156	1.75	175%	85%	
1.32	2	200%	100%	

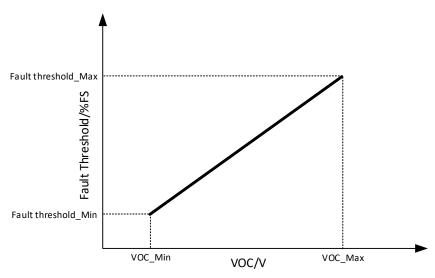


Figure 5.1 VOC and Fault threshold regulation

Overcurrent Fault is triggered when the primary current (positive or negative current) exceeds the overcurrent threshold. Fault is cleared when the absolute value of the primary current is less than the current threshold set minus current hysteresis. Tfr is Fault Response time: the time from the primary current meets the overcurrent condition to Fault pin is pulled down. The timing of overcurrent protection is as follows:

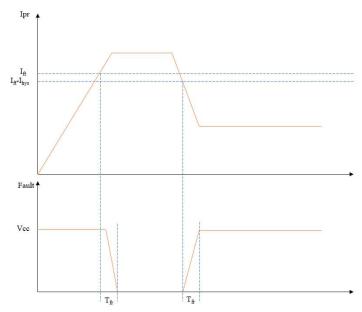


Figure 5.2 NSM2117 Overcurrent Performance

#### 5.4. Definition of NSM2117 terms

#### Power-on time (Tpo)

When the power supply climbs from 0 to the chip's working range, NSM2117 needs some time to establish the internal working logic. Tpo time is defined as: the time from the power supply climbing to Vccmin to the output reaching the steady state within  $\pm 10\%$ , As shown below:

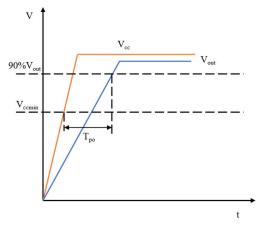


Figure 5.3 NSM2117 Power-on time

#### Rise time (Tr)

The time from 10% to 90% of the output signal is defined as the output rise time. For step input signals, there is such an approximate relationship between the rise time and bandwidth of the output signal: f(-3dB) = 0.35/Tr.

#### Propagation delay (Tpd)

The time from 20% of the primary current to 20% of the output signal is defined as the output propagation delay time.

#### Response time (Tresponse)

The time from 90% of the primary current to 90% of the output signal is defined as the output response time.

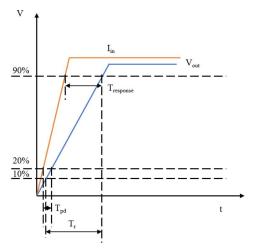


Figure 5.4 NSM2117 response time

#### Sensitivity and sensitivity error

Sensitivity is defined as the ratio of the output voltage proportional to the primary input current. Sensitivity is the slope of the curve in the figure below.

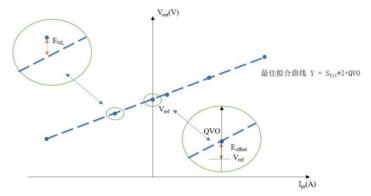


Figure 5.5 NSM2117 Sensitivity and error

The sensitivity error is defined as the deviation between the slope of the best-fit curve and the slope of the ideal curve. The slope of the best-fit curve comes from the measured value:

$$E_{sens} = \frac{(S_{fit} - S_{ideal})}{S_{ideal}} * 100\%$$

#### Offset error

The zero current output error is defined as the difference between the output voltage and the reference voltage when the primary current is 0A, Vref here is VCC/2 or 0.1\*VCC (R version):

$$E_{offset} = QVO - V_{ref}$$

#### **Nonlinear error**

The linearity error is defined as the error from the maximum deviation point of the best-fit curve to the full scale. The mathematical expression is as follows:

$$V_{NL} = V_{outmax} - (S_{fit} * I_{max} + QVO)$$

among them:

Voutmax is the output voltage furthest from the fitted curve;

Imax is the primary current farthest from the fitted curve;

Therefore, the nonlinear error can be mathematically expressed as the following formula:

$$E_{NL} = \frac{V_{NL}}{FS} * 100\%$$

#### **Total error**

The total error is defined as the error between the actual given current and the current measured by the chip, in other words, the difference between the actual output voltage and the ideal output voltage. It should be known that in different current ranges, the factors that dominate the total error are different. If it is under low current measurement, the zero point error is the main source of error; if under high current measurement, the total error caused by the zero point error is very small, and the dominant error is the sensitivity error.

$$E_{total}(I_{pr}) = \frac{V_{out_{ideal}}(I_{pr}) - V_{out}(I_{pr})}{FS}$$

#### 6. Application note

#### 6.1. Typical application circuit

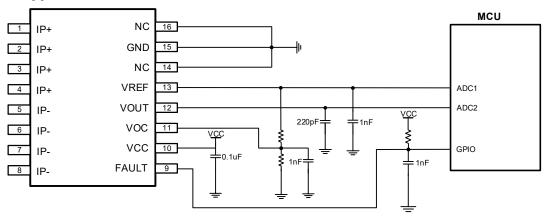


Figure 6.1 Typical application diagram

#### 6.2. Recommended circuit for EMC enhanced

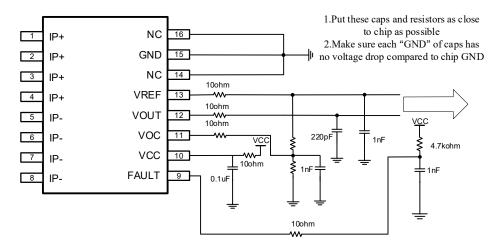


Figure 6.2 Recommended circuit for EMC enhanced

#### 6.3. PCB Layout

For NSM2117 in high-current monitoring applications, a reasonable layout will make the system heat dissipation faster and better. The copper area on the NSM2117 Demo board is 21mm\*18mm (very small copper area is used to illustrate the worse situation, rather than a large copper area), the top layer and the bottom layer are 2oz copper thick. If customers want to achieve better heat dissipation, they can use multi-layer boards and thicken the copper thickness to achieve it, and can use active heat dissipation solutions in the system, such as adding heat sinks and fans. If you need to use the NSM2117 Demo board to evaluate the performance of this current sensor, please contact Novosense sales team for support.

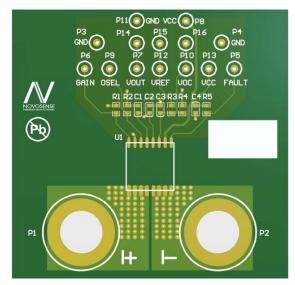


Figure 6.3 NSM2117 PCB Layout

#### 6.4. Thermal evaluation

The thermal evaluation experiment is tested at room temperature, which mainly illustrates the temperature rise of the NSM2117 current sensor under different currents. With these data and the above-mentioned layout guide, customers can design heat dissipation according to actual application requirements. The ambient temperature in this experiment is room temperature. The surface is mounted on the above Demo board for temperature rise test. There is no external active heat dissipation device (such as a fan, etc.). The relationship between junction temperature and time is measured. 10 minutes of temperature data are collected. Under normal circumstances, the temperature rise It is basically fixed in about 10 minutes, and the specific test data are as follows:

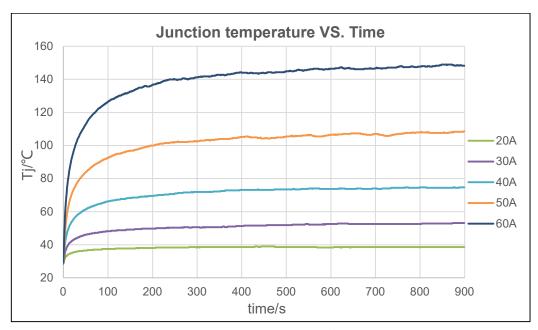


Figure 6.4 NSM2117 Junction temperature vs. Different continues current

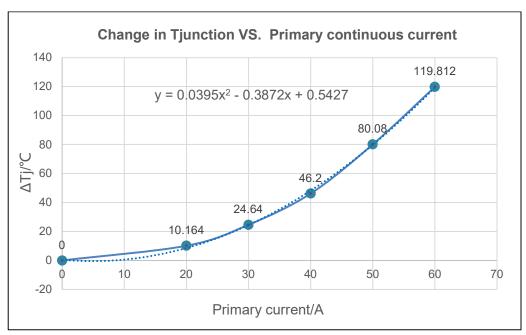


Figure 6.5 NSM2117 Estimation function of junciton temperature at different currents (PCB is in worst case)

It is important to note that the above temperature rise experiment data is only based on the Demo board, in order to reflect the relationship between NSM2117 current and temperature in a worst case. Customers can reduce the temperature rise of Tj by increasing or thickening the copper area of the PCB, using multi-layer boards, or adding active heat dissipation devices such as fans (Tj<150°C). If customers compare NSM2117 with other competing products, please refer to the same PCB design instead of using specially designed PCB provided by competing products. Novosense can provide a 16-pin general-purpose Demo board for comparison of temperature rises of competing products.

### 7. Package Information

#### **SOW16 PACKAGE**

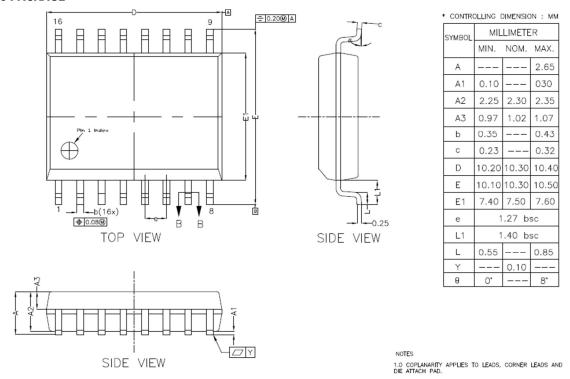


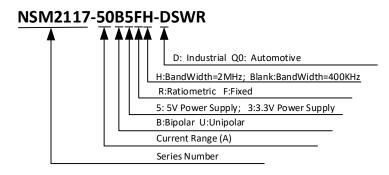
Figure 7.1 SOW16 Package Shape and Dimension in millimeters and (inches)

### 8. Ordering Information

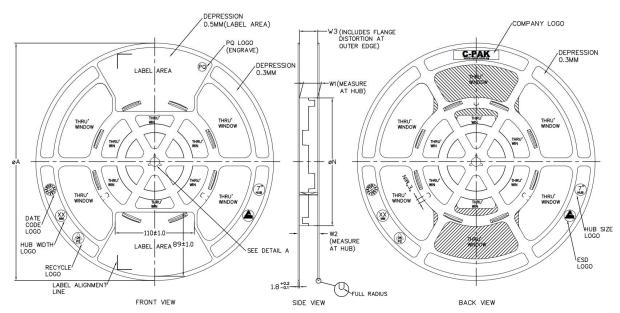
Part number	Primary current(A)	Power supply(V)	Sensitivity(mV/A)	MSL	Package	BandWidth
NSM2117-40B5FH-Q0SWR	±40	5V	50	3	SOW16	1MHz
NSM2117-50B5FH-Q0SWR	±50	5V	40	3	SOW16	1MHz
NSM2117-66B3FH-Q0SWR	±66	3.3V	20	3	SOW16	1MHz
NSM2117-65B5FH-Q0SWR	±65	5V	30.8	3	SOW16	1MHz
NSM2117-65B5F-Q0SWR	±65	5V	30.8	3	SOW16	400KHz
NSM2117-20B3FH-Q0SWR	±20	3.3V	66	3	SOW16	1MHz
NSM2117-20B5FH-Q0SWR	±20	5V	100	3	SOW16	1MHz
NSM2117-100B5FH-Q0SWR	±100	5V	20	3	SOW16	1MHz
NSM2117-66B3F-Q0SWR	±66	3.3V	20	3	SOW16	400KHz
NSM2117-65B5F-Q0SWR	±40	5V	50	3	SOW16	400KHz

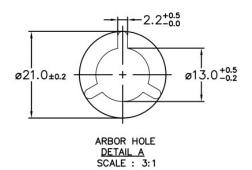
 $<sup>{}^\</sup>star If$  you need other version, please contact Novosense for special support

#### **Part Number Rule:**



### 9. Tape and Reel Information

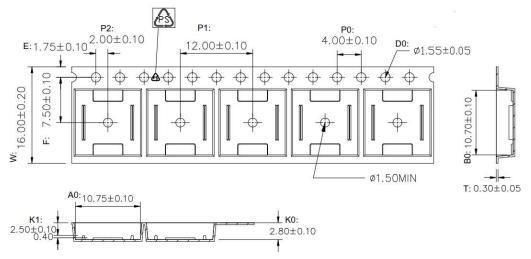




PRODUCT SPECIFICATION							
TAPE WIDTH	ØA ±2.0	øN ±2.0	W1	W2 (MAX)	W3	E (MIN)	
08MM	330	178	8.4 +1.5	14.4		5.5	
12MM	330	178	12.4 +2.0	18.4	SHALL ACCOMMODATE TAPE WIDTH WITHOUT INTERFERENCE	5.5	
16MM	330	178	16.4 + 2.8	22.4		5.5	
24MM	330	178	24.4 +2.8	30.4		5.5	
32MM	330	178	32.4 +2.0	38.4		5.5	

	SURFACE RESISTIVITY							
LEGEND	SR RANGE	TYPE	COLOUR					
Α	BELOW 10 <sup>12</sup>	ANTISTATIC	ALL TYPES					
В	10° TO 10"	STATIC DISSIPATIVE	BLACK ONLY					
С	105 & BELOW 105	CONDUCTIVE (GENERIC)	BLACK ONLY					
Ε	10° TO 10"	ANTISTATIC (COATED)	ALL TYPES					

Note: MPQ(SOW16):1K



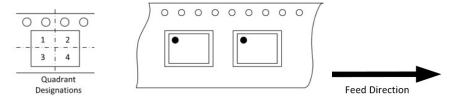


Figure 9.1 Tape and Reel Information of SOW16

#### 10. Revision History

Revision	Description	Date
0.0	Initial Version.	2023/6/1
0.1	Add NSM2117-66B3F information	2023/7/7
0.2	Update part number rule and add NSM2117-65B5FH/NSM2117-65B5F information	2023/8/1
0.3	Add NSM2117-20B3FH information	2023/10/16
0.4	Add NSM2117-20B5FH information	2023/10/23
0.5	Add NSM2117-100B5FH information	2023/12/15
0.6	Update spec in common characteristics	2024/1/8
0.7	Add NSM2117-66B3F information	2024/5/14
0.8	Modify the bandwidth in the Part Number rule diagram	2024/6/13
0.9	Update response time in common characteristics and application circuit for EMC enhanced	2024/6/17
0.10	Add MSL information	2024/8/8
0.11	Update Junction temperature	2024/8/20
0.12	Update PCB Layout and Thermal evaluation information	2024/9/20
0.13	Add NSM2117-40B5F information and update Fault Response time in Specifications	2024/11/13
0.14	Update lifetime drift and accuracy parameter in Specifications	2024/11/14
0.15	Update Fault Response time in Specifications	2024/11/15

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