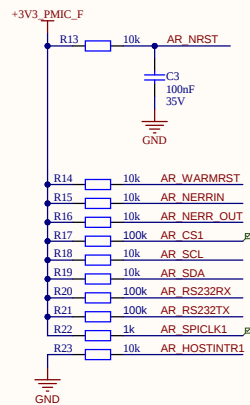
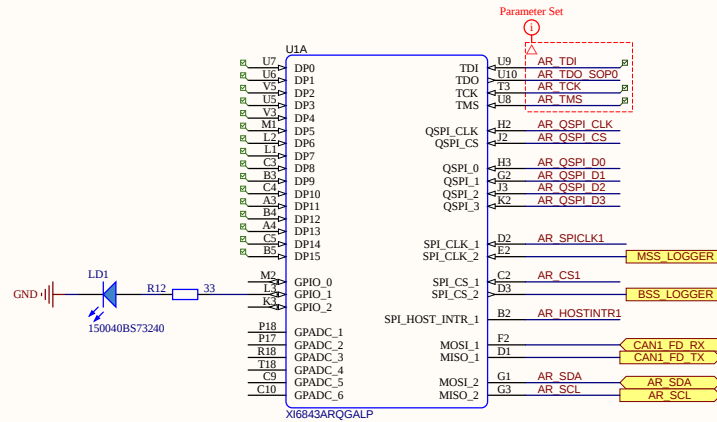
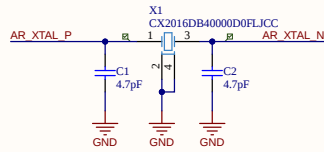
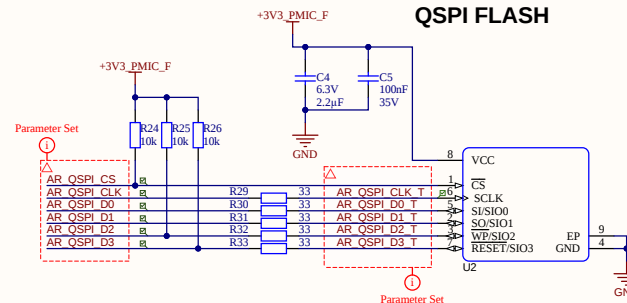


40MHz CRYSTAL

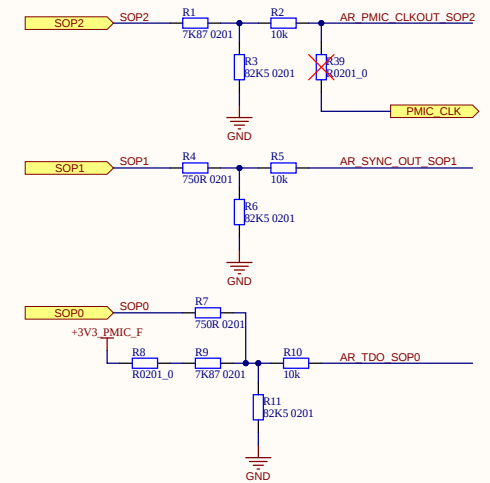


DESIGN NOTE:
Device's Band Gap Reference Output.

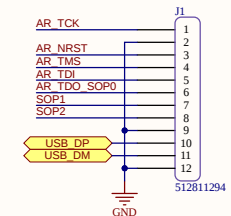
QSPI FLASH



SOP OPTIONS



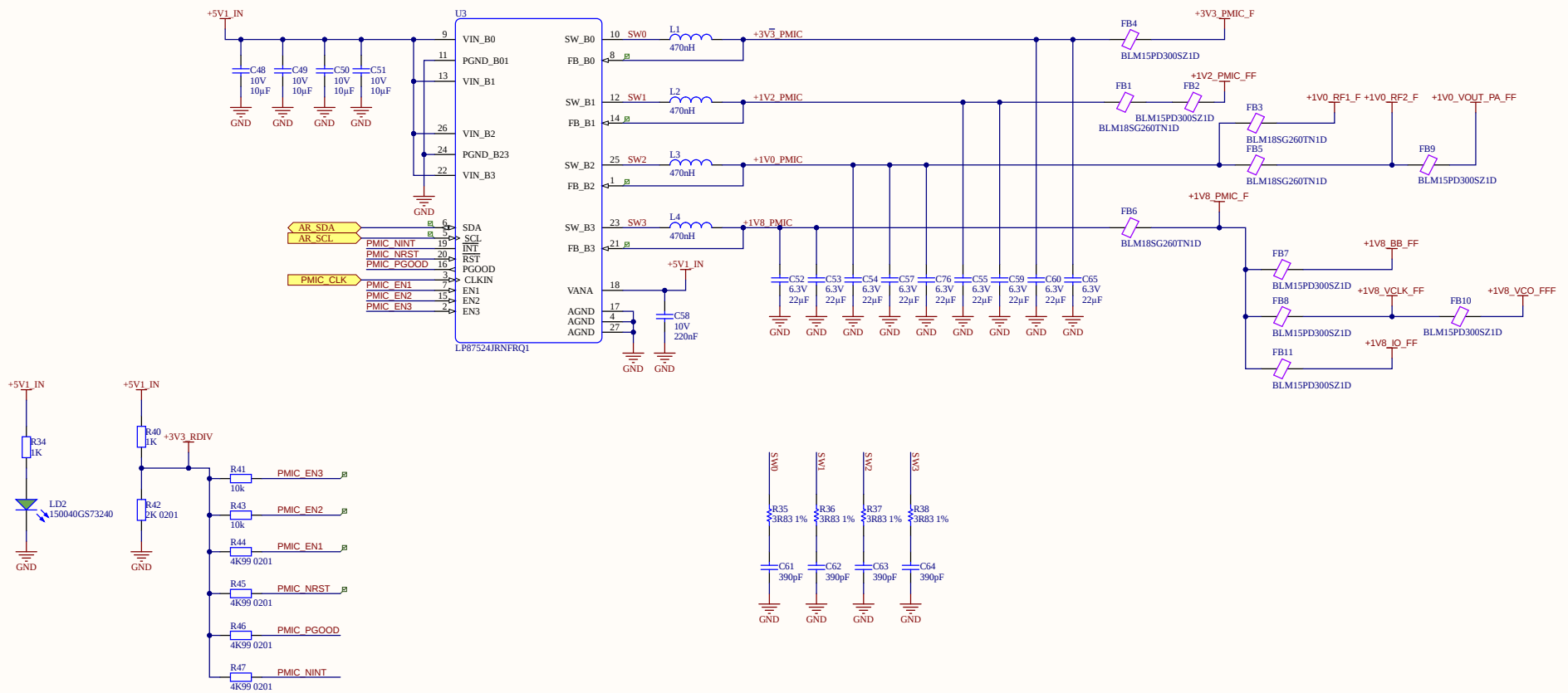
SOP_MODE2 - '011' - DEV/DEBUG
SOP_MODE4 - '001' - FUNCTIONAL MODE
SOP_MODE5 - '101' - FLASH MODE



AoP IO, SOP, JTAG, FLASH

Author	Variant	Page	Date
Isaikin R.E.	[No Variations]	x/x	28/10/2020

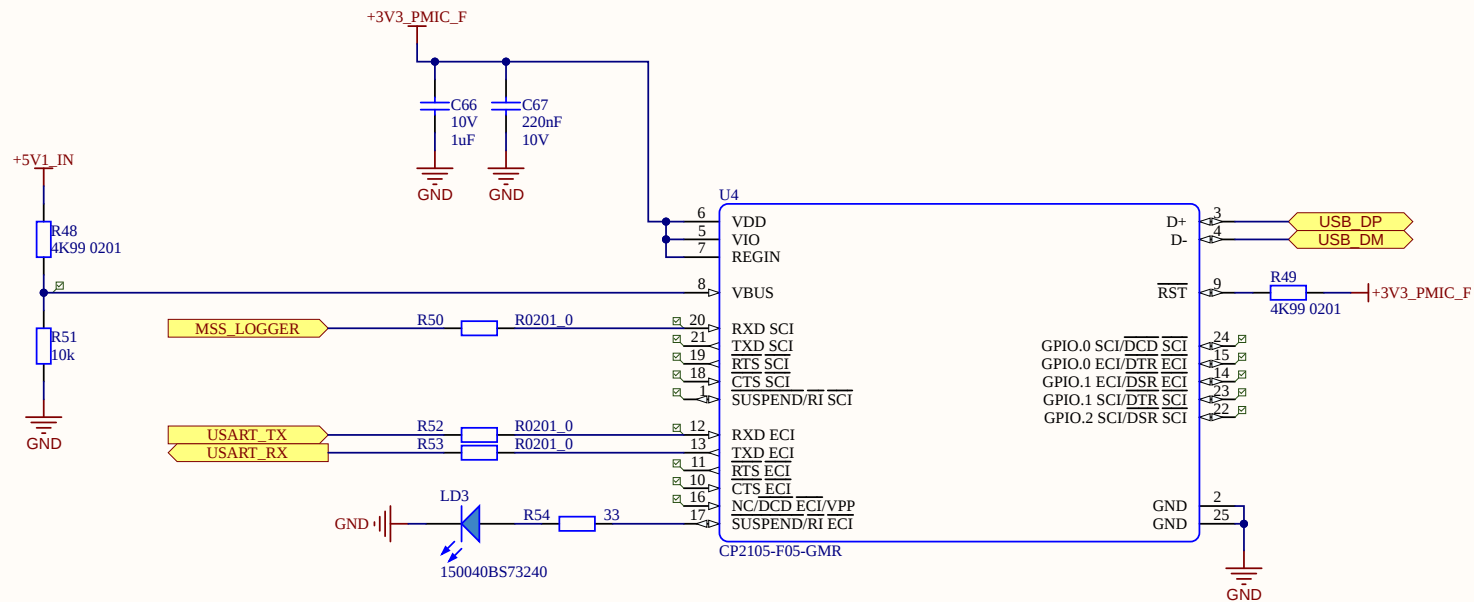
wheel.me



AoP PMIC

Author	Variant	Page	Date
Isaikin R.E.	[No Variations]	x/x	28/10/2020

wheel.me

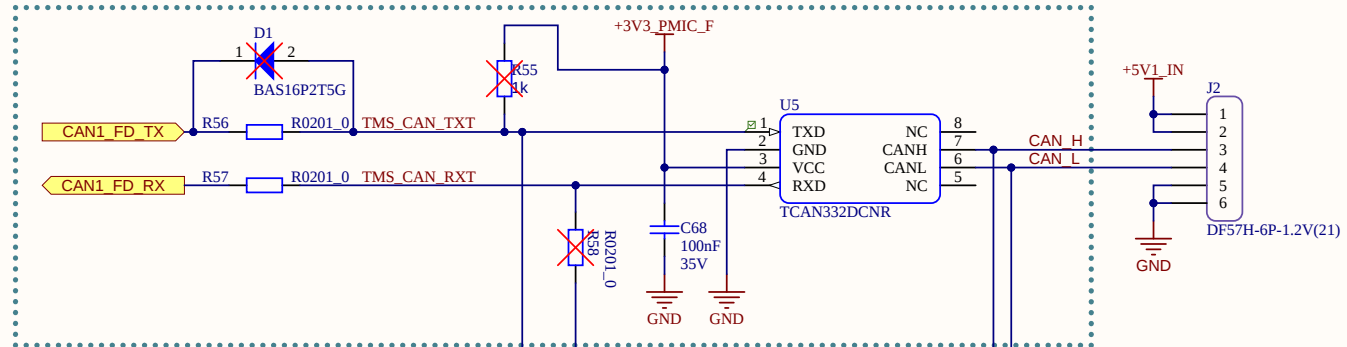


USB - UART

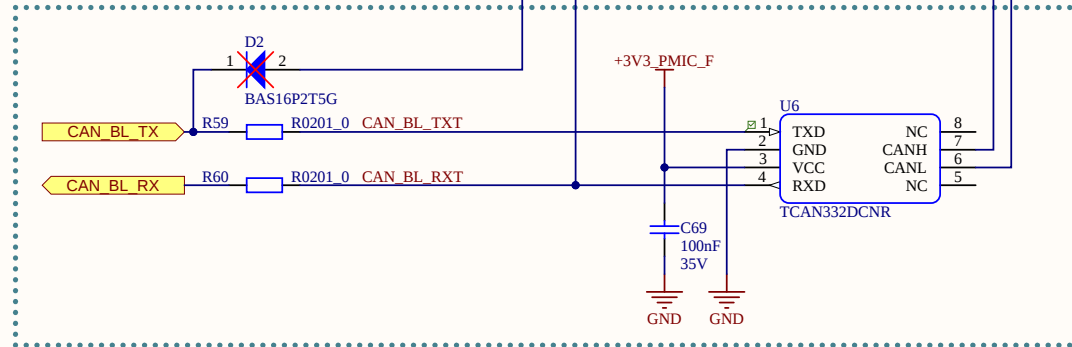
Author	Variant	Page	Date
Isaikin R.E.	[No Variations]	x/x	28/10/2020

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AoP CAN transceiver



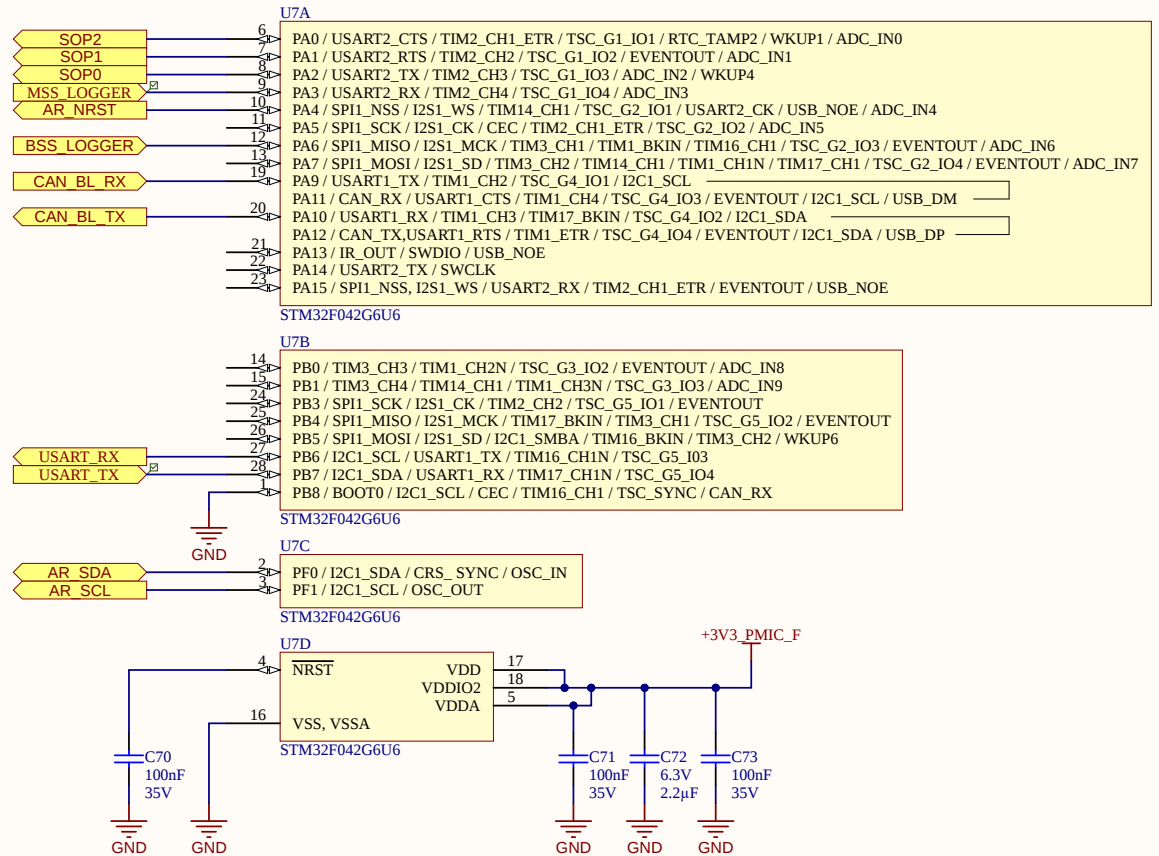
BL CAN transceiver



CAN Transceivers

Author	Variant	Page	Date
Isaikin R.E.	[No Variations]	x/x	28/10/2020

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Bootloader MCU

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Date and Revision

LL1

DATE

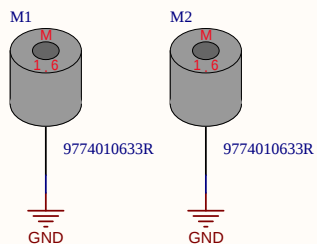
LL2

REVISION

Company logo

Other labels

Mount holes



Labels, Mount holes

Author	Variant	Page	Date
Isaikin R.E.	[No Variations]	*	28/10/2020

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