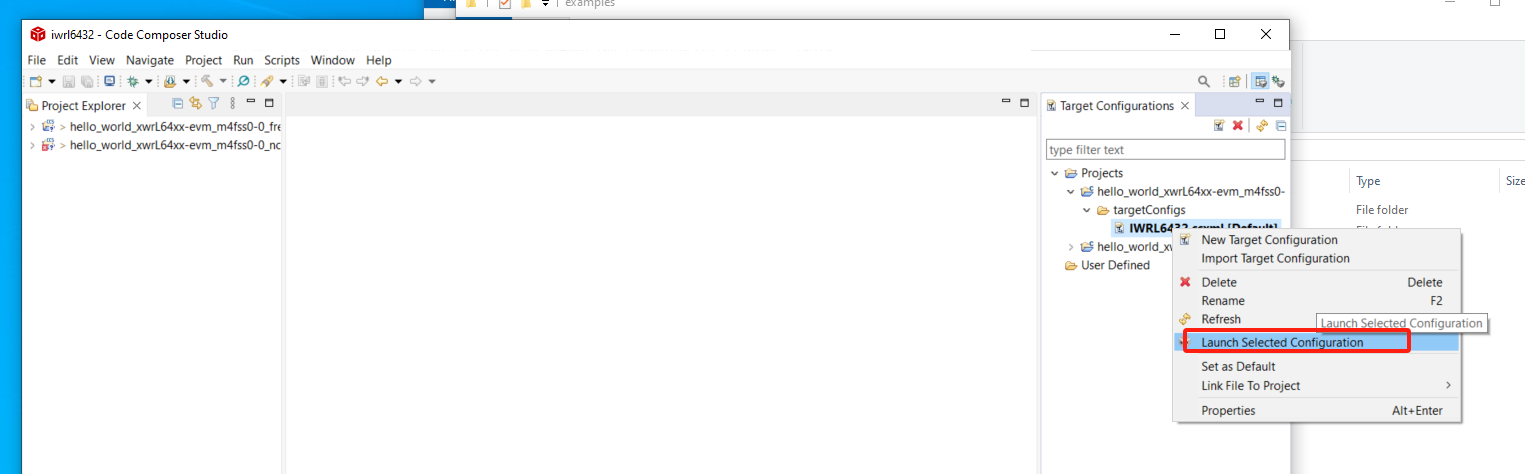
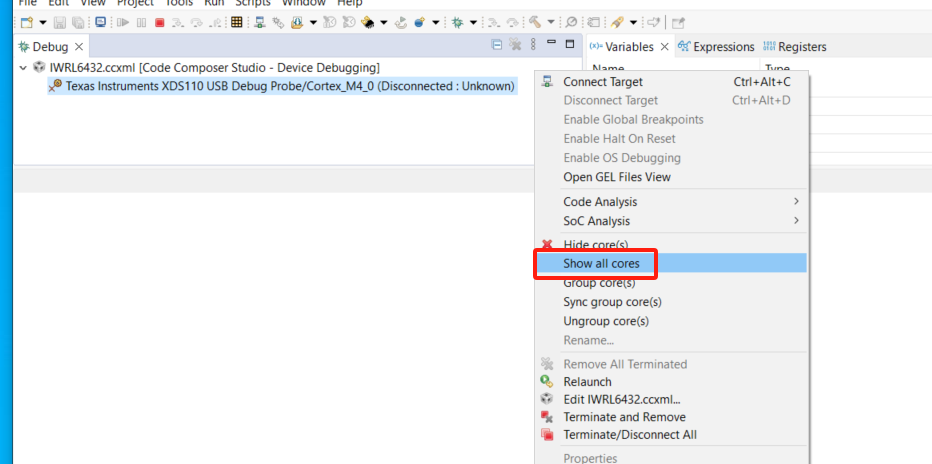
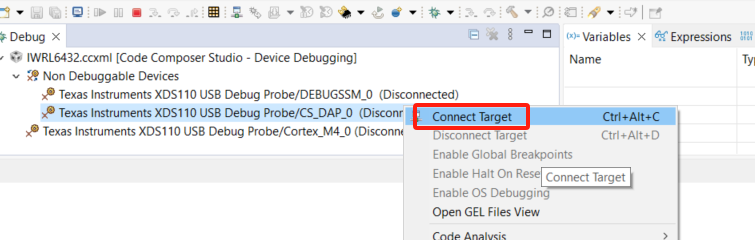
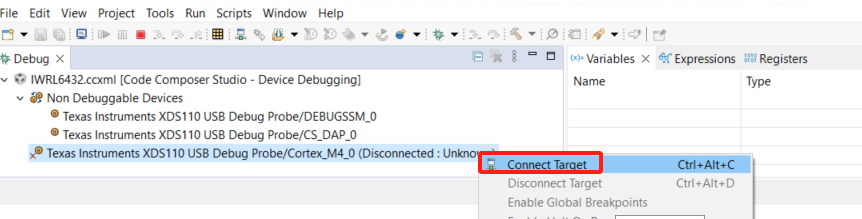
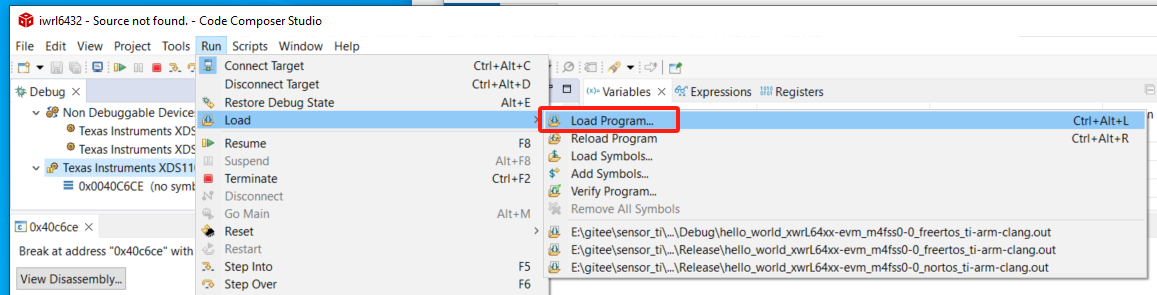
# debug the project “hello\_world”(Release prebuild)

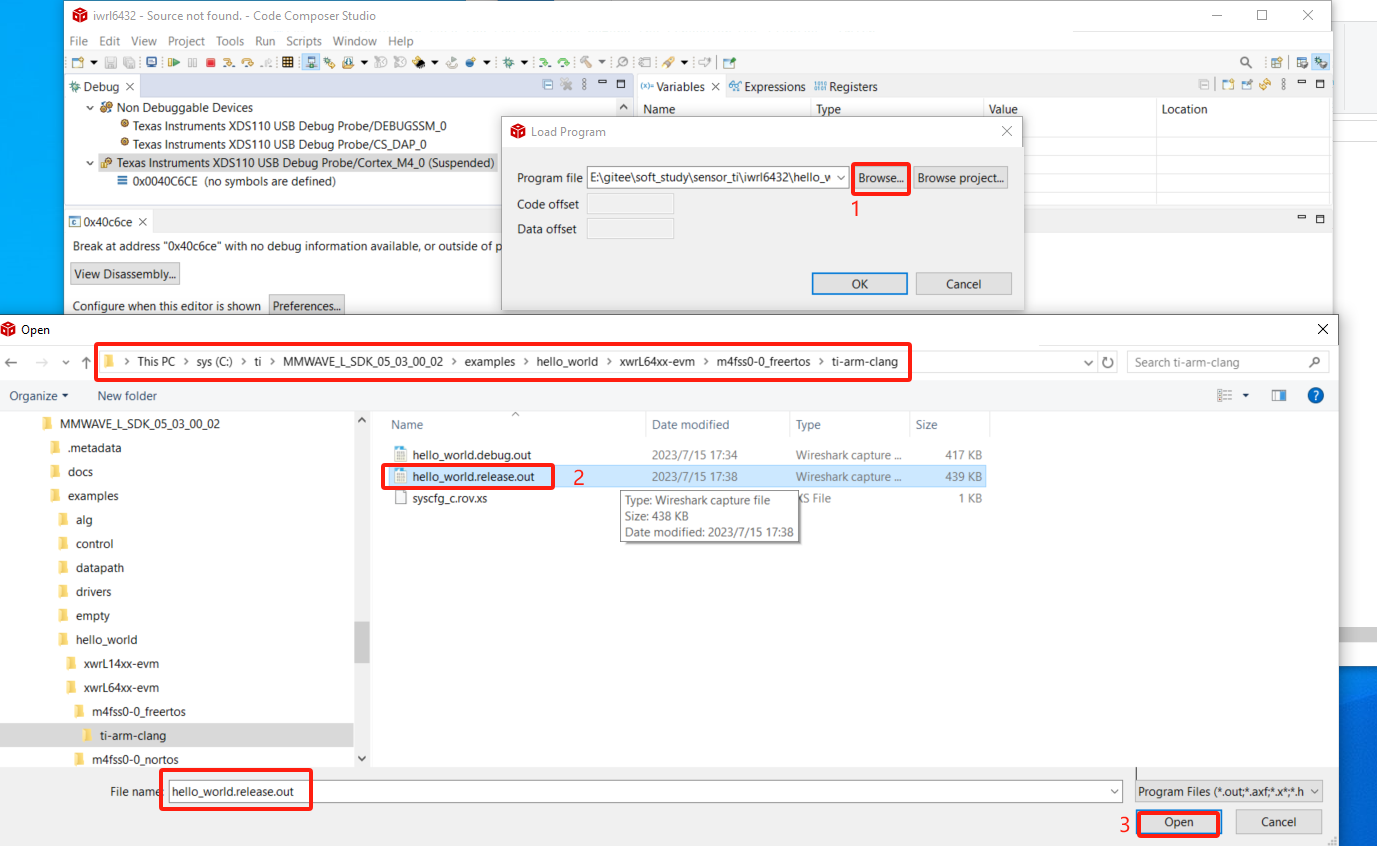


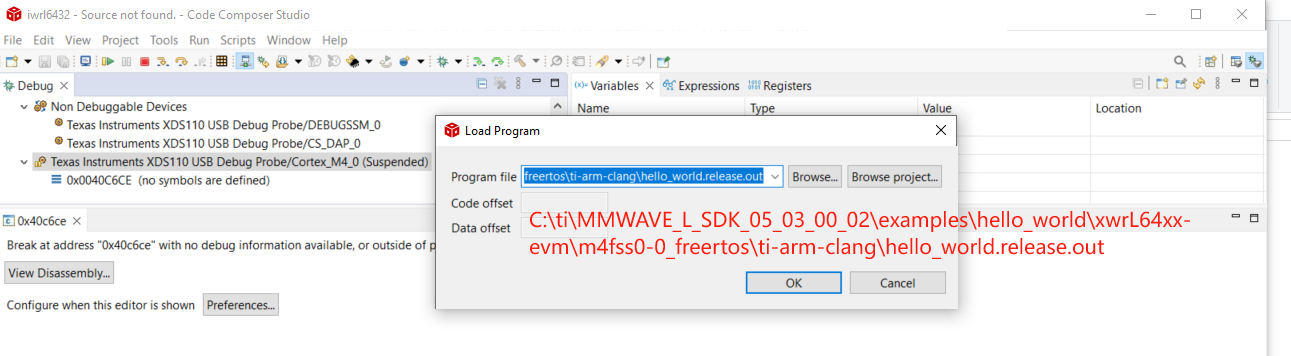


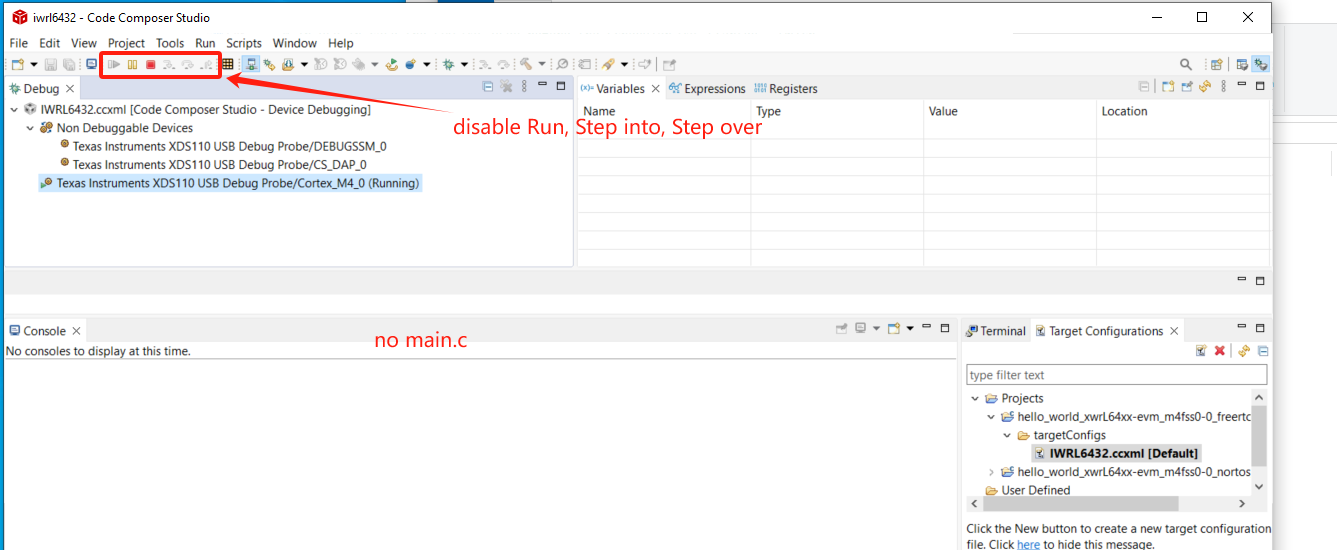


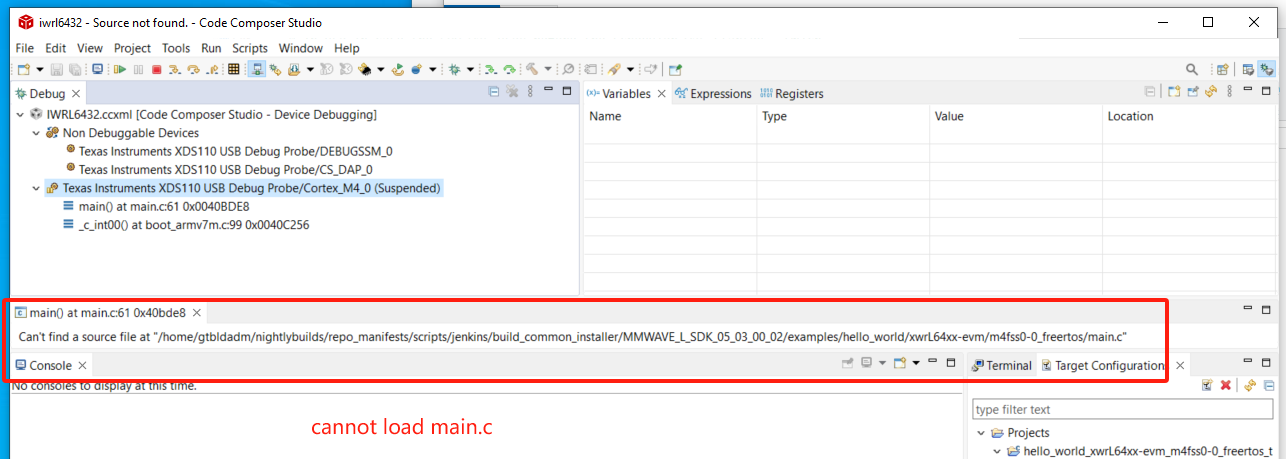












# debug the project “hello\_world”(debug prebuild)

launch configuration “IWRL6432.ccxml”

show cores

connect

connect

load program

