

Figure 1. A Two Chip Cascade System

synchronization

OSC\_CLKOUT

Question1.  
 - Is it necessary to construct a clock synchronization system including the processor (AM2732)?  
 - If we use "OSC\_CLKOUT" of the slave of the front end (AWR2243) to input the clock to the (AM2732) of the processor, the independent oscillator of the processor is not necessary. Is there a problem with this method?

Question3.  
 - Is the input to the processor (FE1\_REFCLK) correct for the connection shown in Figure 1?

Question2.  
 Which system would be better with the processor as the oscillation source, as shown in the figure below?

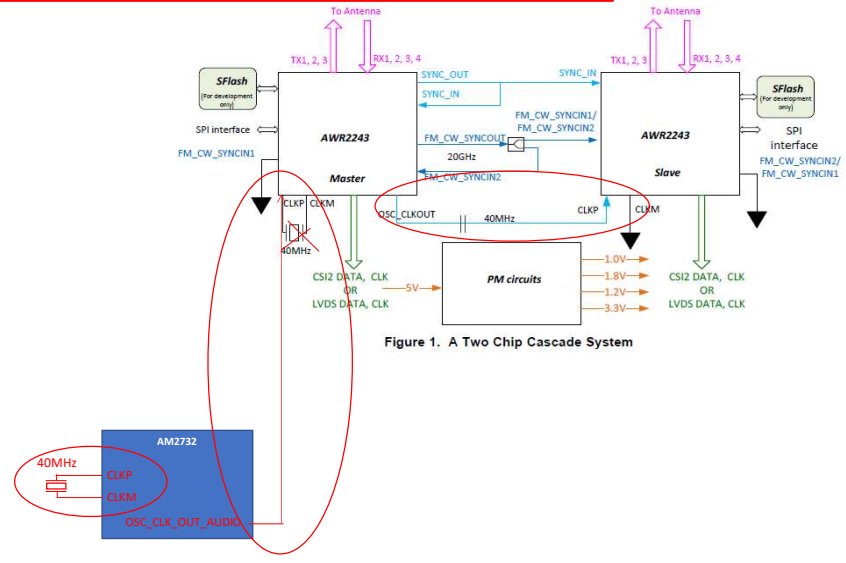


Figure 1. A Two Chip Cascade System

Figure 2. A Four Chip Cascade System

