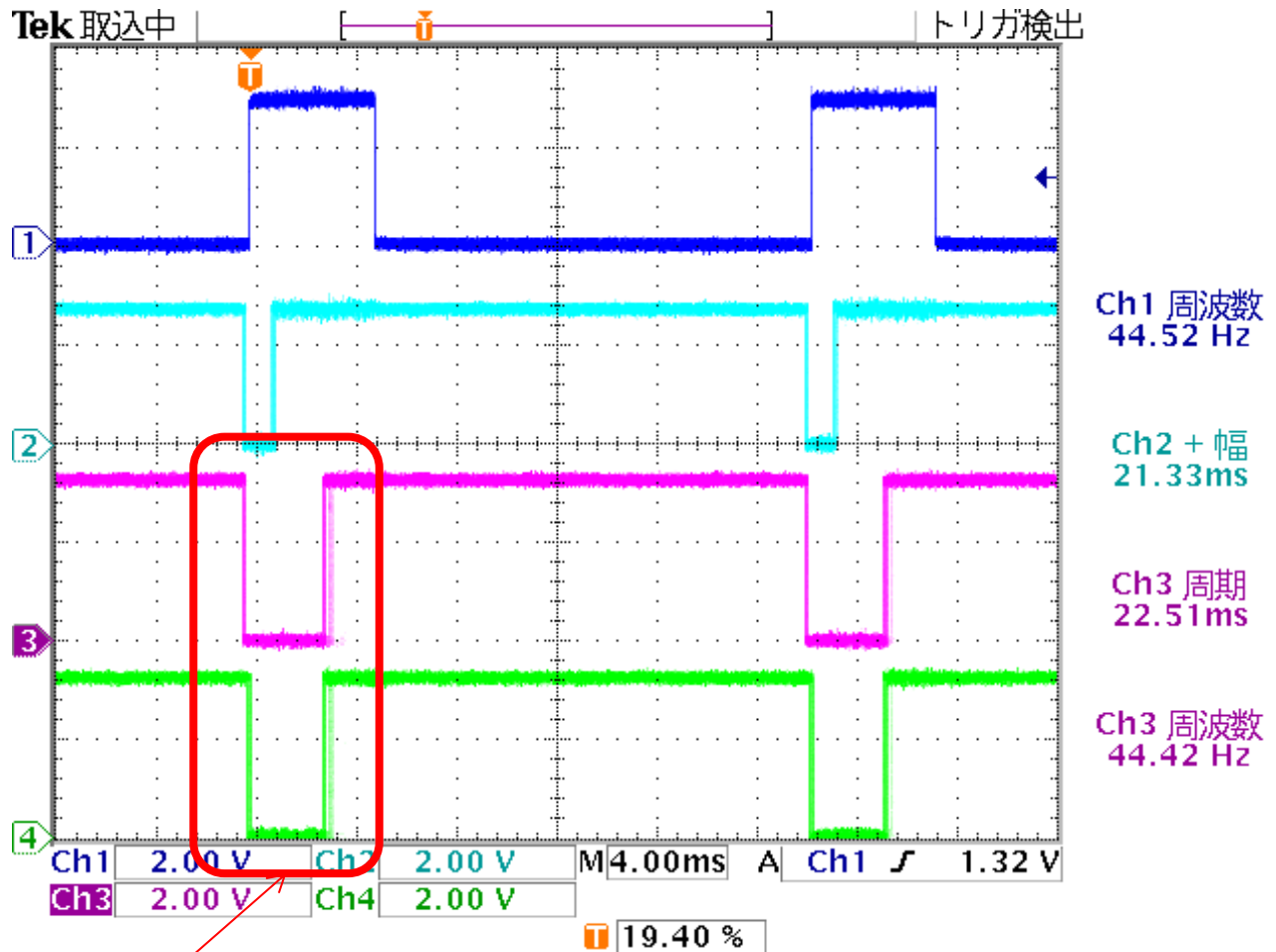


PCLK from imager was stopped at this time.



VSYNC Des low output is longer than VSYNC Ser and this time is synchronized with PLL lock output of Des.