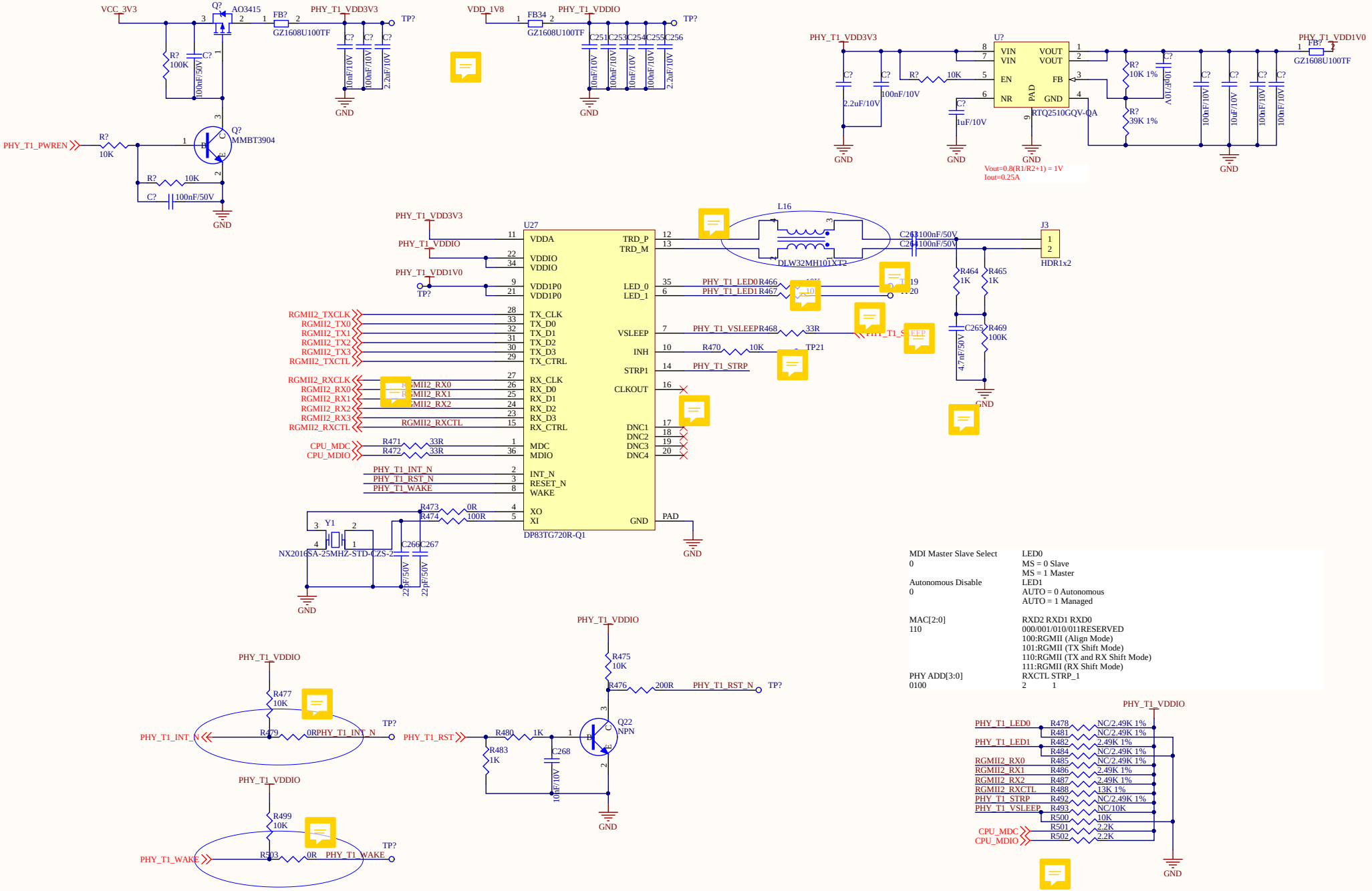
VCC_3V3
Q? AO3415
FB? GZ1608U100TF
PHY_T1_VDD3V3
TP?
R? 100K
C? 100nF/50V
C? 10nF/10V
C? 2.2uF/10V
GND
PHY_T1_PWREN
R? 10K
Q? MMBT3904
C? 100nF/50V
VDD_1V8
FB34 GZ1608U100TF
PHY_T1_VDDIO
C251 C253 C254 C255 C256
U?
VIN VIN EN NR VOUT VOUT FB GND PAD
RTQ2510GQV-QA
R? 10K
C? 1uF/10V
R? 10K 1%
R? 39K 1%
C? 10uF/10V
PHY_T1_VDD1V0
FB? GZ1608U100TF
Vout=0.8(R1/R2+1) = 1V
Iout=0.25A
U27
DP83TG720R-Q1
VDDA
VDDIO
VDD1P0
TX_CLK
TX_D0
TX_D1
TX_D2
TX_D3
TX_CTRL
RX_CLK
RX_D0
RX_D1
RX_D2
RX_D3
RX_CTRL
MDC
MDIO
INT_N
RESET_N
WAKE
XO
XI
TRD_P
TRD_M
LED_0
LED_1
VSLEEP
INH
STRP1
CLKOUT
DNC1
DNC2
DNC3
DNC4
GND
PAD
RGMII2_TXCLK
RGMII2_TX0
RGMII2_TX1
RGMII2_TX2
RGMII2_TX3
RGMII2_TXCTL
RGMII2_RXCLK
RGMII2_RX0
RGMII2_RX1
RGMII2_RX2
RGMII2_RX3
RGMII2_RXCTL
CPU_MDC
CPU_MDIO
PHY_T1_INT_N
PHY_T1_RST_N
PHY_T1_WAKE
R471 33R
R472 33R
R473 0R
R474 100R
Y1 NX2016SA-25MHZ-STD-CZS-2
C266 C267 22pF/50V
L16 DLW32MH101XT2
C263 100nF/50V
C264 100nF/50V
J3 HDR1x2
PHY_T1_LED0 R466
PHY_T1_LED1 R467
PHY_T1_VSLEEP R468 33R
R470 10K
PHY_T1_STRP
R464 1K
R465 1K
C265 4.7nF/50V
R469 100K
R475 10K
R476 200R
Q22 NPN
R480 1K
R483 1K
C268 10uF/10V
R477 10K
R479 0R
R499 10K
R503 0R
MDI Master Slave Select
0
MS = 0 Slave
MS = 1 Master
Autonomous Disable
0
LED1
AUTO = 0 Autonomous
AUTO = 1 Managed
MAC[2:0]
110
RXD2 RXD1 RXD0
000/001/010/011RESERVED
100:RGMII (Align Mode)
101:RGMII (TX Shift Mode)
110:RGMII (TX and RX Shift Mode)
111:RGMII (RX Shift Mode)
PHY ADD[3:0]
0100
RXCTL STRP_1
2 1
R478 NC/2.49K 1%
R481 NC/2.49K 1%
R482 2.49K 1%
R484 NC/2.49K 1%
R485 NC/2.49K 1%
R486 2.49K 1%
R487 2.49K 1%
R488 13K 1%
R492 NC/2.49K 1%
R493 NC/10K
R500 10K
R501 2.2K
R502 2.2K
Title
Size A3
Number
Revision
Date: 2021/10/9
Sheet of
File: D:\SVN\..\PHY_2_T1.SchDoc
Drawn By: