



## TekExpress Ethernet

### 10BASE-T Test Report

Setup Information			
DUT ID	DUT001	Scope Information	DPO71254C, B010684
Date/Time	2024-02-02 17:35:35	Scope F/W Version	10.8.3 Build 3
Device Type	Ethernet	DATA Probe Model	TDP1500
TekExpress Ethernet Version	10.2.1.11	DATA Probe Serial Number	B048050
TekExpress Framework Version	4.11.0.45		
Execution Mode	Live		
Compliance Mode	True		
Overall Test Result	<b>Fail</b>		
Overall Execution Time	0:42:10		
DUT COMMENT:	General comment		

Test Name Summary Table	
<a href="#">Link Pulse Load1(TPM)</a>	Pass
<a href="#">Link Pulse Timing Load1(TPM)</a>	Pass
<a href="#">Link Pulse Load2(TPM)</a>	Pass
<a href="#">Link Pulse Timing Load2(TPM)</a>	Pass
<a href="#">Link Pulse Load3(TPM)</a>	Pass
<a href="#">Link Pulse Timing Load3(TPM)</a>	Pass
<a href="#">Link Pulse Load1</a>	Pass
<a href="#">Link Pulse Timing Load1</a>	Pass
<a href="#">Link Pulse Load2</a>	Pass
<a href="#">Link Pulse Timing Load2</a>	Pass
<a href="#">Link Pulse Load3</a>	Pass
<a href="#">Link Pulse Timing Load3</a>	Pass
<a href="#">Differential Voltage</a>	Fail
<a href="#">TP_IDL Load1</a>	Pass
<a href="#">TP_IDL Load2</a>	Pass
<a href="#">TP_IDL Load3</a>	Pass
<a href="#">TP_IDL Load1(TPM)</a>	Fail
<a href="#">TP_IDL Load2(TPM)</a>	Pass
<a href="#">TP_IDL Load3(TPM)</a>	Pass
<a href="#">Jitter Normal(TPM)</a>	Pass
<a href="#">Jitter 8.0(TPM)</a>	Pass
<a href="#">Jitter 8.5(TPM)</a>	Pass
<a href="#">Jitter Normal</a>	Pass
<a href="#">Jitter 8.0</a>	Pass
<a href="#">Jitter 8.5</a>	Pass
<a href="#">MAU Internal</a>	Fail
<a href="#">MAU External</a>	Fail
<a href="#">MAU Internal(Inverted)</a>	Fail
<a href="#">MAU External(Inverted)</a>	Fail
<a href="#">Harmonic</a>	Fail
<a href="#">CM Voltage</a>	Skipped

Link Pulse Load1(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load1 With Twisted Pair</a>	0	Pass	H:1	N.A	1	Hits	No Hits

<a href="#">cable_Run1</a>							
COMMENTS		Mask Model Selected: Head and Tail					
<a href="#">Back to Summary</a>							

Link Pulse Timing Load1(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load1 With Twisted Pair cable_Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.
COMMENTS							
<a href="#">Back to Summary</a>							

Link Pulse Load2(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load2 With Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS		Mask Model Selected: Head and Tail					
<a href="#">Back to Summary</a>							

Link Pulse Timing Load2(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load2 With Twisted Pair cable_Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.
COMMENTS							
<a href="#">Back to Summary</a>							

Link Pulse Load3(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load3 With Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS		Mask Model Selected: Head and Tail					
<a href="#">Back to Summary</a>							

Link Pulse Timing Load3(TPM)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load3 With Twisted Pair cable_Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.
COMMENTS							
<a href="#">Back to Summary</a>							

Link Pulse Load1							
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Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load1 Without Twisted Pair cable _Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS		Mask Model Selected: Head and Tail					

[Back to Summan](#)

Link Pulse Timing Load1							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load1 Without Twisted Pair cable _Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.
COMMENTS							

[Back to Summan](#)

Link Pulse Load2							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load2 Without Twisted Pair cable _Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS		Mask Model Selected: Head and Tail					

[Back to Summan](#)

Link Pulse Timing Load2							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load2 Without Twisted Pair cable _Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.
COMMENTS							

[Back to Summan](#)

Link Pulse Load3							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Load3 Without Twisted Pair cable _Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS		Mask Model Selected: Head and Tail					

[Back to Summan](#)

Link Pulse Timing Load3							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Link Pulse Timing Load3 Without Twisted Pair cable _Run1</a>	16.0003	Pass	L:8.0003 H:7.9997	8	24	ms	Average of 5 Link Interval 16.0003ms. Link Interval ranges from 16.0003ms 16.0003ms.

COMMENTS							
<a href="#">Back to Summary</a>							
<b>Differential Voltage</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Differential Voltage MaxPositive_Run1</a>	1.7200	Fail	L:-0.4800 H:1.0800	2.2	2.8	V	
<a href="#">Differential Voltage MaxNegative_Run1</a>	1.7200	Fail	L:-0.4800 H:1.0800	2.2	2.8	V	
COMMENTS							
<a href="#">Back to Summary</a>							
<b>TP_IDL Load1</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">TP_IDL Load1 Without Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS							
MaskSelection: Both							
<a href="#">Back to Summary</a>							
<b>TP_IDL Load2</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">TP_IDL Load2 Without Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS							
MaskSelection: Both							
<a href="#">Back to Summary</a>							
<b>TP_IDL Load3</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">TP_IDL Load3 Without Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits
COMMENTS							
MaskSelection: Both							
<a href="#">Back to Summary</a>							
<b>TP_IDL Load1 (TPM)</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">TP_IDL Load1 With Twisted Pair cable_Run1</a>	850	Fail	H:-849	N.A	1	Hits	Hits in Head segments: ['1:0', '2:850'] Hits in Tail segments: ['1:0', '2:0']
COMMENTS							
MaskSelection: Both							
<a href="#">Back to Summary</a>							
<b>TP_IDL Load2 (TPM)</b>							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">TP_IDL Load2 With Twisted Pair cable_Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits

COMMENTS		MaskSelection: Both						<a href="#">Back to Summary</a>
<b>TP_IDL Load3(TPM)</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">TP_IDL Load3 With Twisted Pair cable _Run1</a>	0	Pass	H:1	N.A	1	Hits	No Hits	
COMMENTS		MaskSelection: Both						<a href="#">Back to Summary</a>
<b>Jitter Normal(TPM)</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">Jitter Normal With Twisted Pair Cable Internal _Run1</a>	1.6500	Pass	H:9.3500	N.A	11.0	ns		
COMMENTS								<a href="#">Back to Summary</a>
<b>Jitter 8.0(TPM)</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">Jitter 8.0 With Twisted Pair Cable Internal _Run1</a>	5.3100	Pass	H:16.6900	N.A	22.0	ns		
COMMENTS								<a href="#">Back to Summary</a>
<b>Jitter 8.5(TPM)</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">Jitter 8.5 With Twisted Pair Cable Internal _Run1</a>	5.1400	Pass	H:16.8600	N.A	22.0	ns		
COMMENTS								<a href="#">Back to Summary</a>
<b>Jitter Normal</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">Jitter Normal Without Twisted Pair Cable Internal _Run1</a>	2.5900	Pass	H:13.4100	N.A	16.0	ns		
COMMENTS								<a href="#">Back to Summary</a>
<b>Jitter 8.0</b>								
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments	
<a href="#">Jitter 8.0 Without Twisted Pair Cable Internal _Run1</a>	13.6700	Pass	H:26.3300	N.A	40.0	ns		
COMMENTS								<a href="#">Back to Summary</a>

Jitter 8.5							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">Jitter 8.5 Without Twisted Pair Cable Internal_Run1</a>	13.7000	Pass	H:26.3000	N.A	40.0	ns	
COMMENTS							

[Back to Summary](#)

MAU Internal							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">MAU Internal_Run1</a>	11345	Fail	H:-11344	N.A	1	Hits	Hits in segr [ '1:50', '2:47 3:2578', '4:(008']
COMMENTS							

[Back to Summary](#)

MAU External							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">MAU External_Run1</a>	11336	Fail	H:-11335	N.A	1	Hits	Hits in segr [ '1:0', '2:516 1694', '4:0', 75']
COMMENTS							

[Back to Summary](#)

MAU Internal(Inverted)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">MAU Internal Inverted_Run1</a>	11217	Fail	H:-11216	N.A	1	Hits	Hits in segr [ '1:296', '2:5 '3:1703', '4: 4191']
COMMENTS							

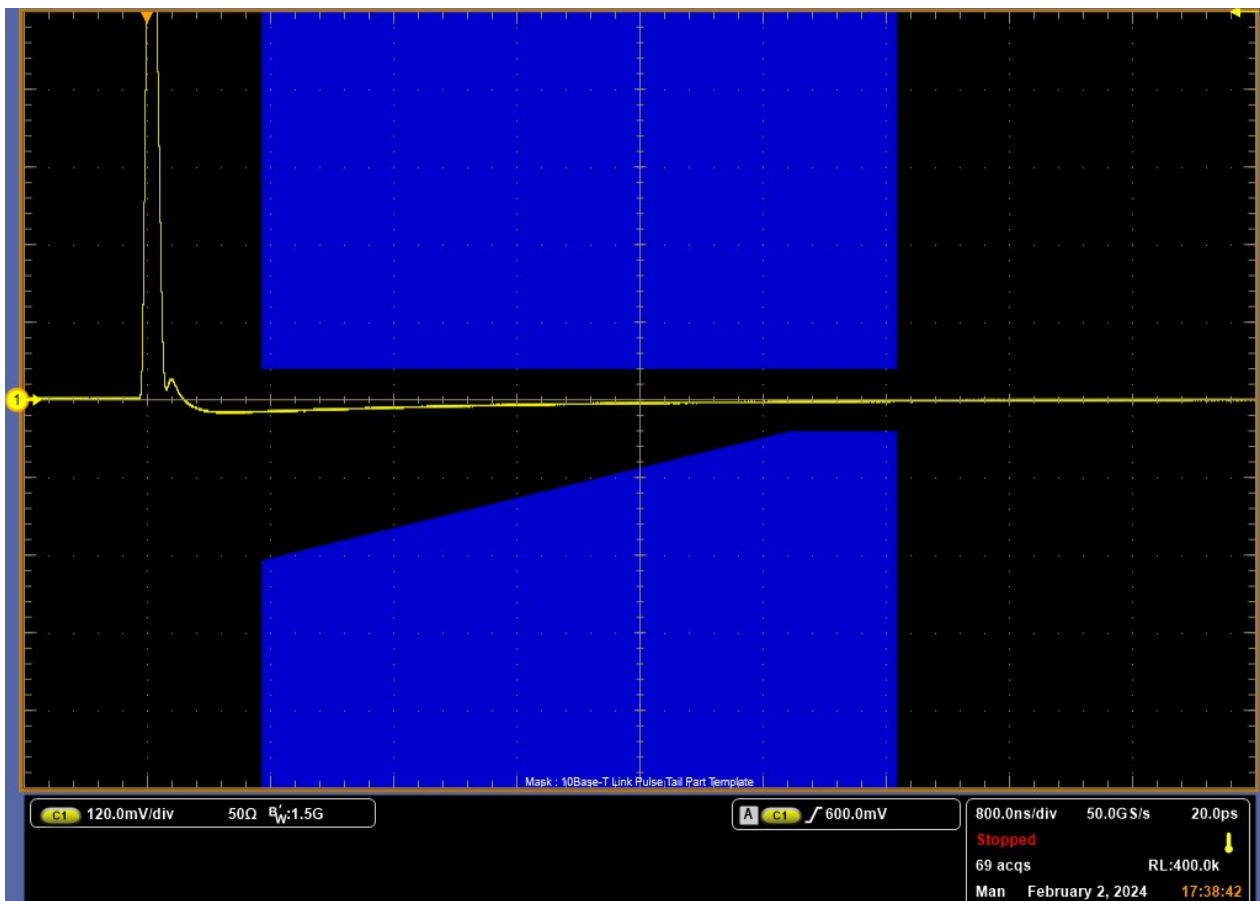
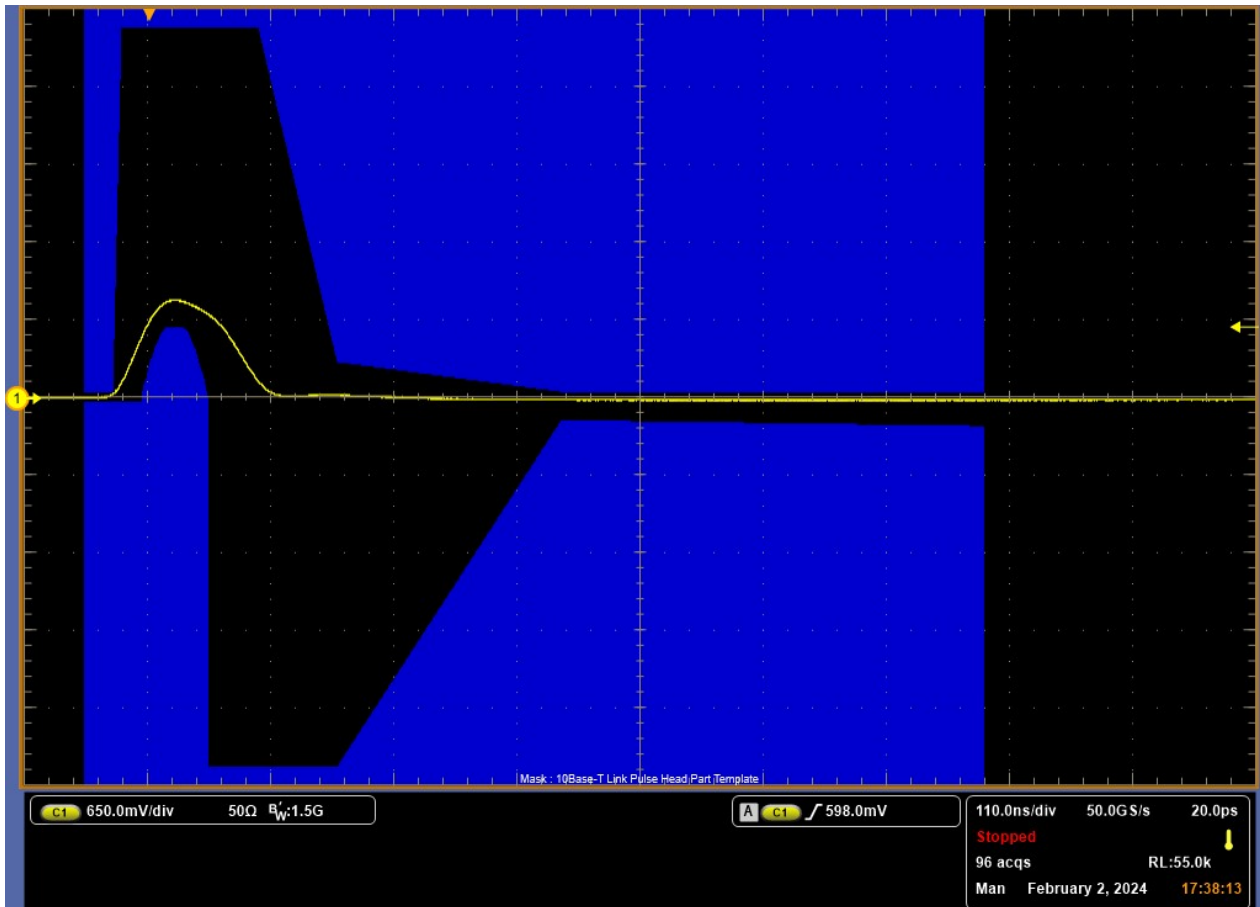
[Back to Summary](#)

MAU External(Inverted)							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
<a href="#">MAU External Inverted_Run1</a>	11464	Fail	H:-11463	N.A	1	Hits	Hits in segr [ '1:109', '2:5 '3:2493', '4: 3127']
COMMENTS							

[Back to Summary](#)

Harmonic							
Measurement Details	Measured Value	Test Result	Margin	Low Limit	High Limit	Units	Comments
							Freq   Mag   (MHz) (dBm) 10*  -72.11 20   -72.41 30   -71.85 40   -71.50 50   -71.03 60   -73.81

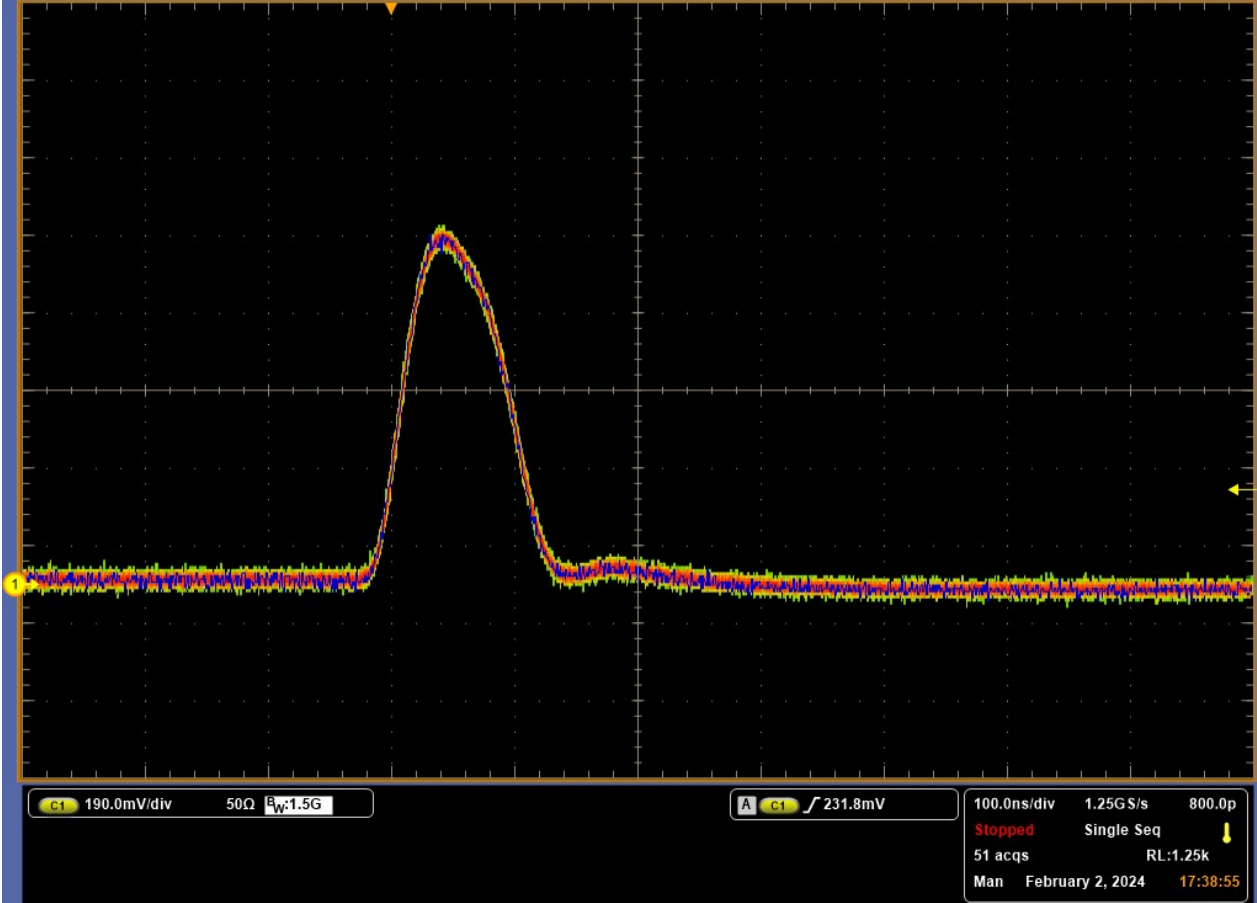






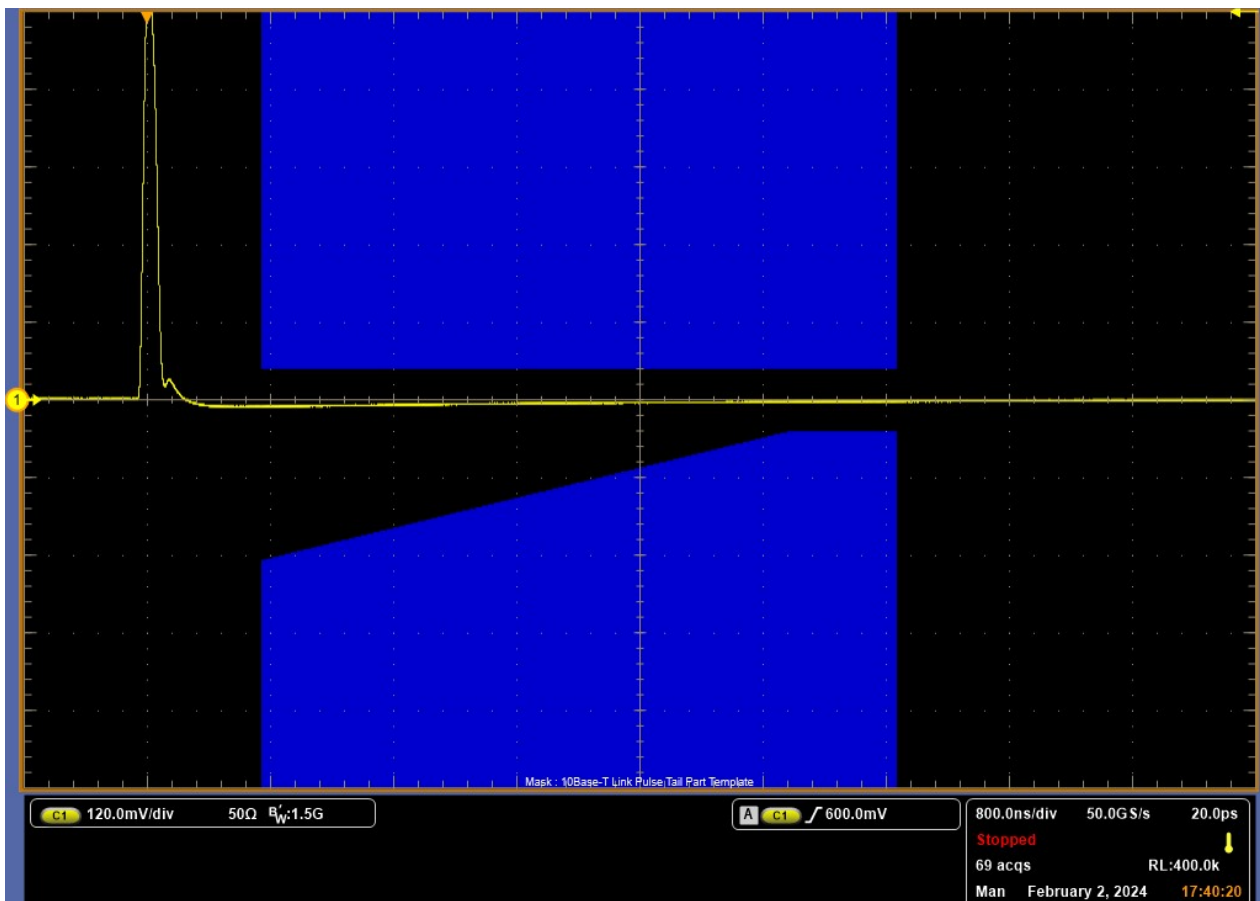
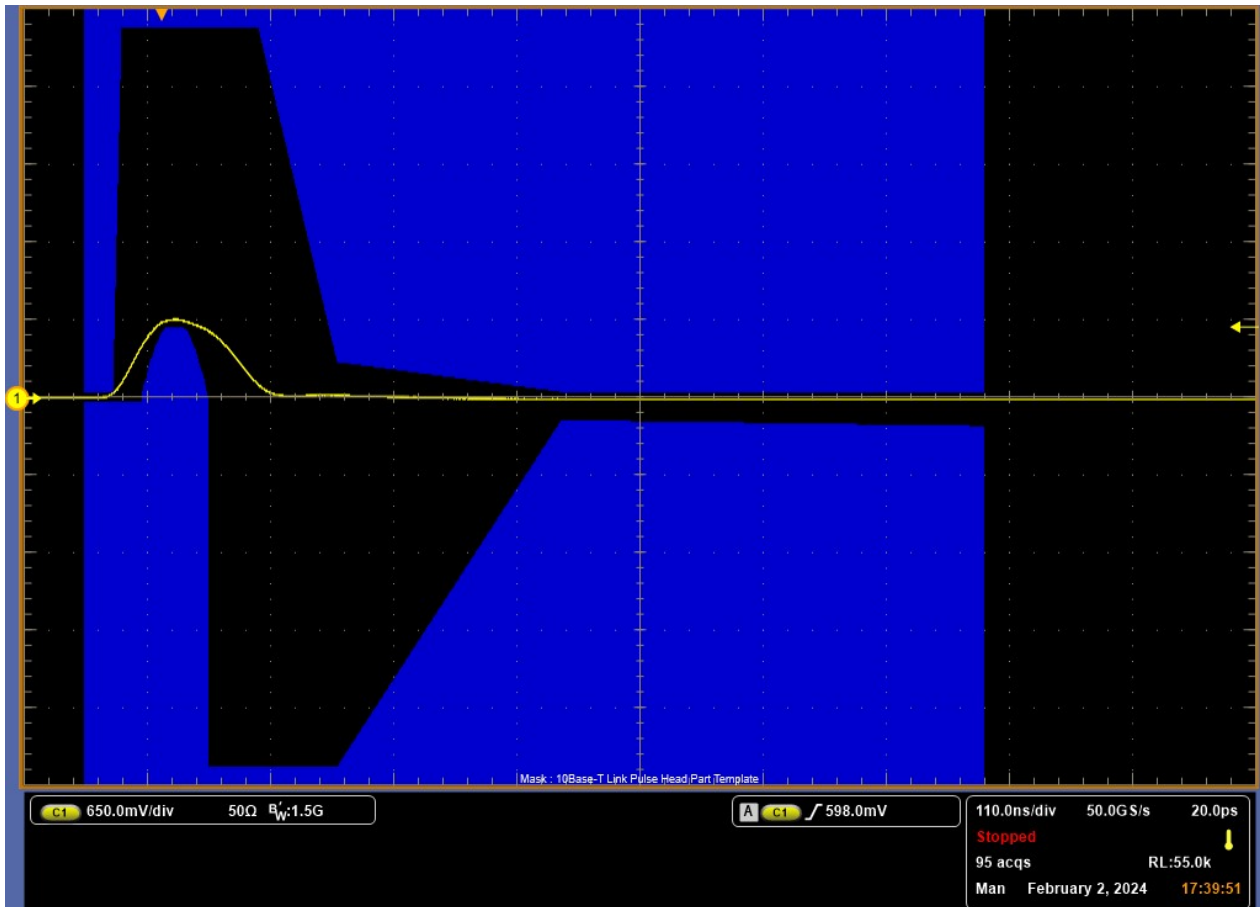
Link Pulse Timing Load1(TPM)

[Link Pulse Timing Load1 With Twisted Pair cable \\_Run1](#)



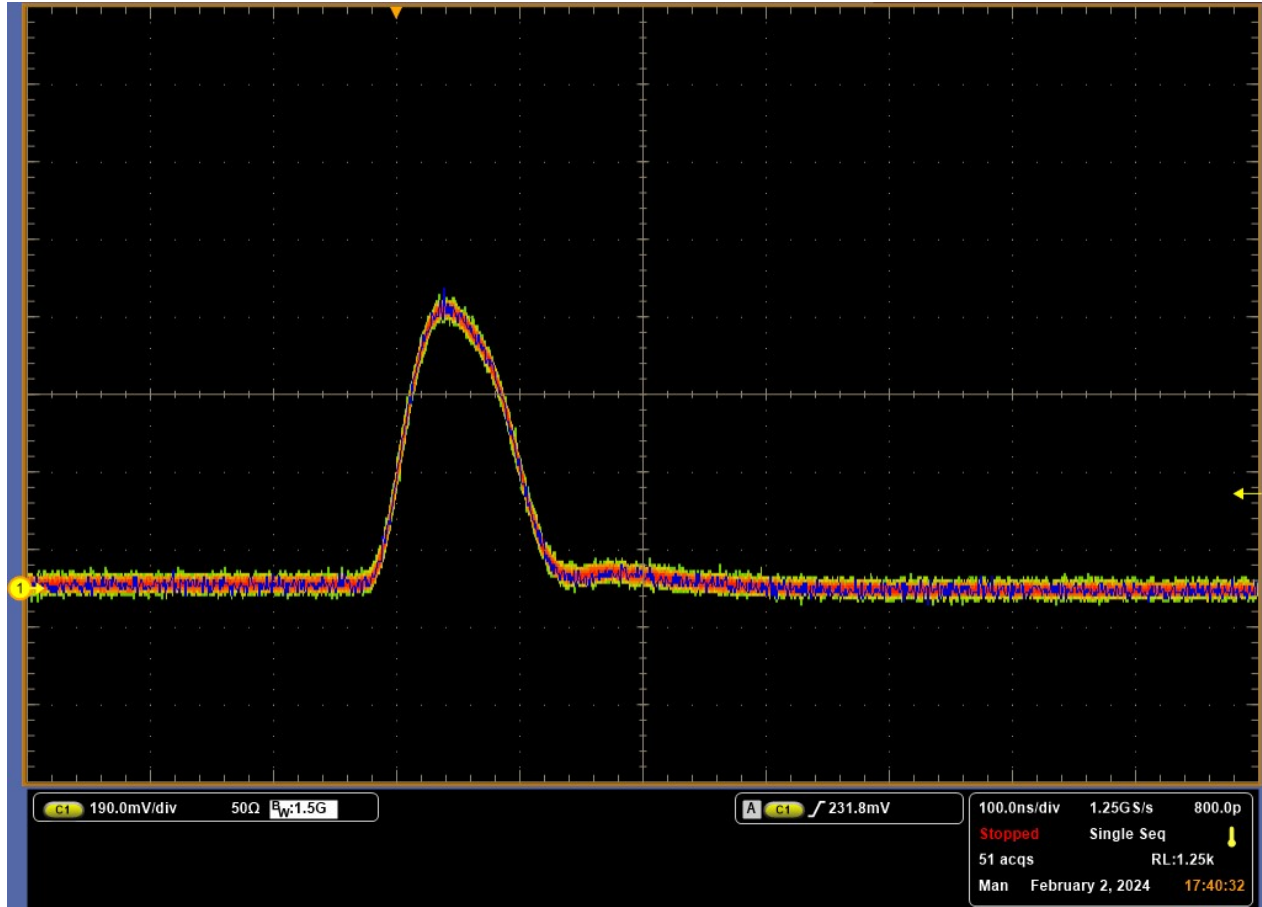
Link Pulse Load2(TPM)

[Link Pulse Load2 With Twisted Pair cable Head\\_Run1](#) [Link Pulse Load2 With Twisted Pair cable Tail\\_Run1](#)



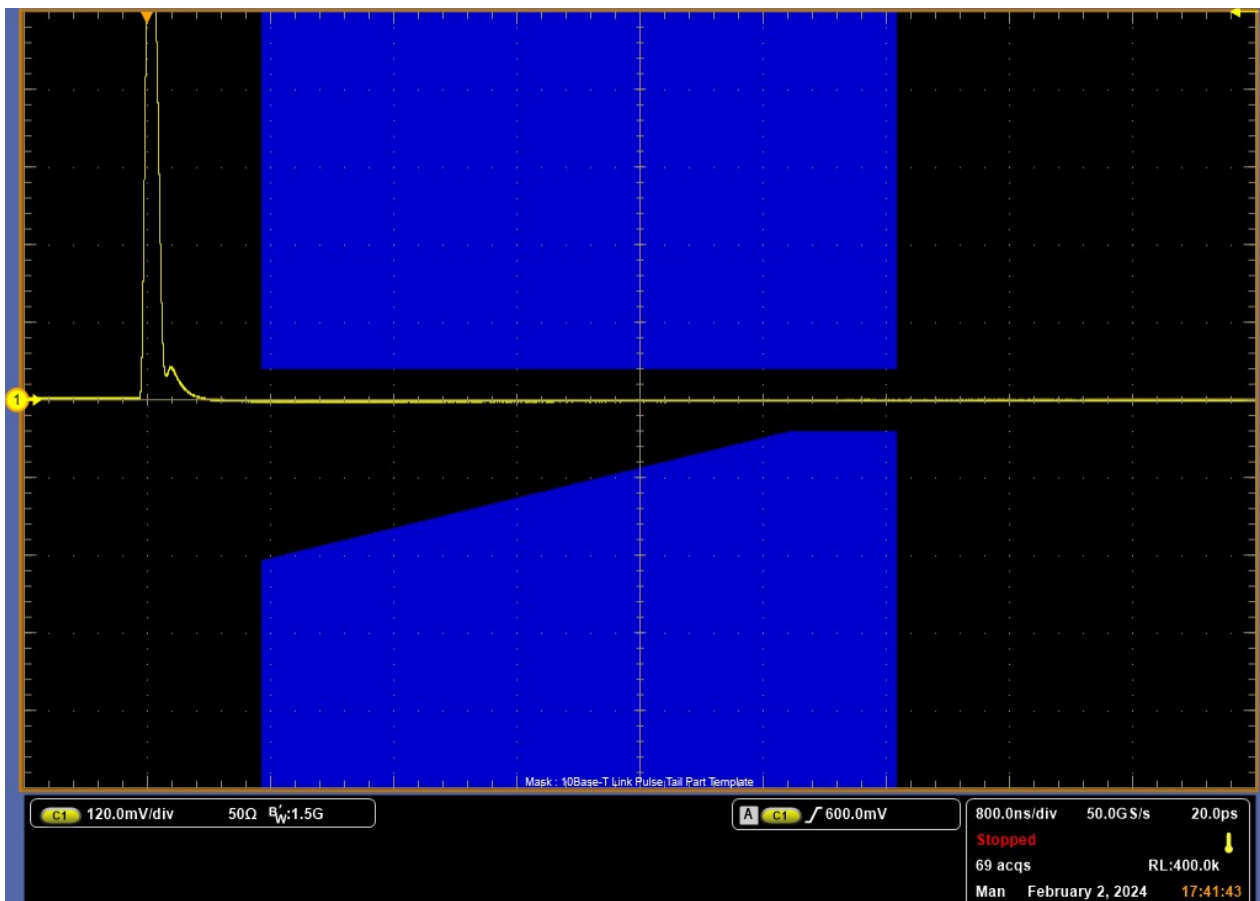
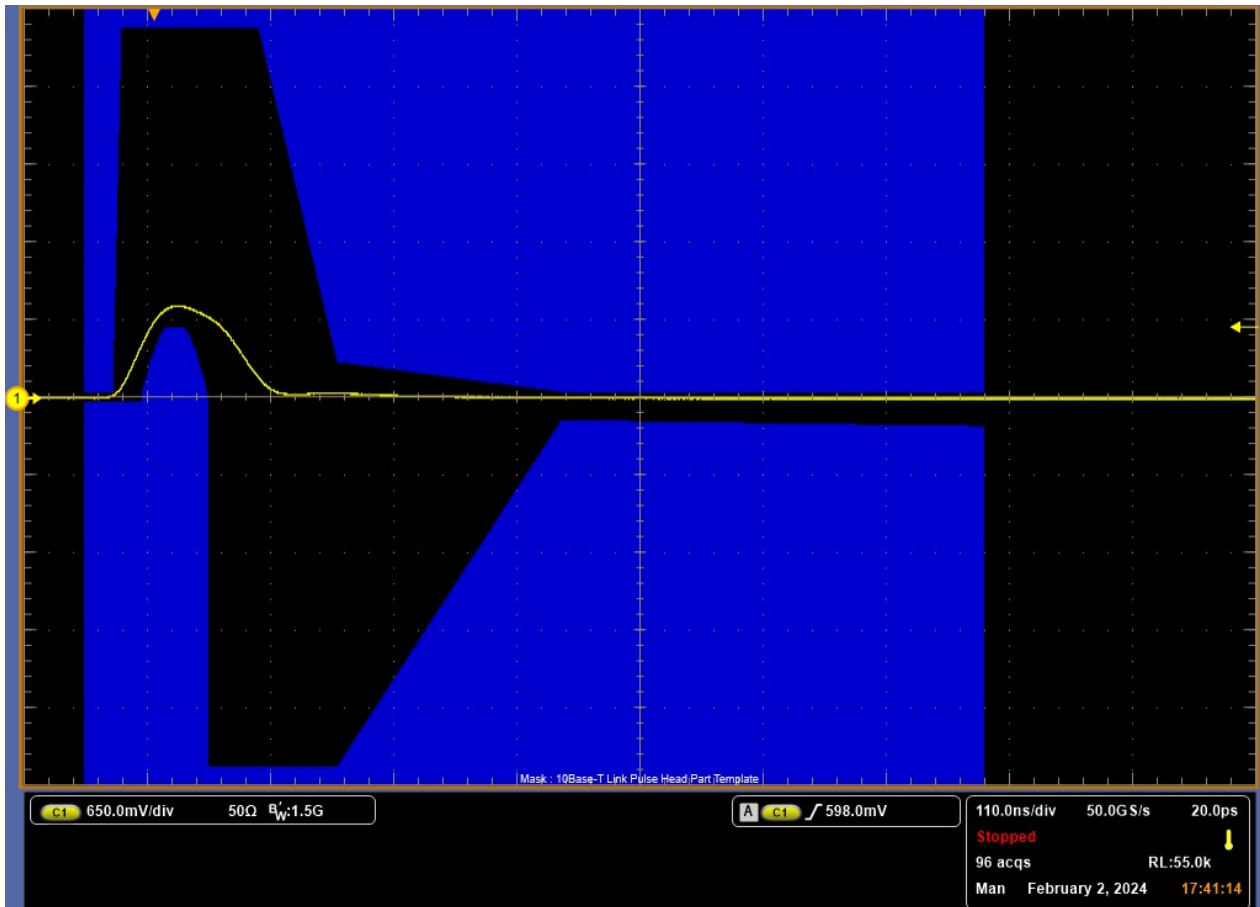
Link Pulse Timing Load2(TPM)

Link Pulse Timing Load2 With Twisted Pair cable \_Run1



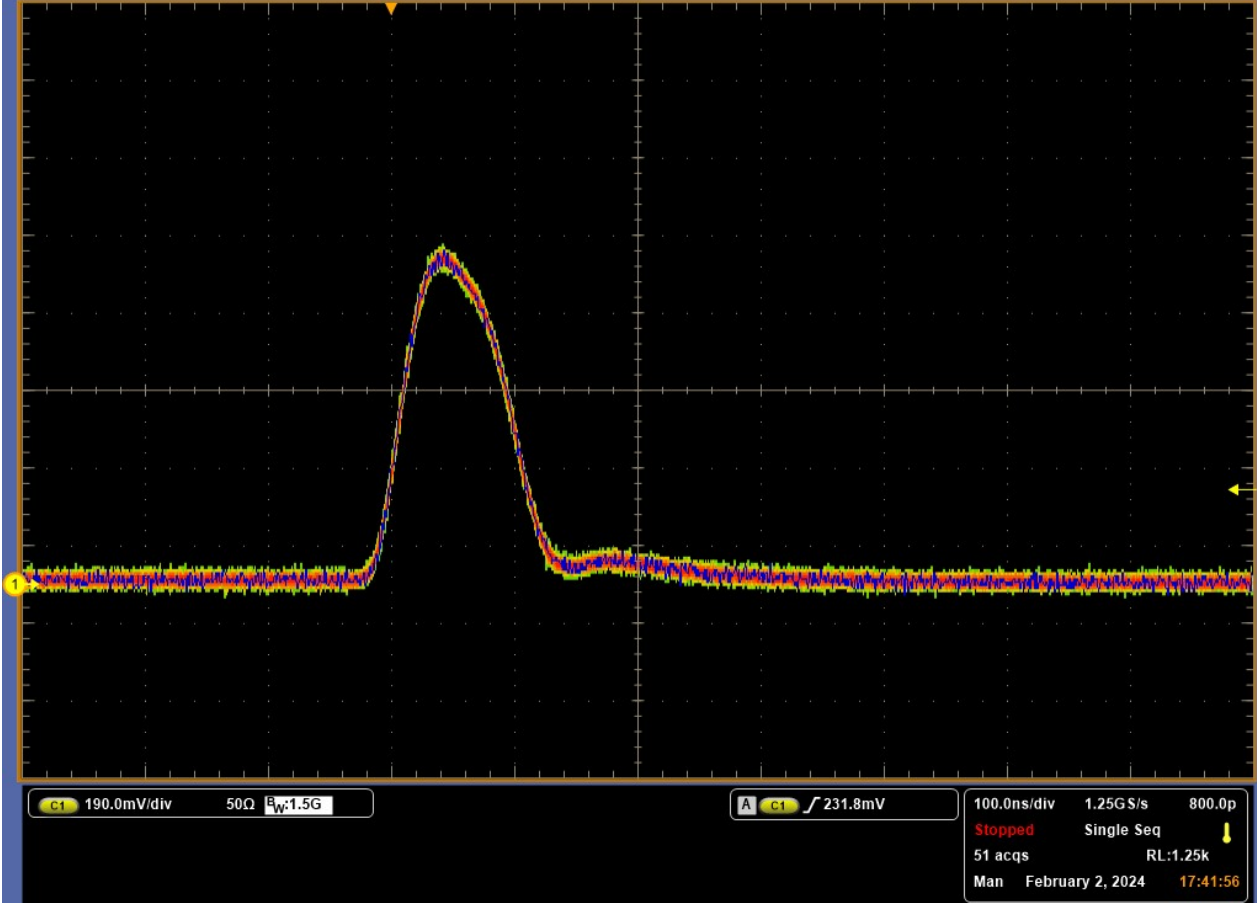
Link Pulse Load3(TPM)

[Link Pulse Load3 With Twisted Pair cable Head\\_Run1](#) [Link Pulse Load3 With Twisted Pair cable Tail\\_Run1](#)



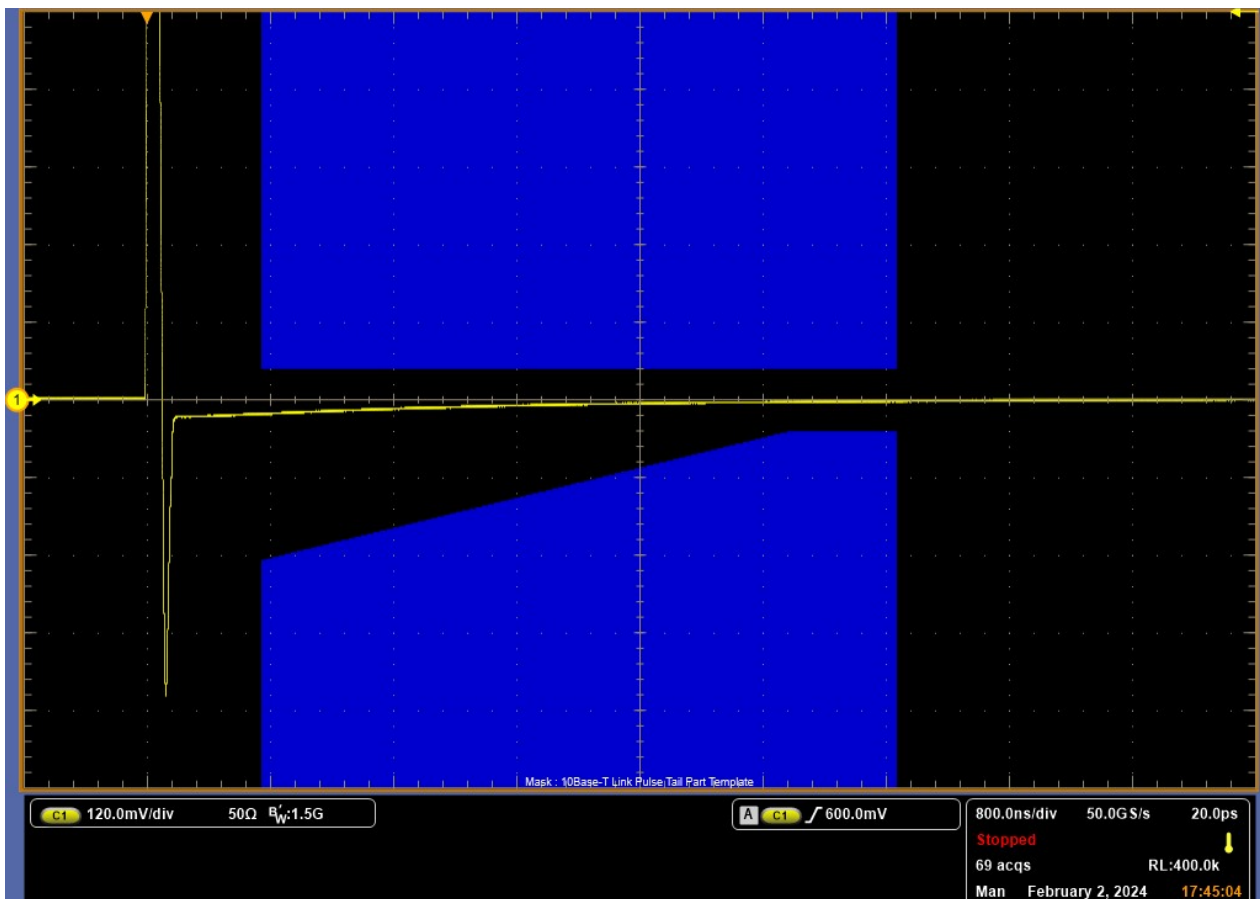
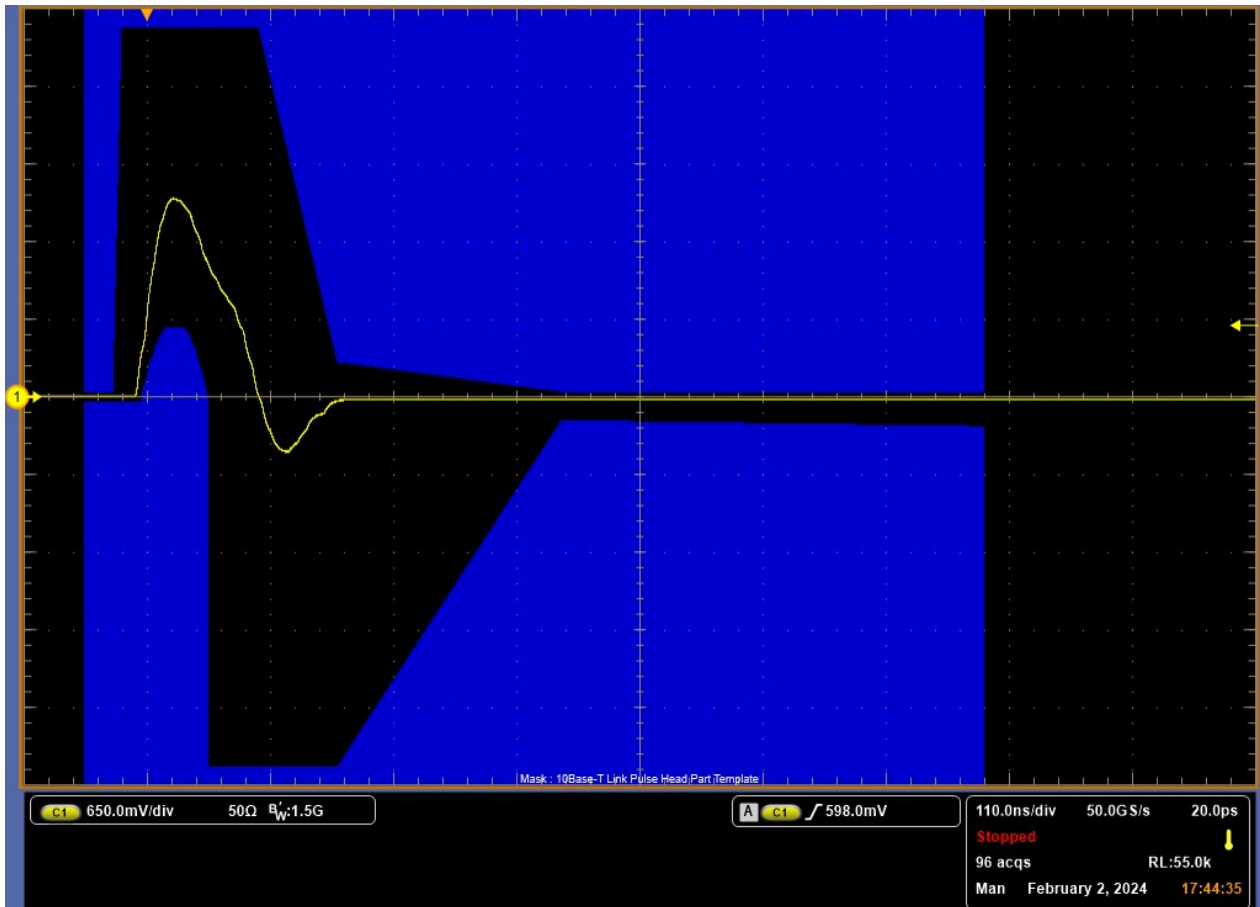
Link Pulse Timing Load3(TPM)

Link Pulse Timing Load3 With Twisted Pair cable \_Run1



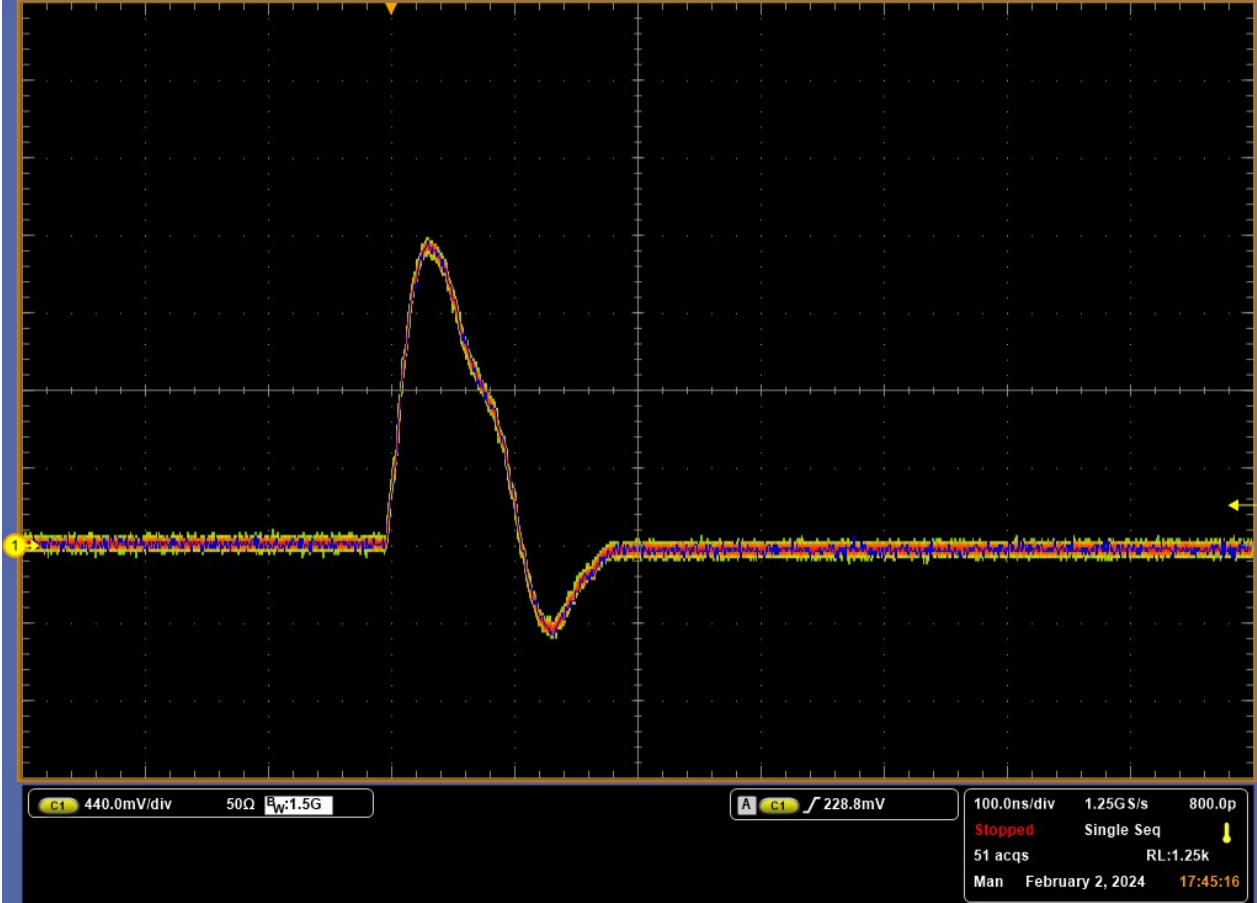
Link Pulse Load1

[Link Pulse Load1 Without Twisted Pair cable Head\\_Run1](#) [Link Pulse Load1 Without Twisted Pair cable Tail\\_Run1](#)



Link Pulse Timing Load1

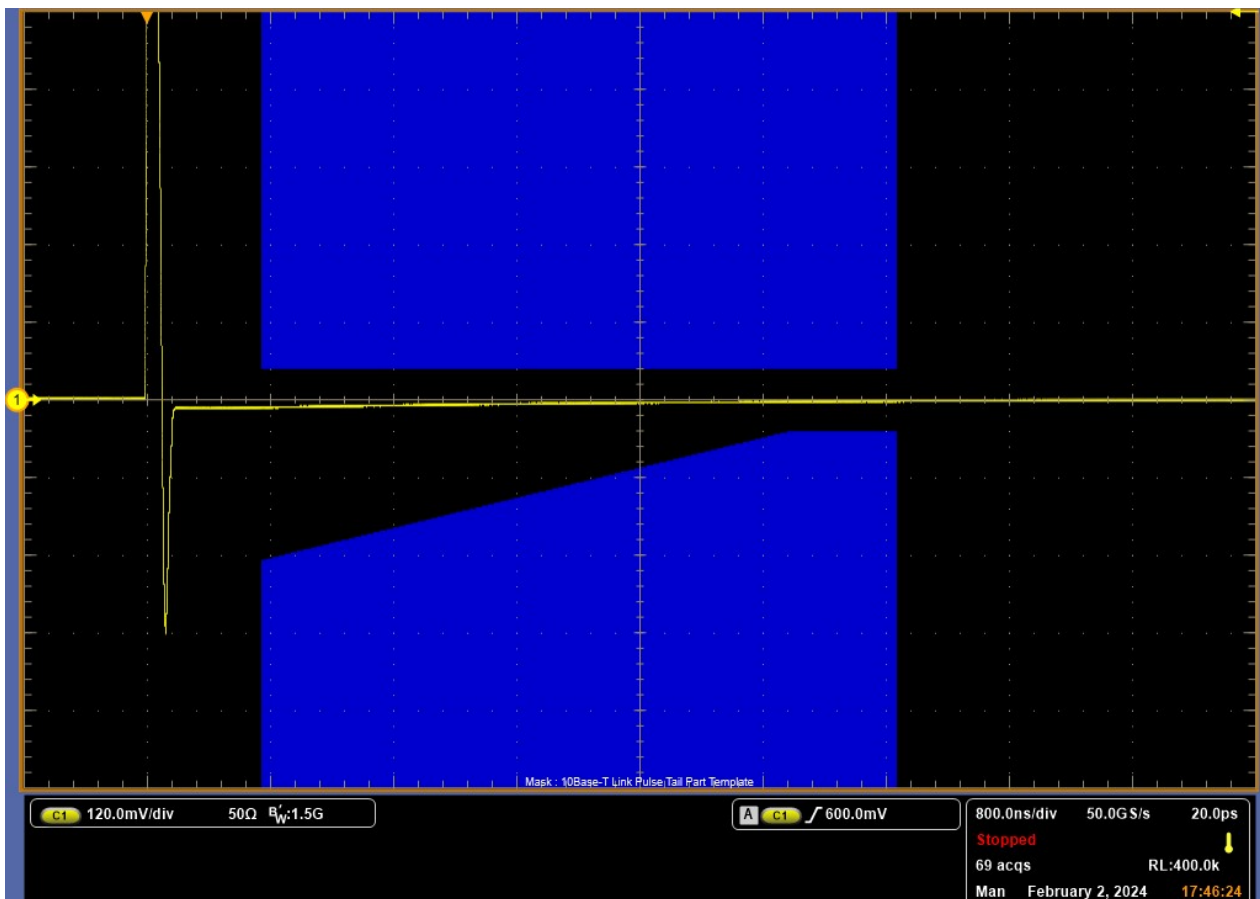
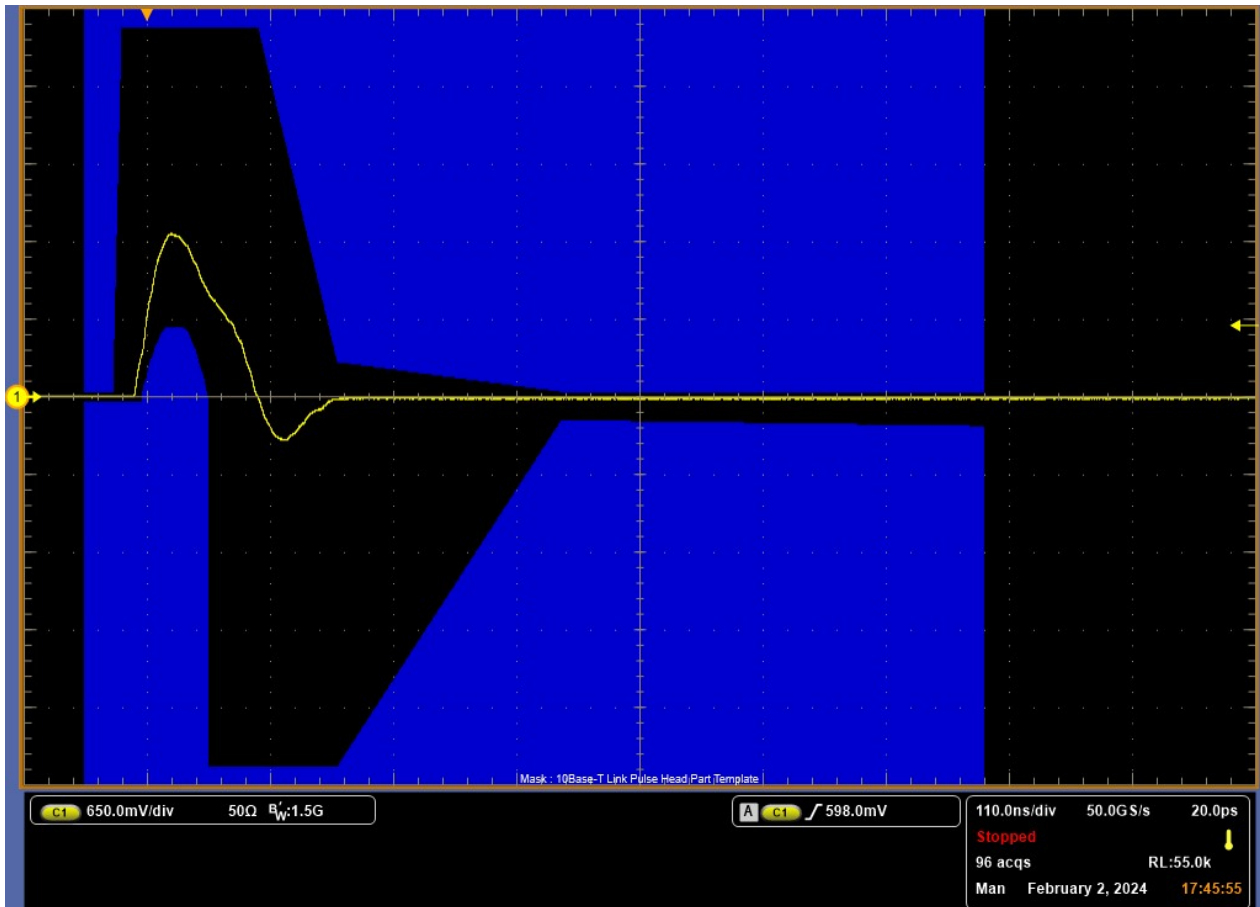
[Link Pulse Timing Load1 Without Twisted Pair cable \\_Run1](#)



Link Pulse Load2

[Link Pulse Load2 Without Twisted Pair cable Head\\_Run1](#) [Link Pulse Load2 Without Twisted Pair cable Tail\\_Run1](#)

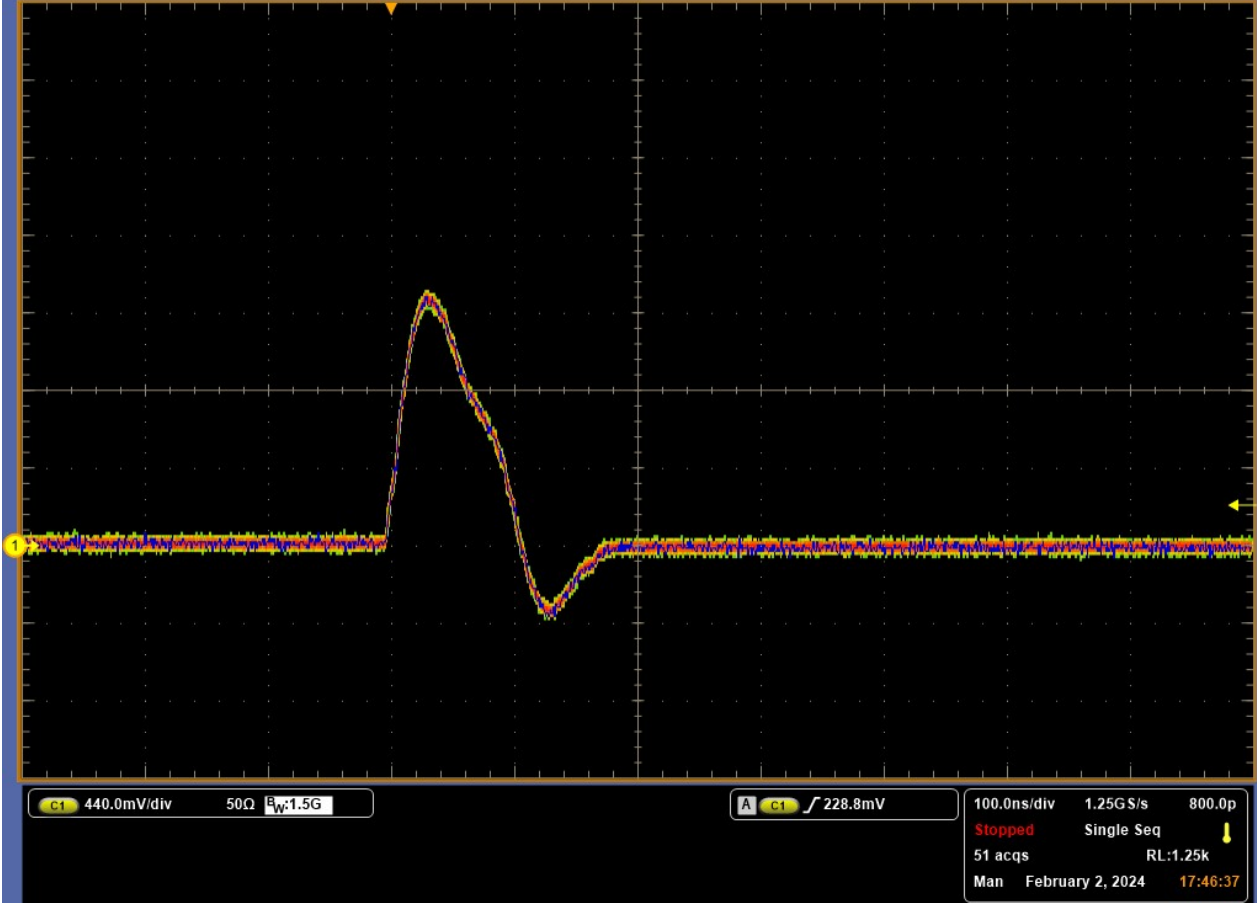






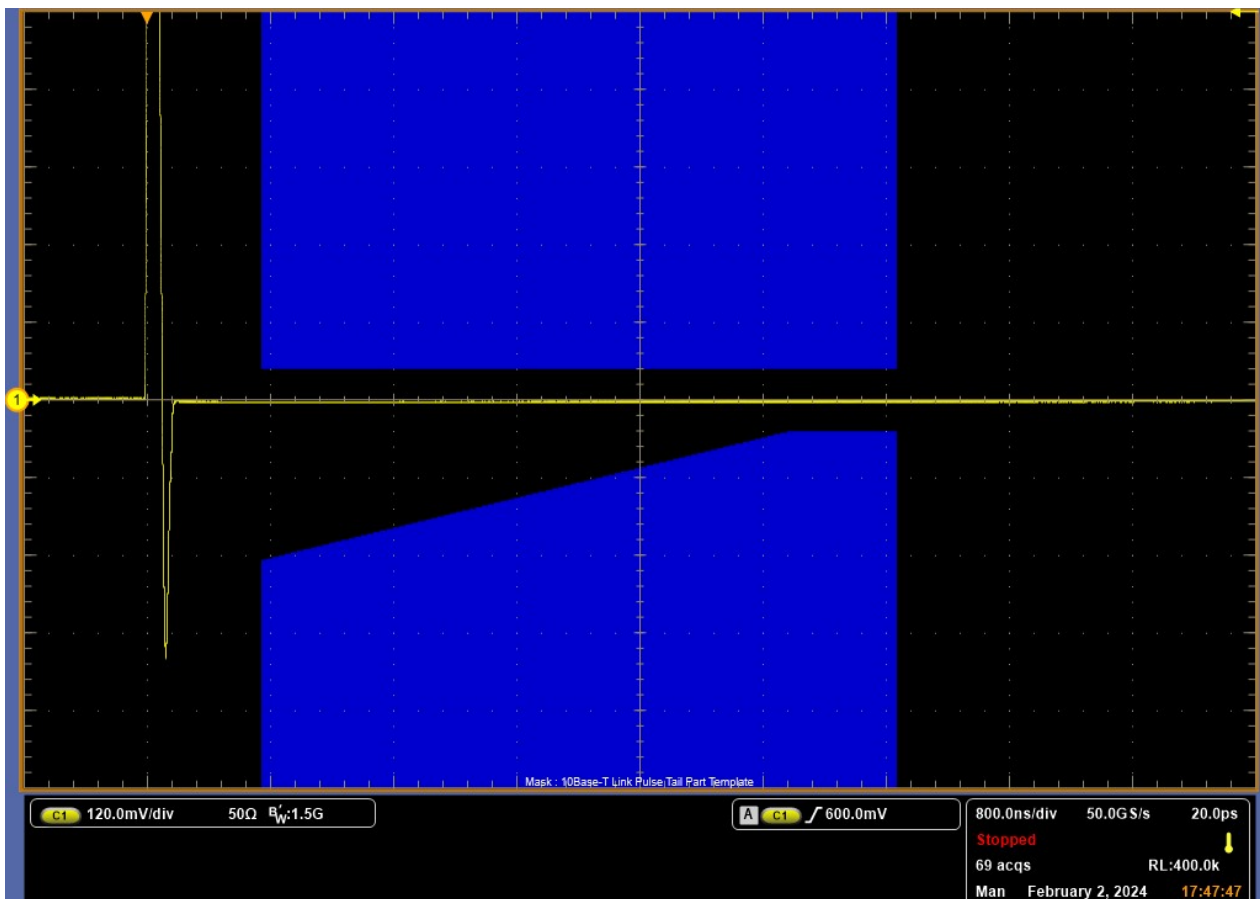
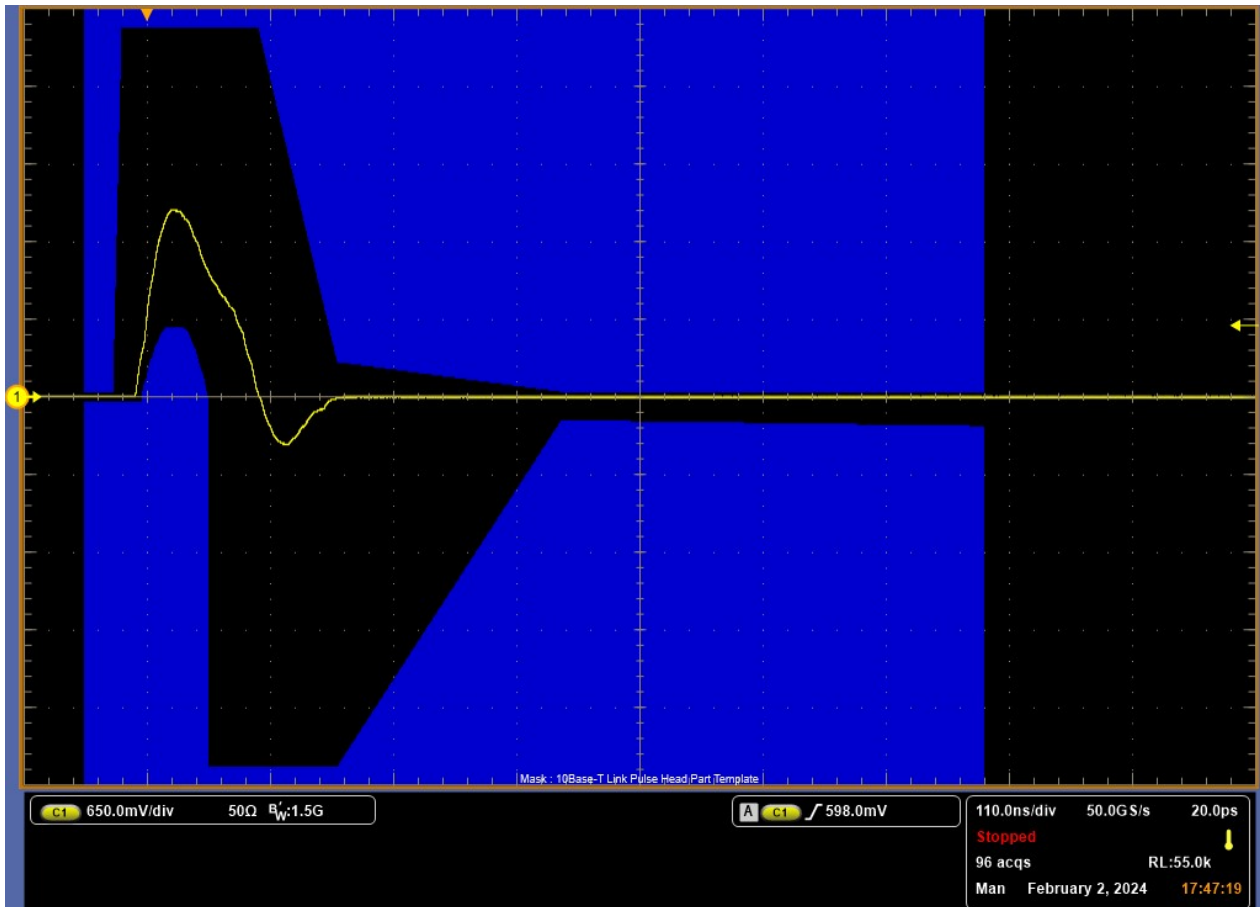
Link Pulse Timing Load2

[Link Pulse Timing Load2 Without Twisted Pair cable \\_Run1](#)



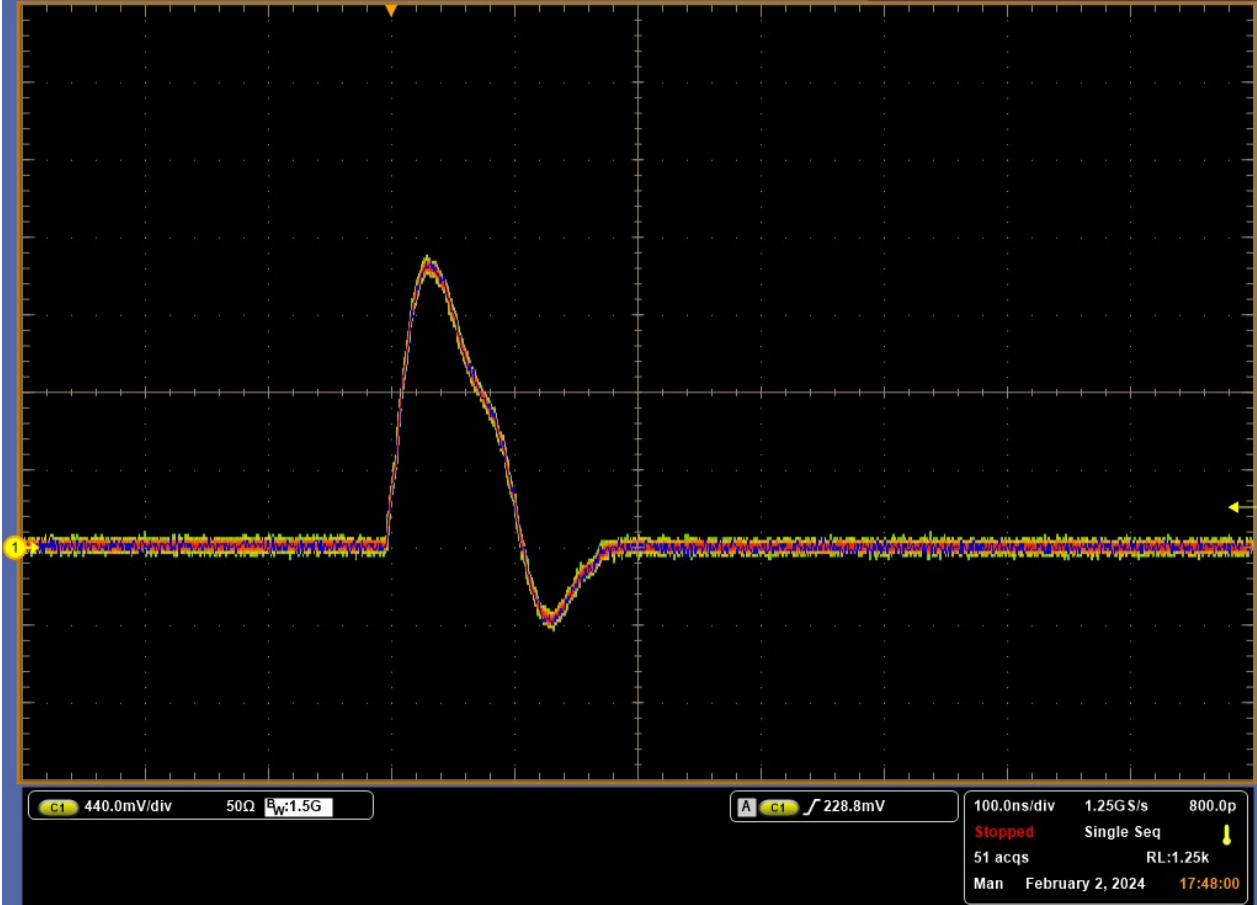
Link Pulse Load3

[Link Pulse Load3 Without Twisted Pair cable Head\\_Run1](#) [Link Pulse Load3 Without Twisted Pair cable Tail\\_Run1](#)



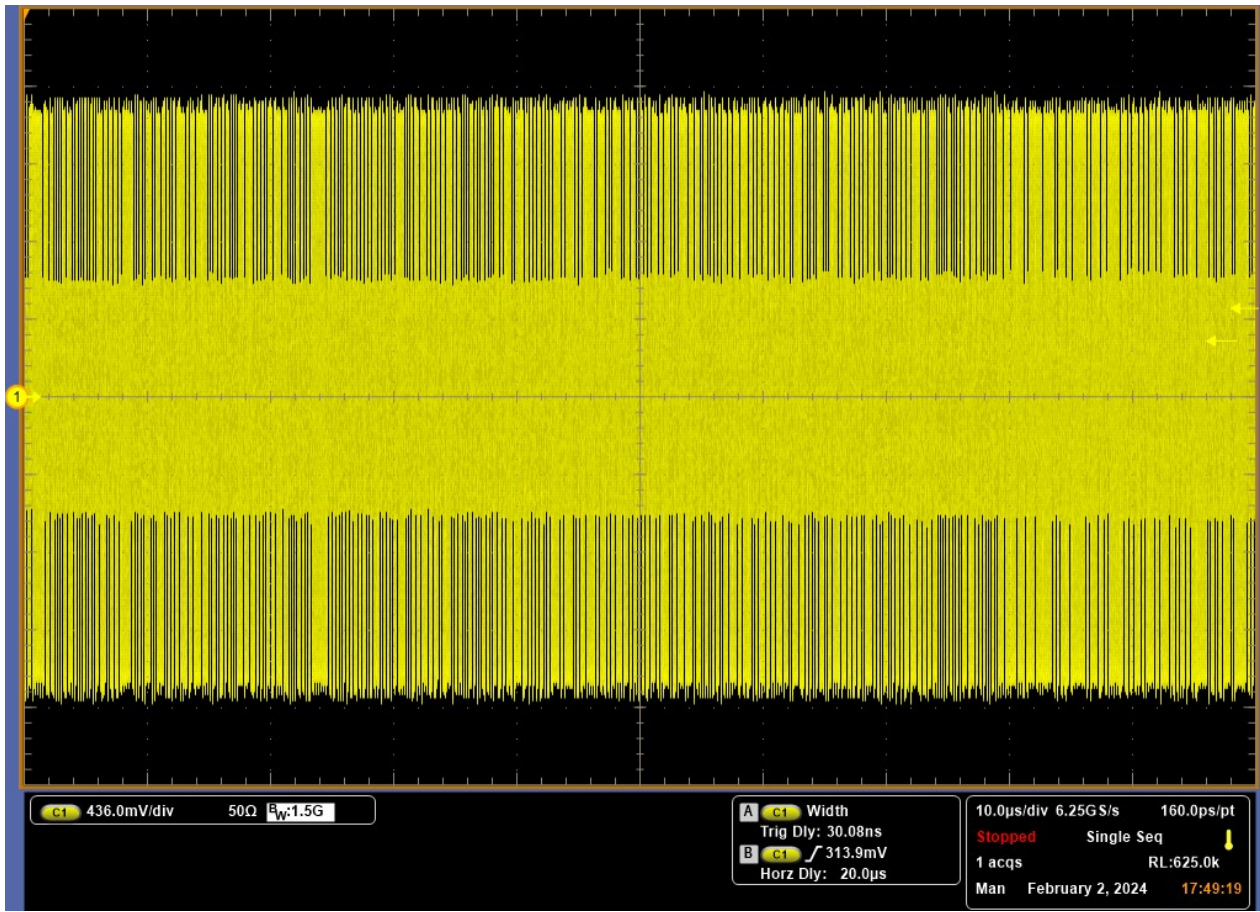
Link Pulse Timing Load3

Link Pulse Timing Load3 Without Twisted Pair cable \_Run1



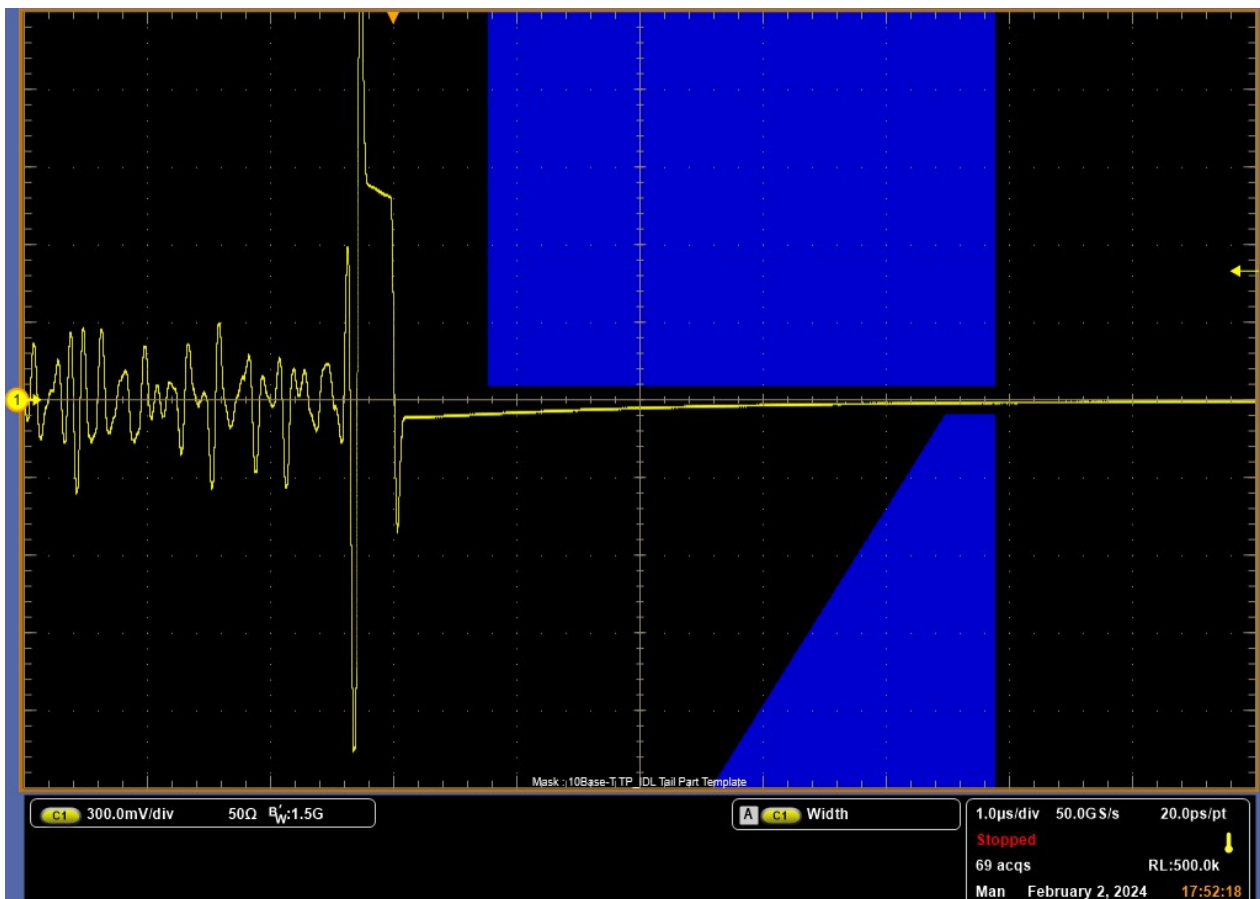
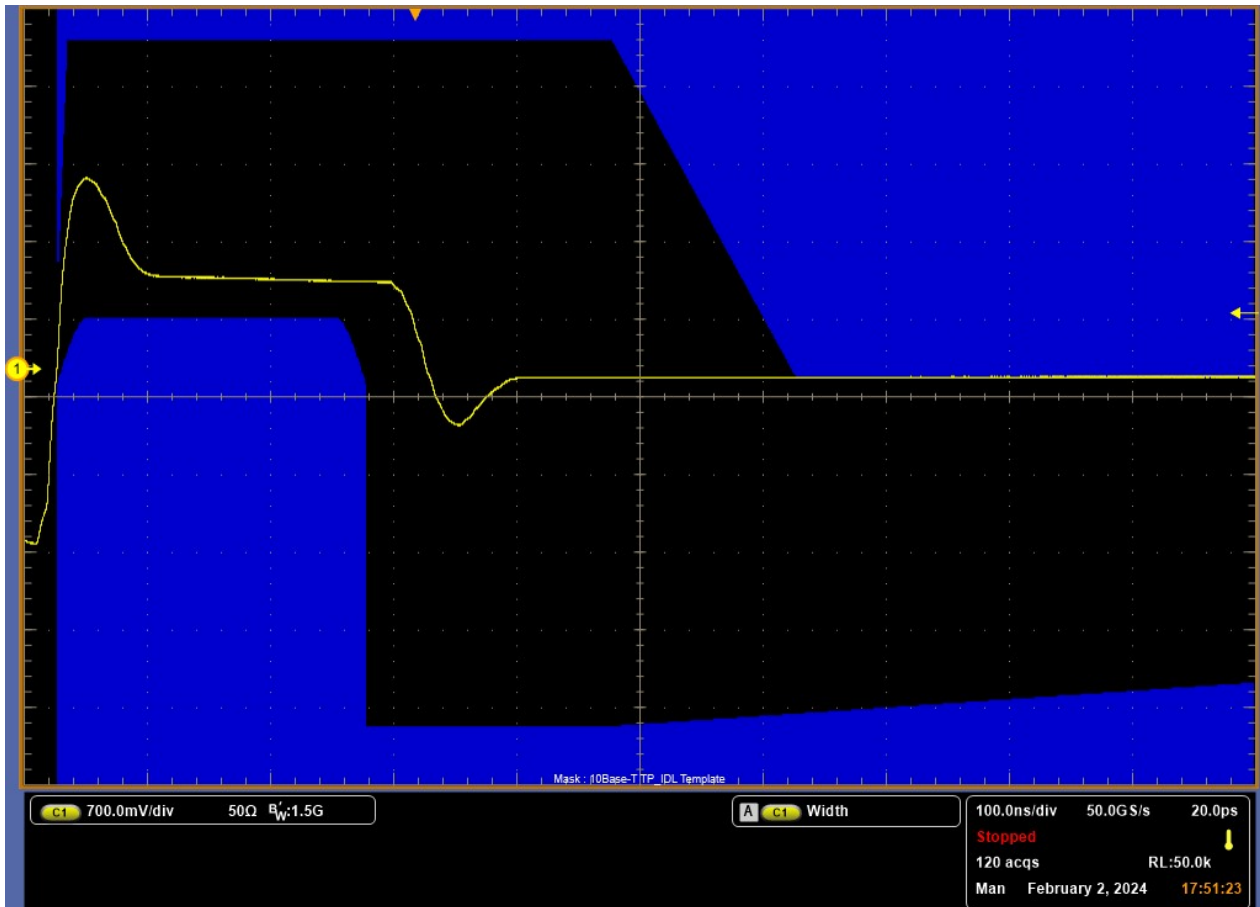
Differential Voltage

Differential Voltage \_Run1



TP\_IDL Load1

[TP\\_IDL Load1 Without Twisted Pair cable Head1](#) [TP\\_IDL Load1 Without Twisted Pair cable Tail1](#)



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TP\_IDL Load2

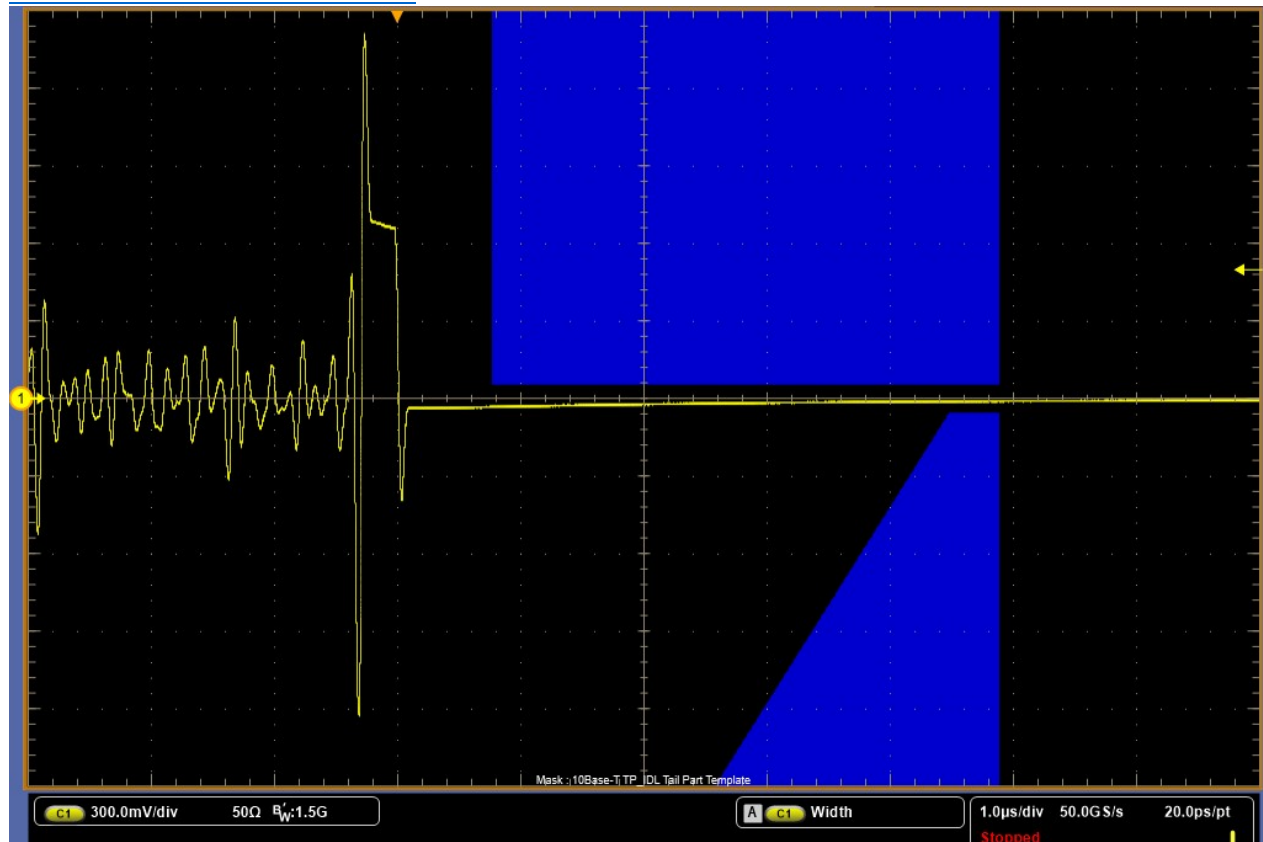
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TP\_IDL Load2 Without Twisted Pair cable Head1



TP\_IDL Load2 Without Twisted Pair cable Tail



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TP\_IDL Load3

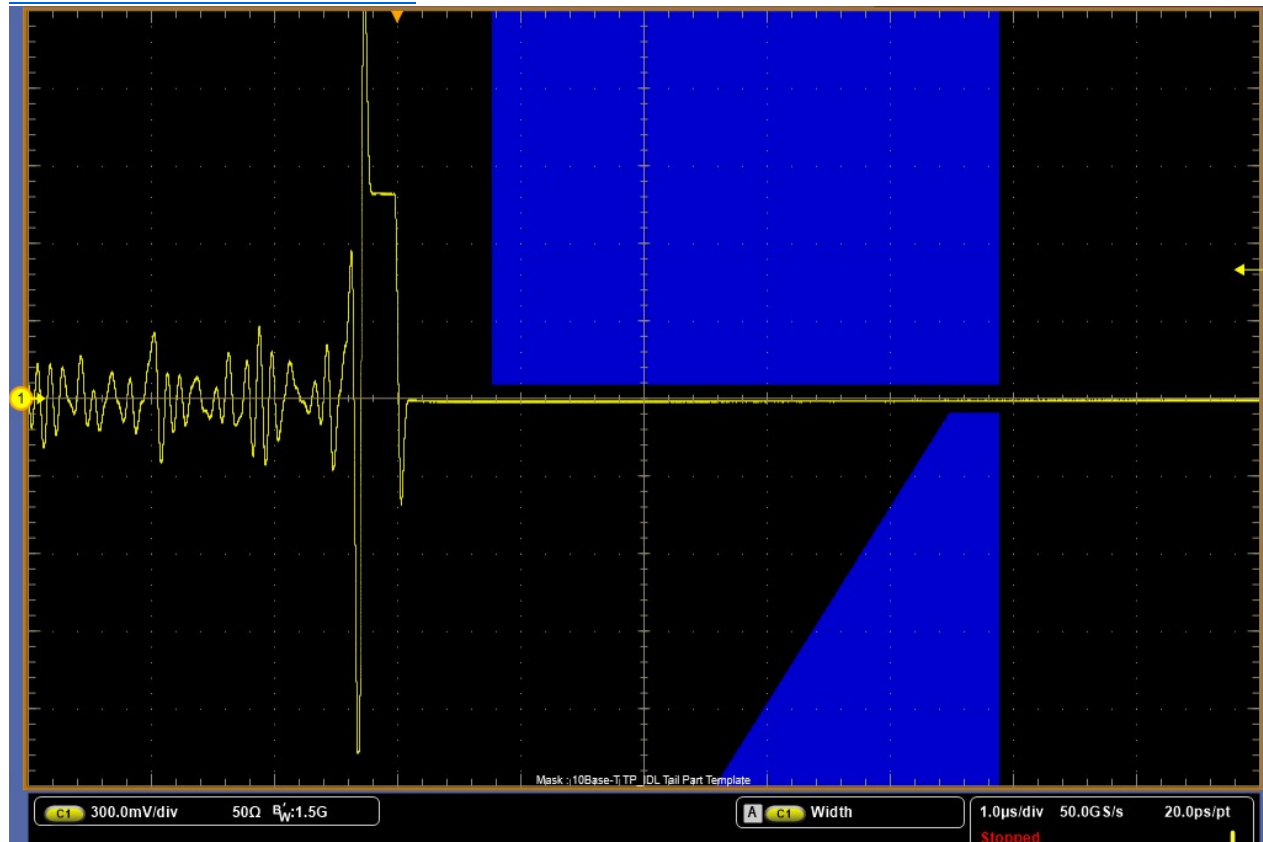
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TP\_IDL Load3 Without Twisted Pair cable Head1



TP\_IDL Load3 Without Twisted Pair cable Tail



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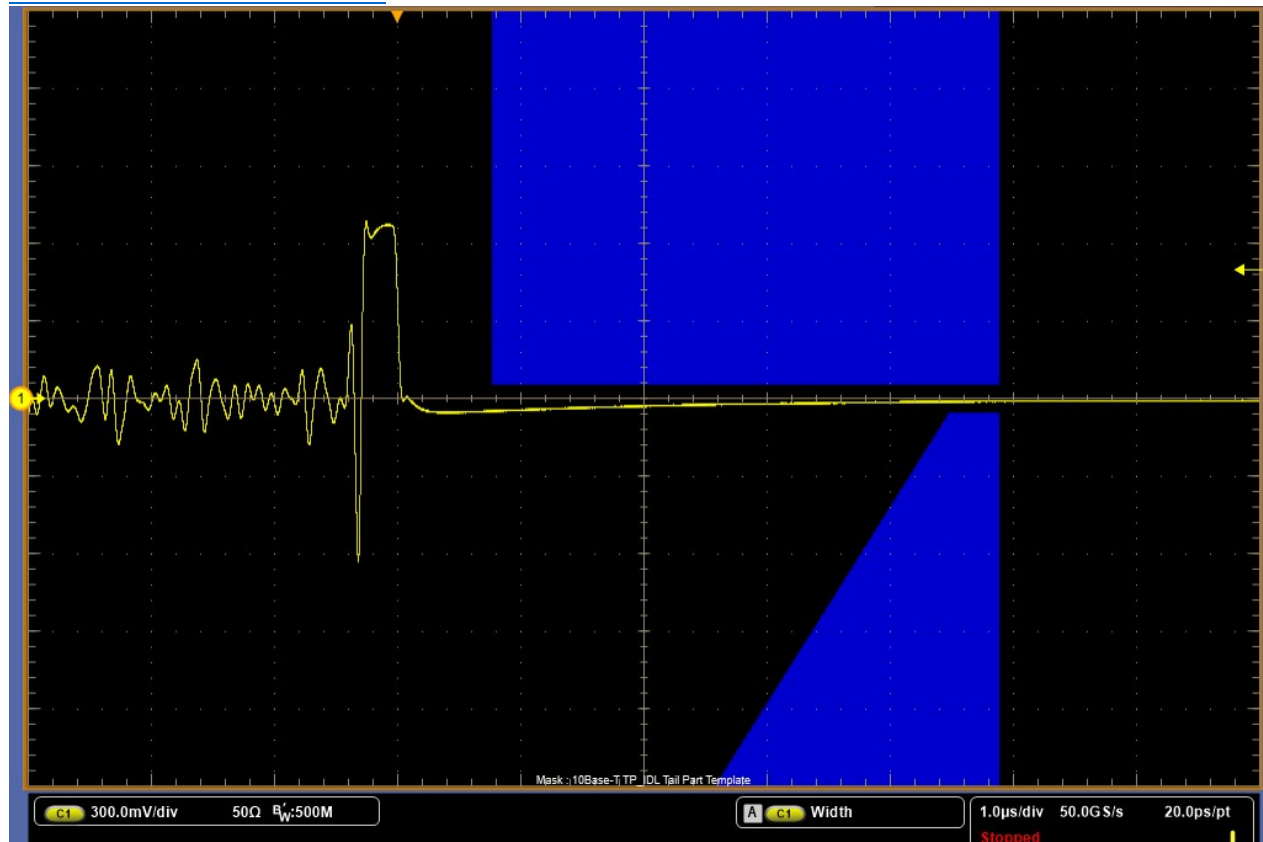
TP\_IDL Load1(TPM)

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TP\_IDL Load1 With Twisted Pair cable Head



TP\_IDL Load1 With Twisted Pair cable Tail

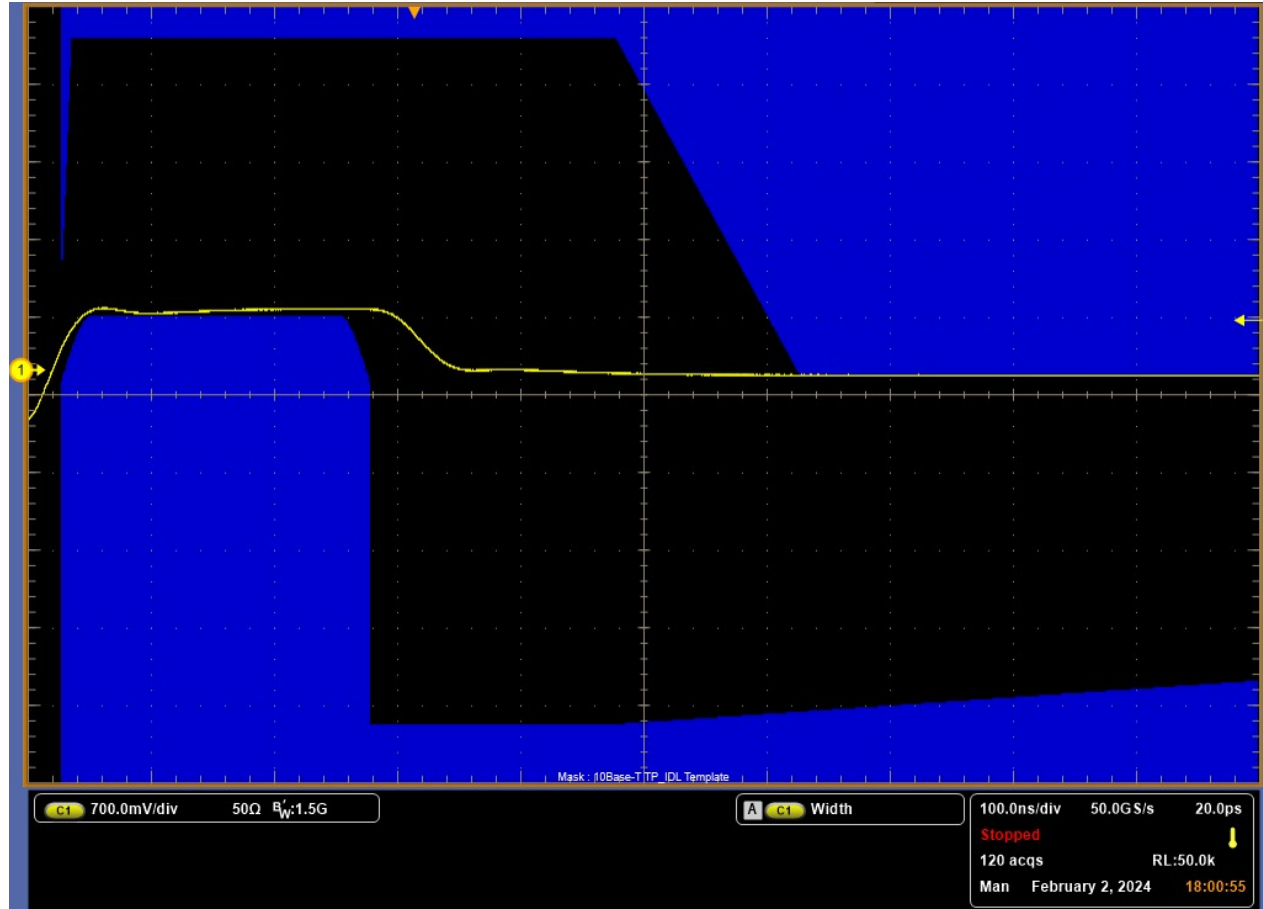


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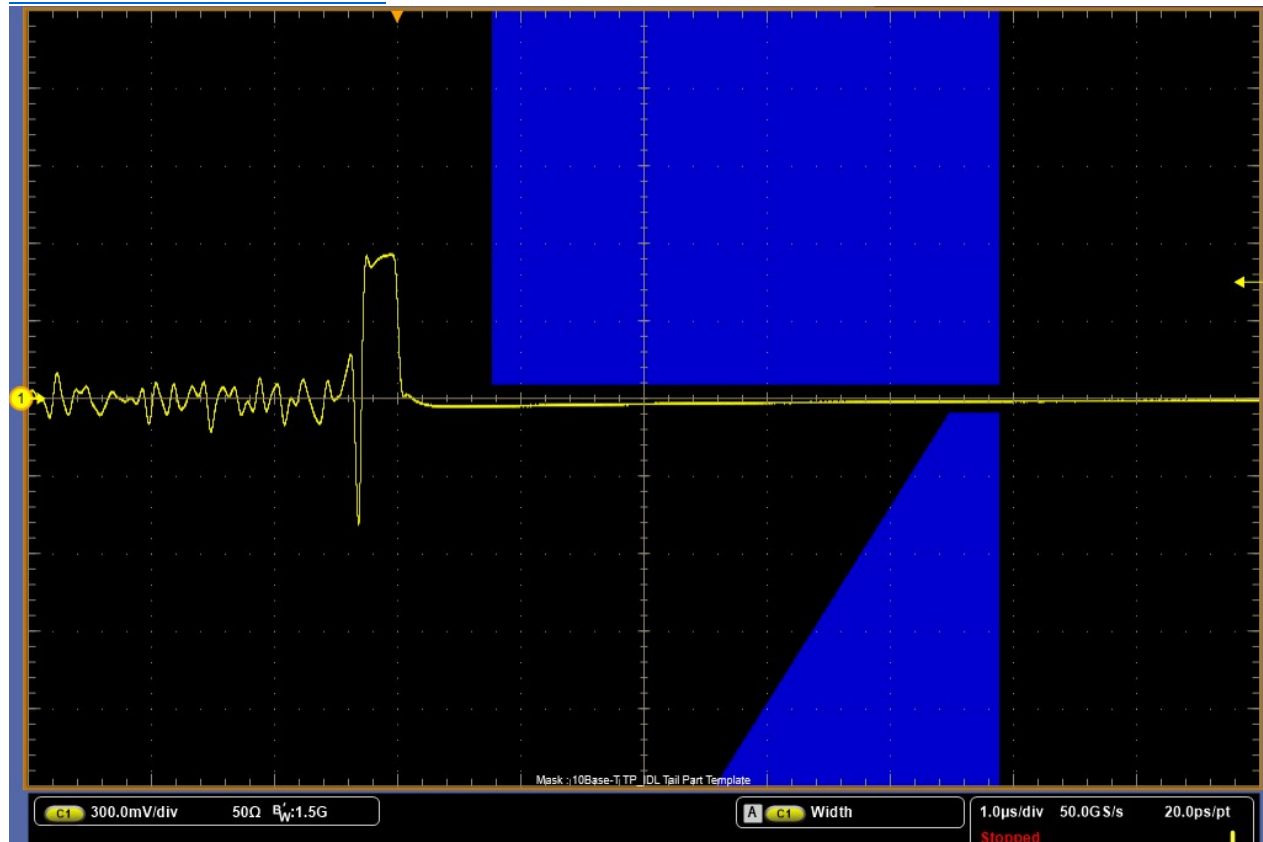
TP\_IDL Load2(TPM)

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TP\_IDL Load2 With Twisted Pair cable Head1



TP\_IDL Load2 With Twisted Pair cable Tail1

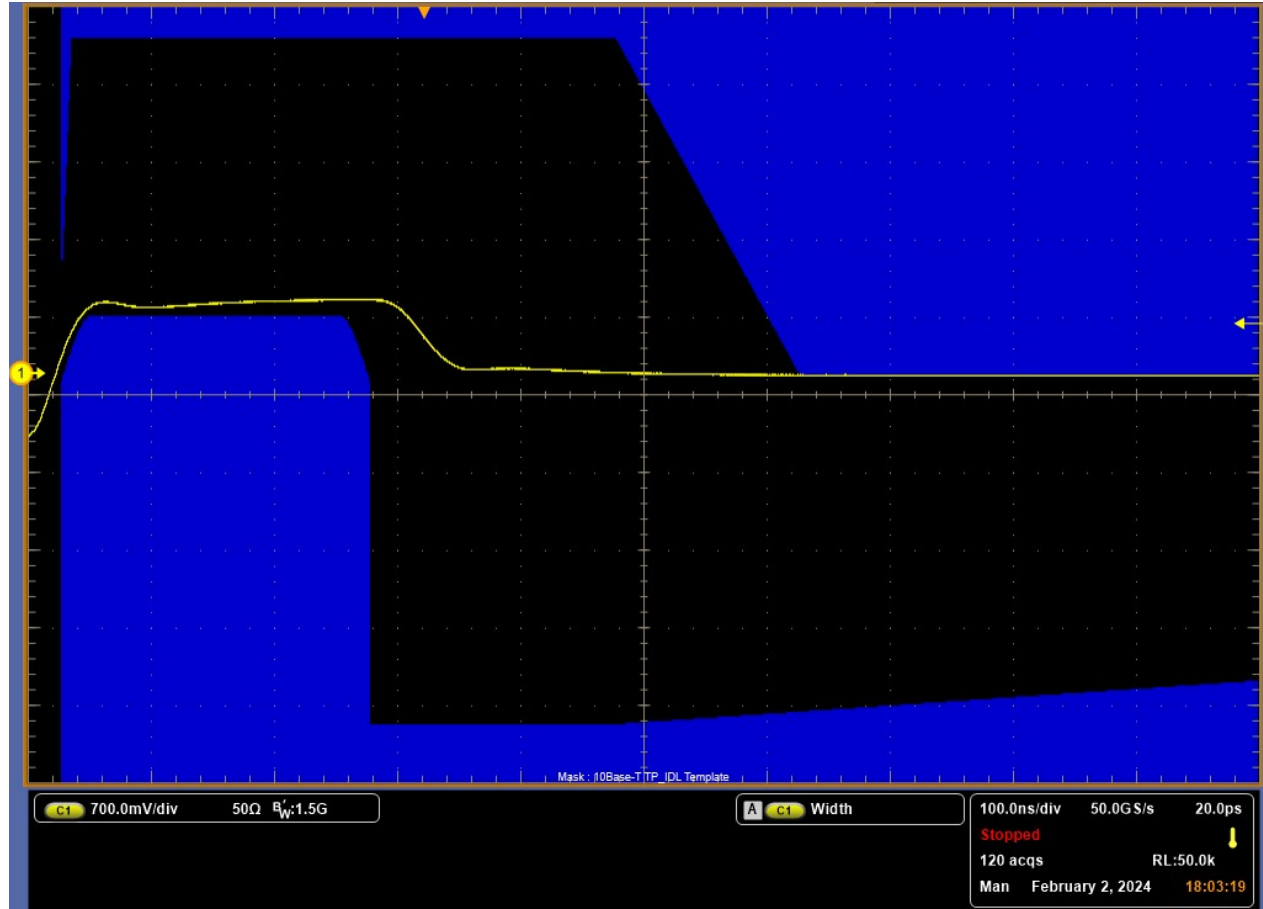


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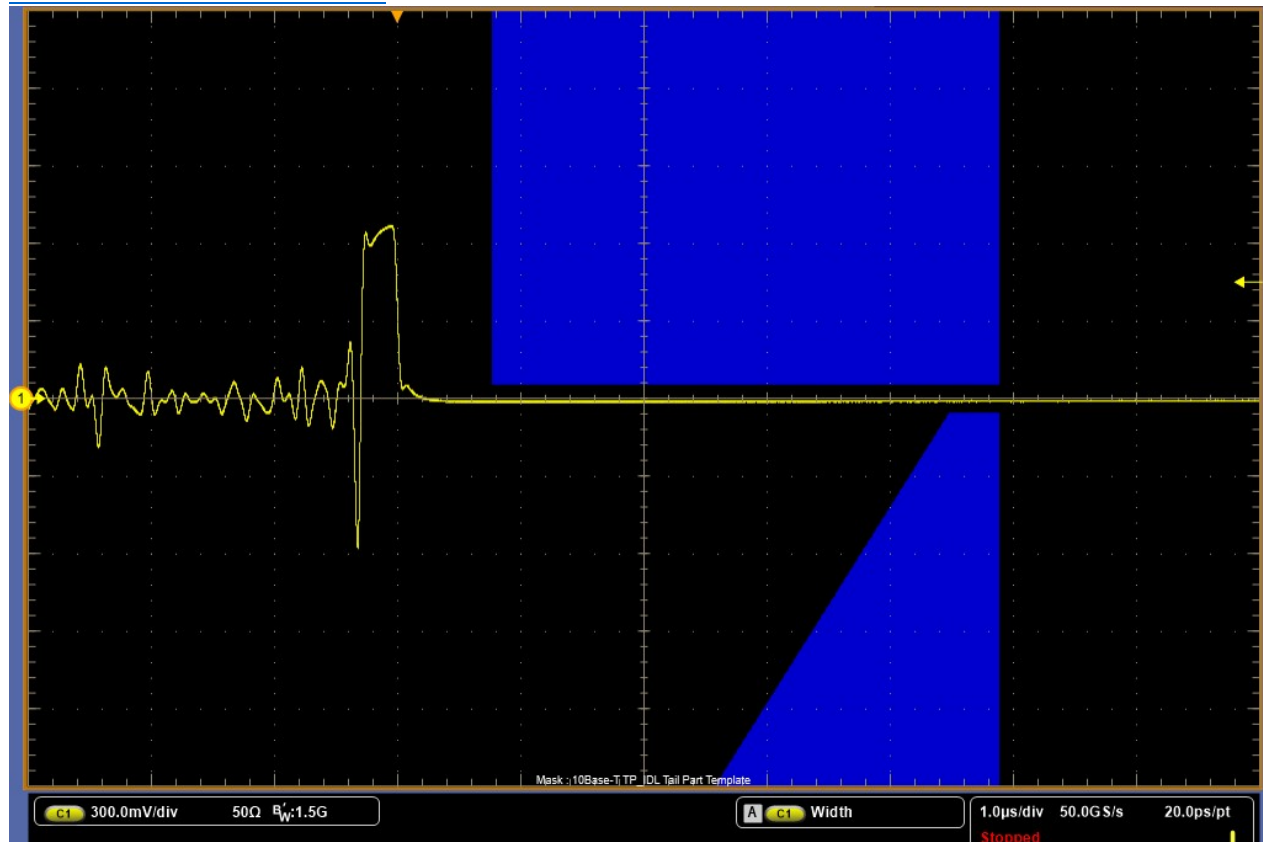
TP\_IDL Load3(TPM)

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TP\_IDL Load3 With Twisted Pair cable Head1

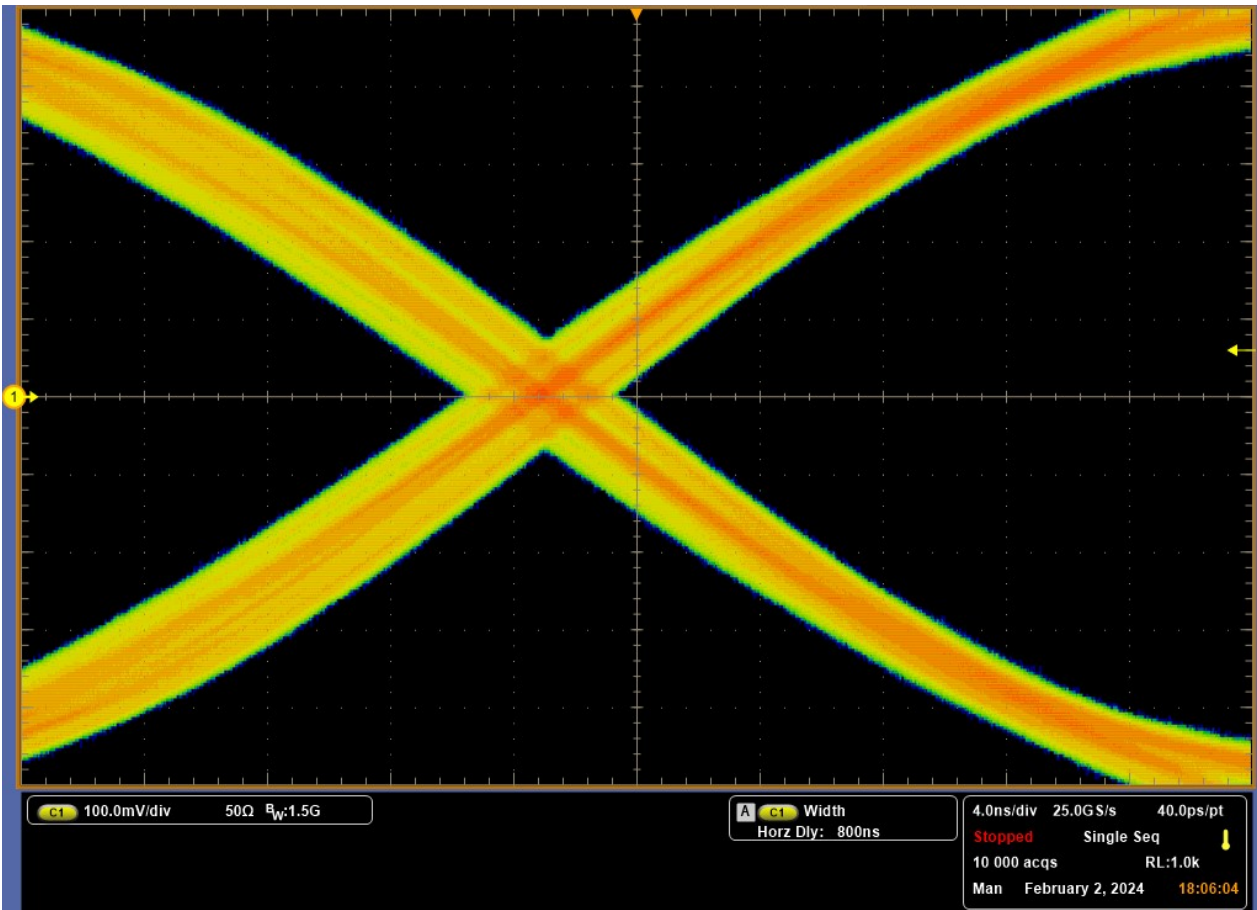


TP\_IDL Load3 With Twisted Pair cable Tail1



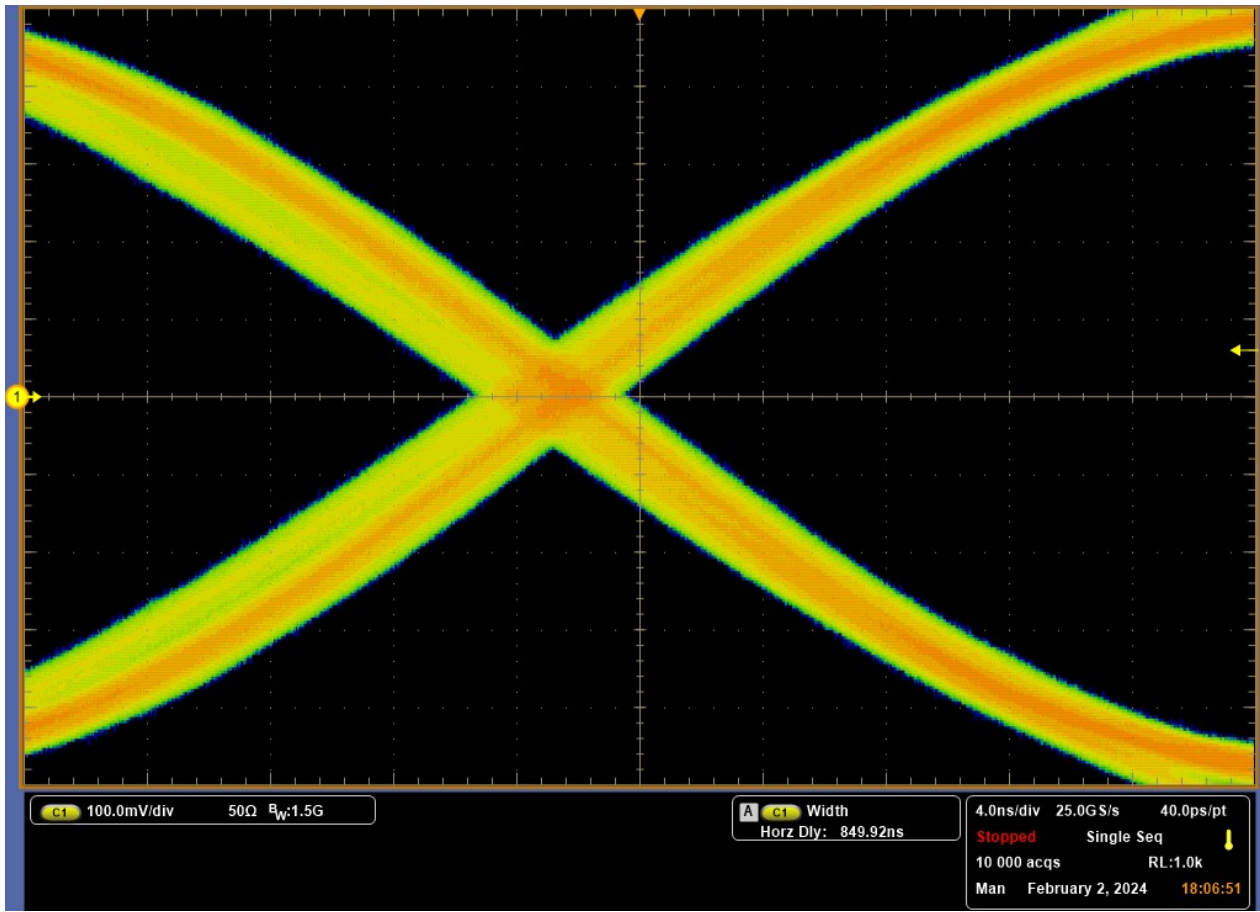






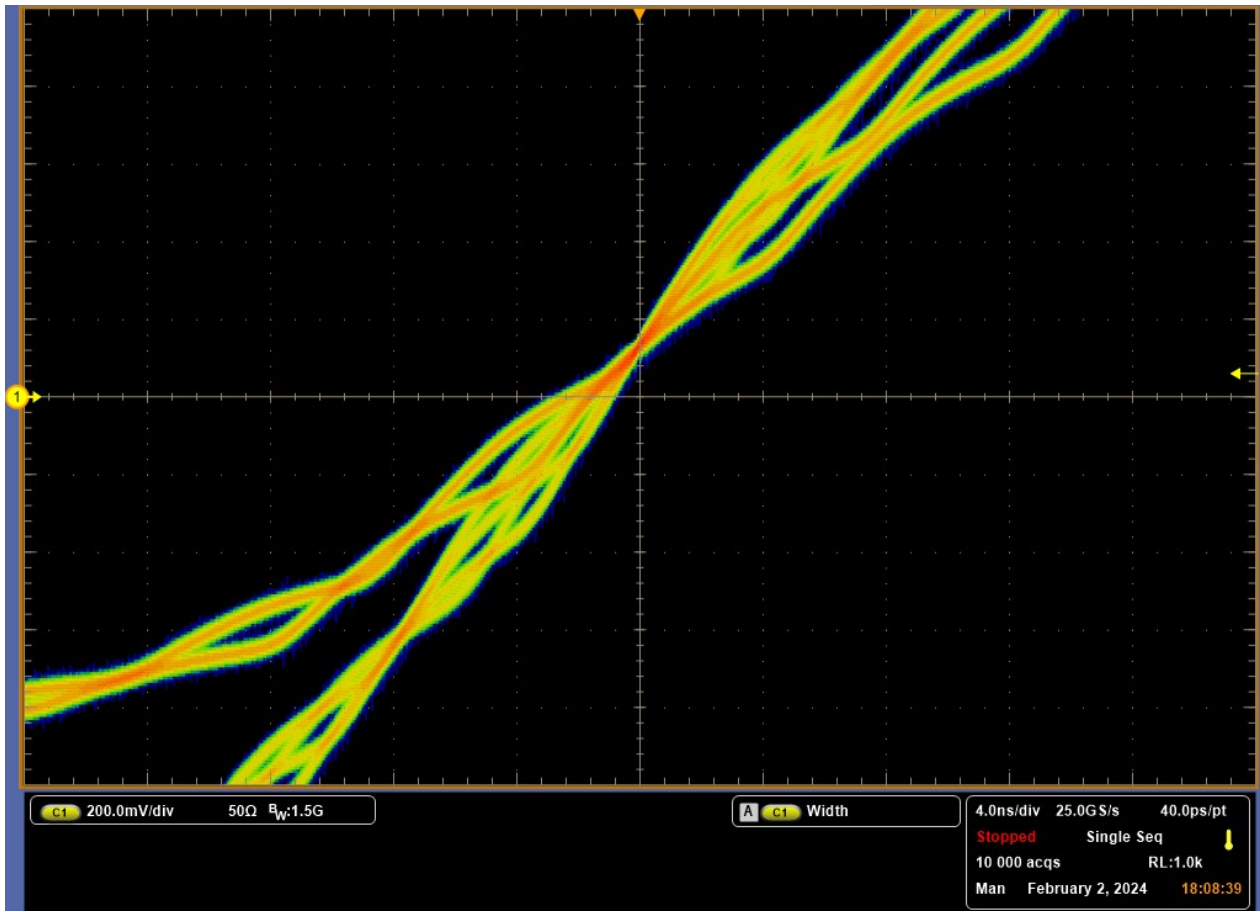
Jitter 8.5(TPM)

[Jitter 8.5 With Twisted Pair Cable \\_Run1](#)



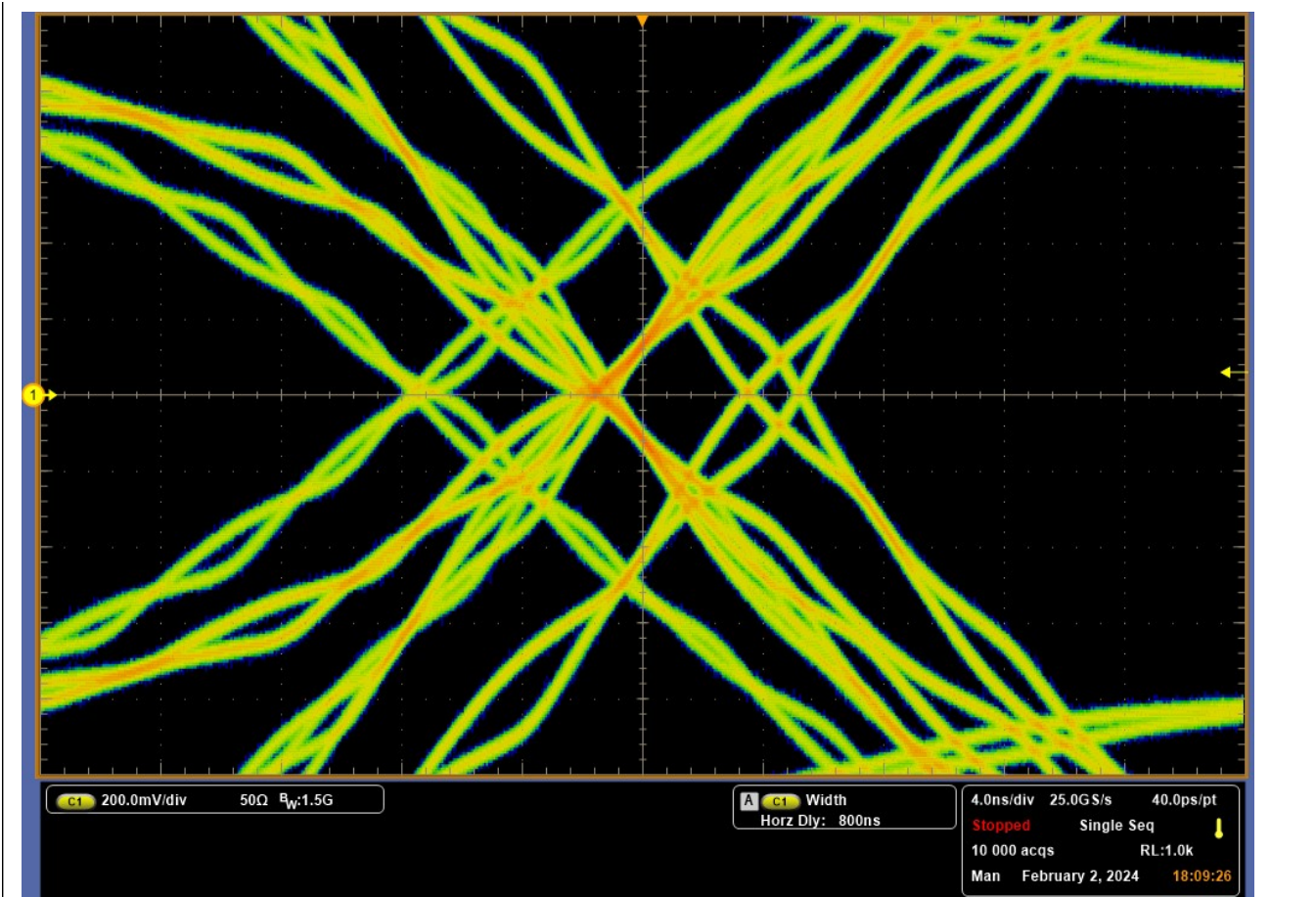
Jitter Normal

[Jitter Normal Without Twisted Pair Cable \\_Run1](#)



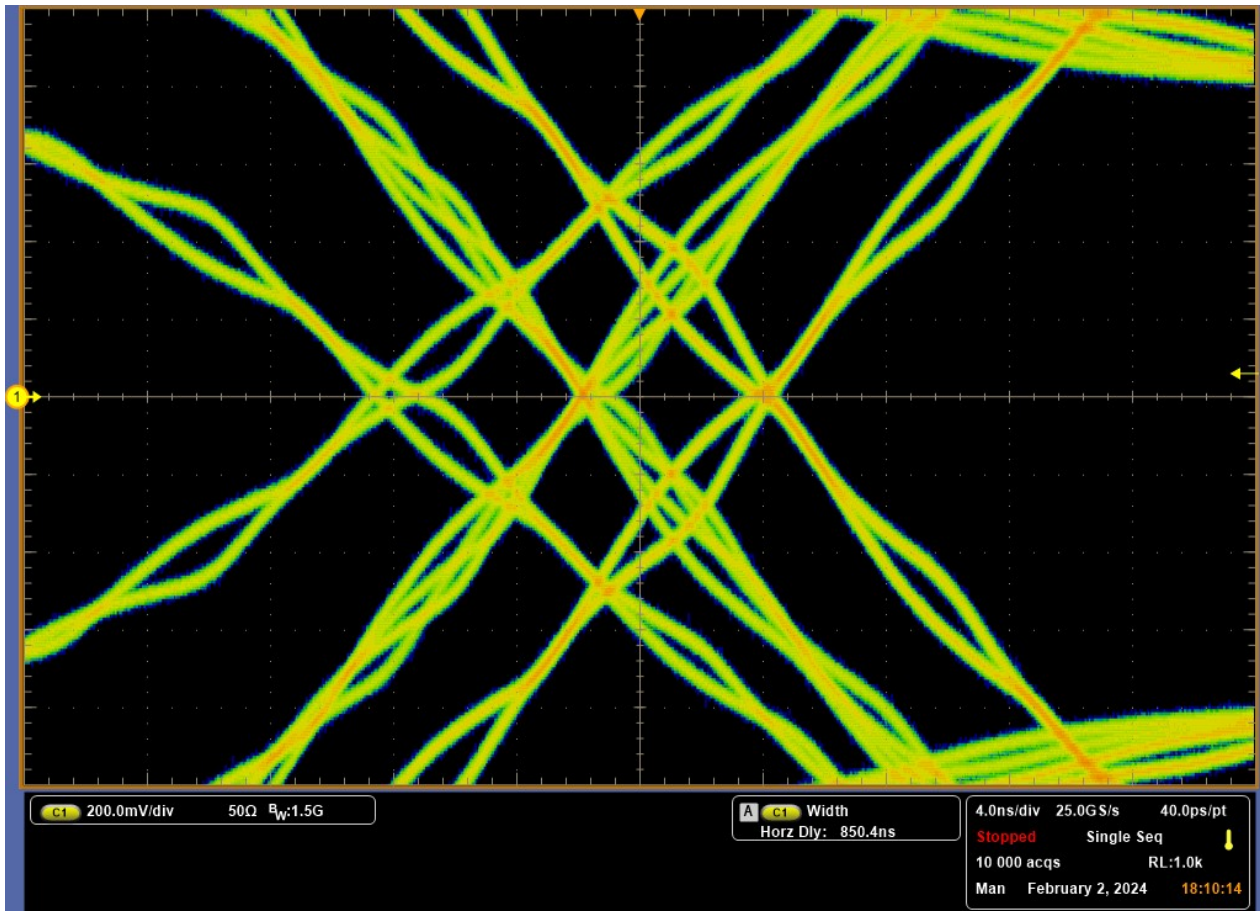
Jitter 8.0

[Jitter 8.0 Without Twisted Pair Cable \\_Run1](#)



Jitter 8.5

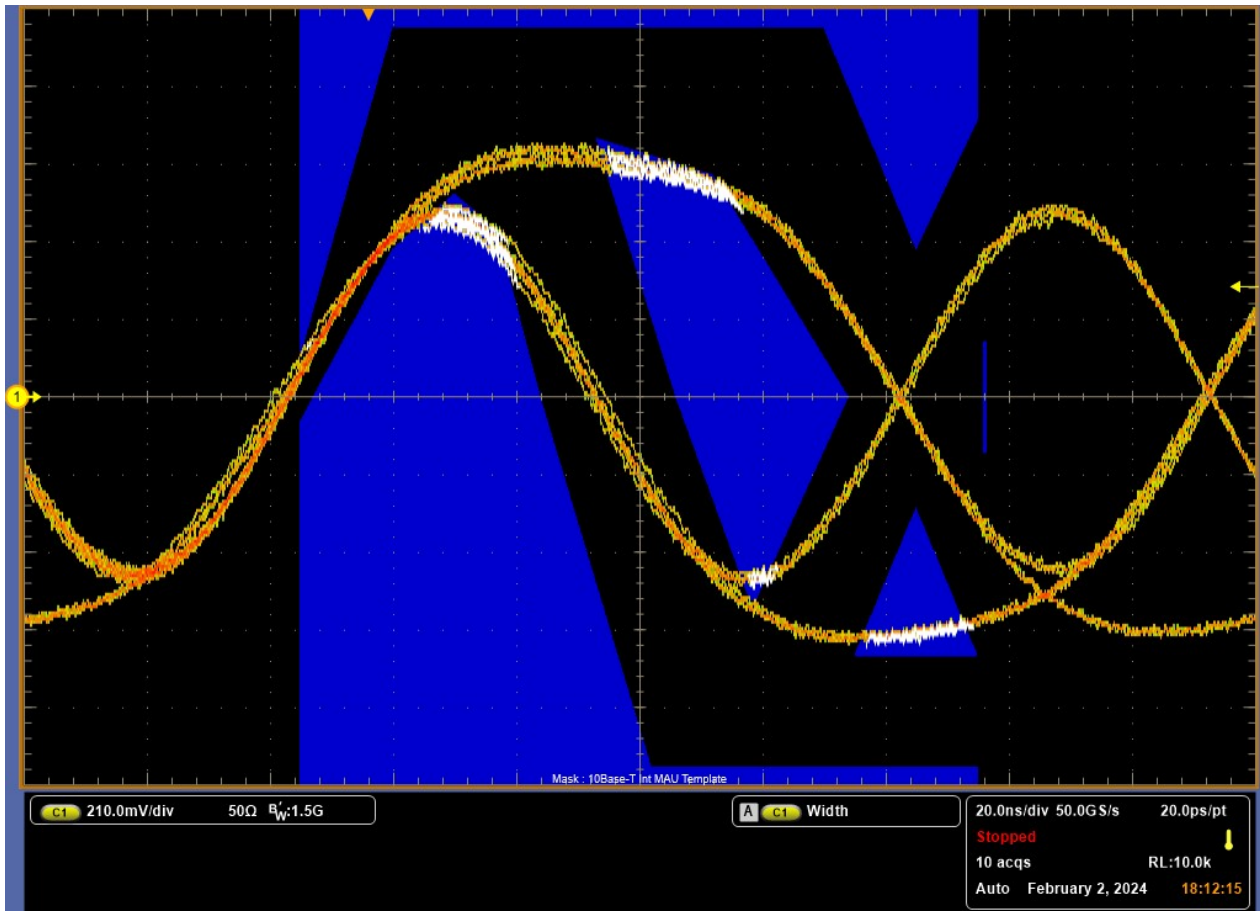
[Jitter 8.5 Without Twisted Pair Cable \\_Run1](#)



MAU Internal

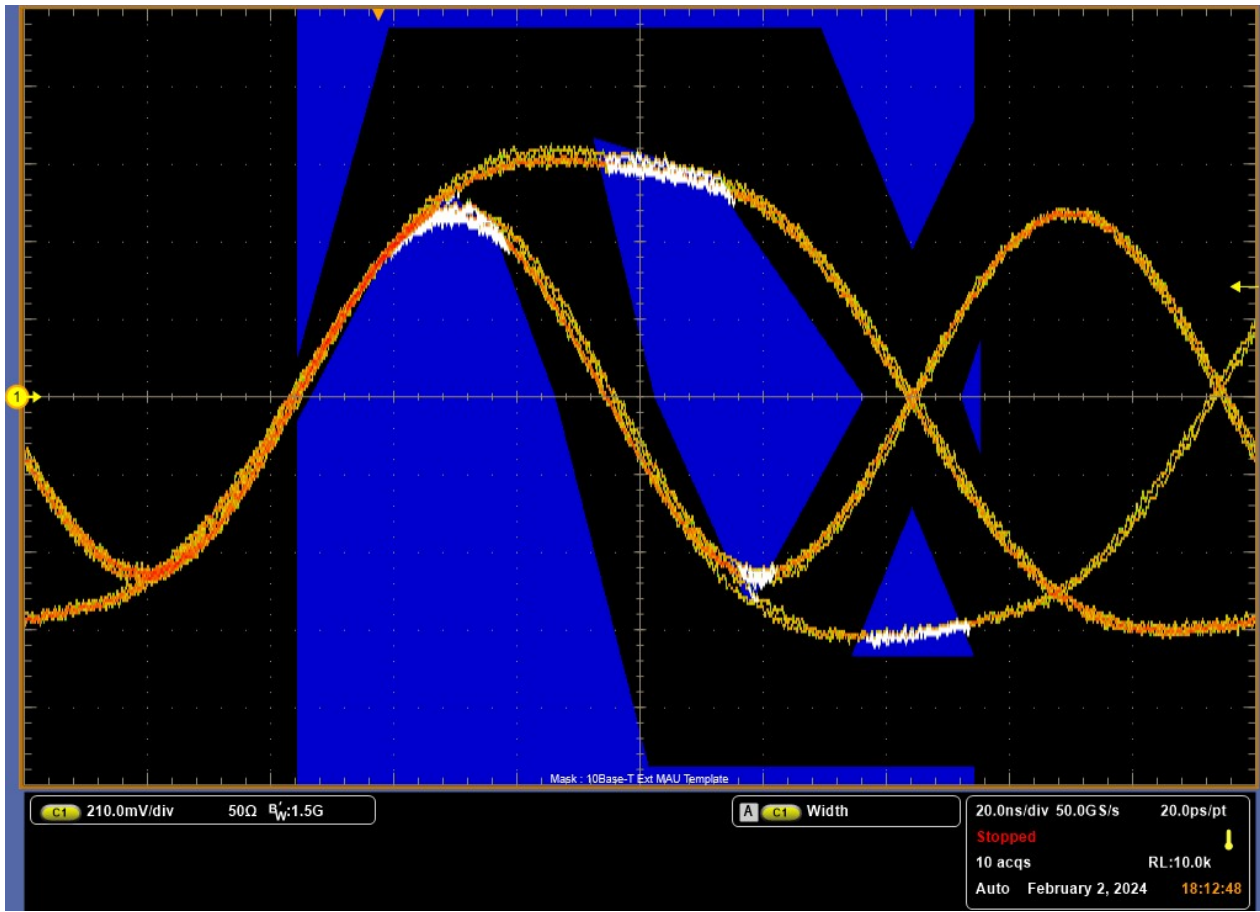
[MAU Internal\\_Run1](#)





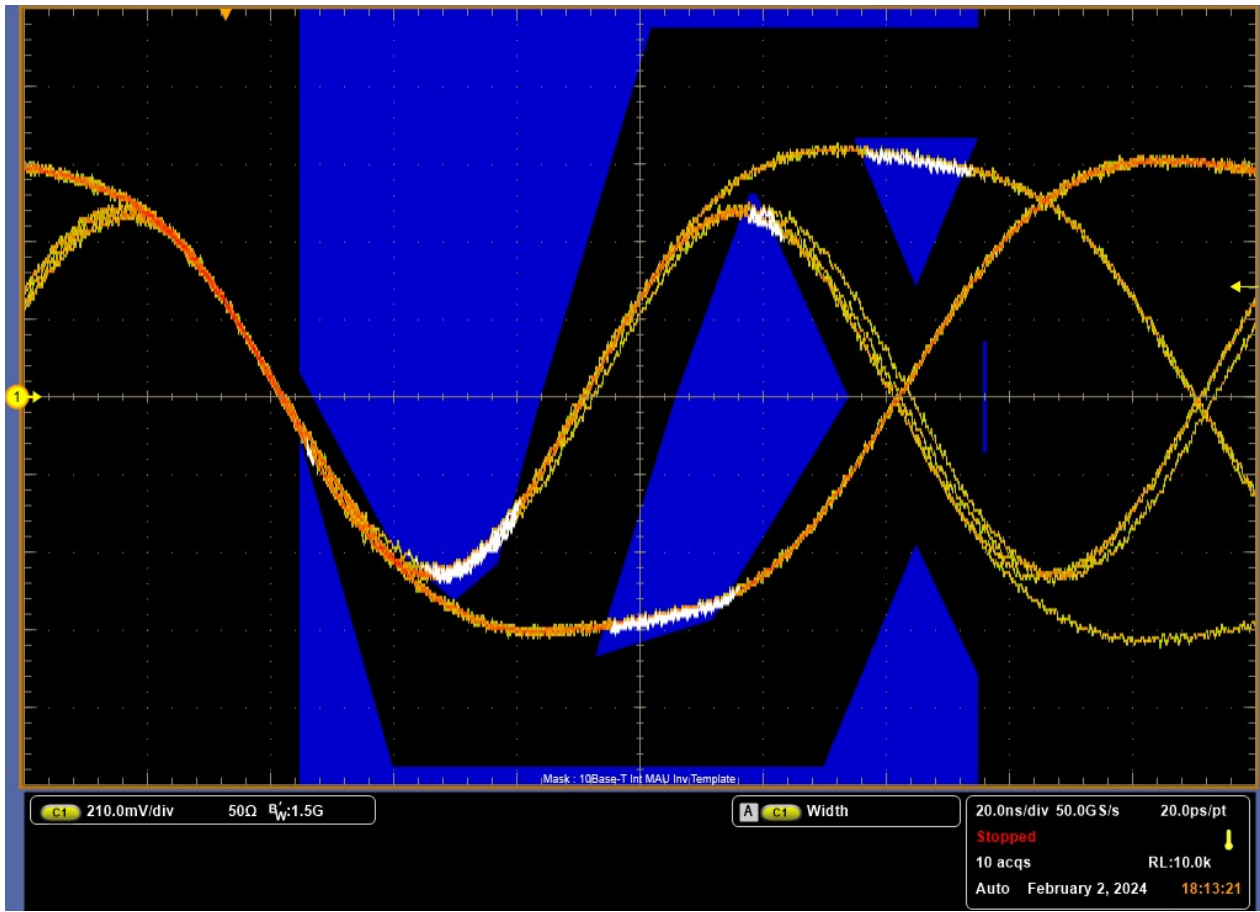
MAU External

[MAU External\\_Run1](#)



MAU Internal(Inverted)

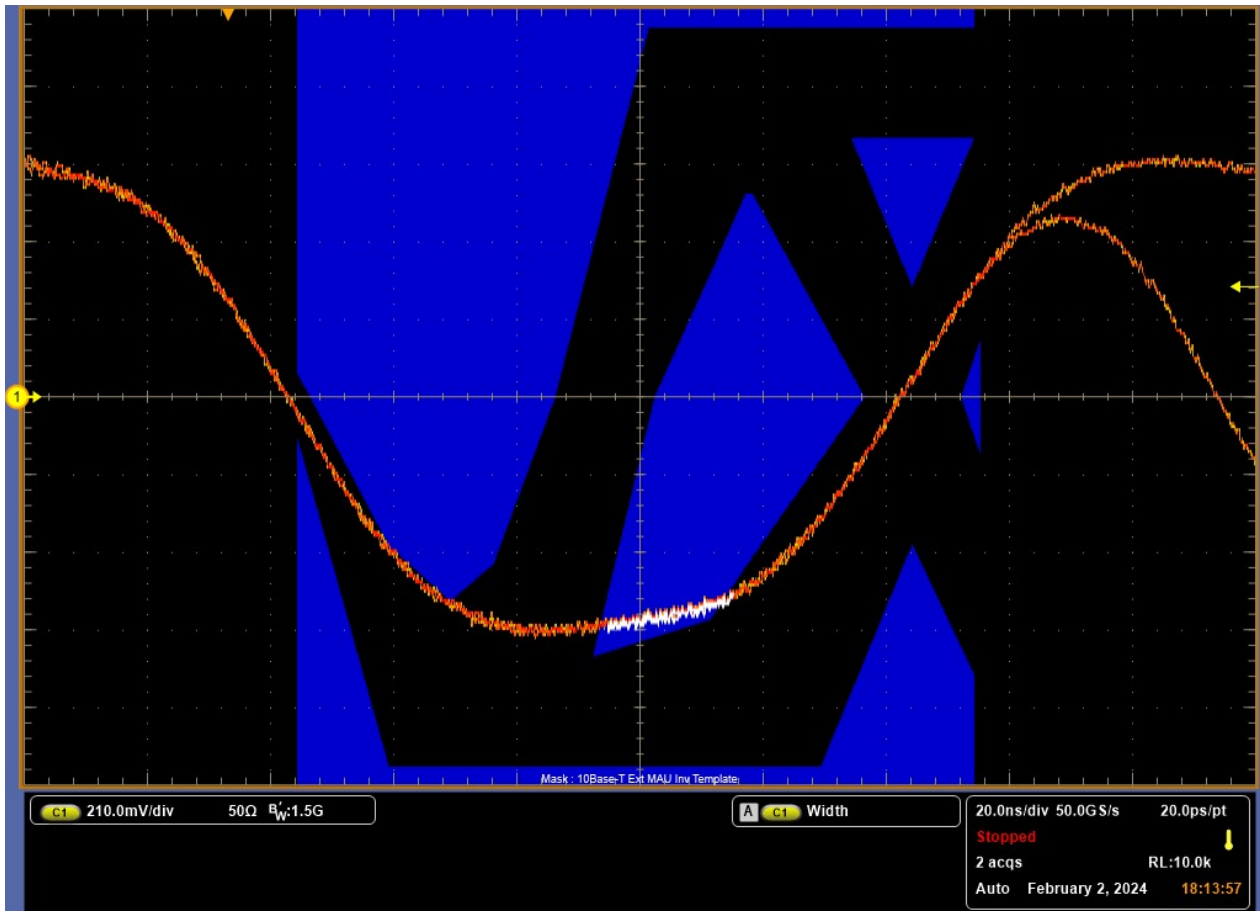
[MAU Internal Inverted \\_Run1](#)



MAU External(Inverted)

[MAU External Inverted \\_Run1](#)





Harmonic

[Harmonic\\_Run1](#)

