

ECR	ECR NUMBER	DATE
	-----	01/02/13

NOTES :

1. SILKSCREEN ALL JUMPERS AND HEADERS.
2. PLACE ALL PARTS ON A 0 OR 90 DEGREE ORIENTATION.
3. HIGH SPEED SERIAL DATA SHOULD BE ROUTED AS SINGLE-ENDED 50 OHM TRANSMISSION LINES. ROUTING DISTANCE SHOULD BE 3 INCHES OR LESS.
4. USE FR4-370 MATERIAL.
5. PLACE TI LOGO IN TOP SIDE METAL

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TEXAS INSTRUMENTS

SCHEMATIC TITLE
HD3SS21X_1_2MUX

ENGINEER	DATE
K. Shaw	01/02/13

LAYOUT	DATE

RELEASED	DATE

PAGE TITLE

SIZE	DOCUMENT NUMBER	REV	SHEET
B	6567854	A	1 of 5

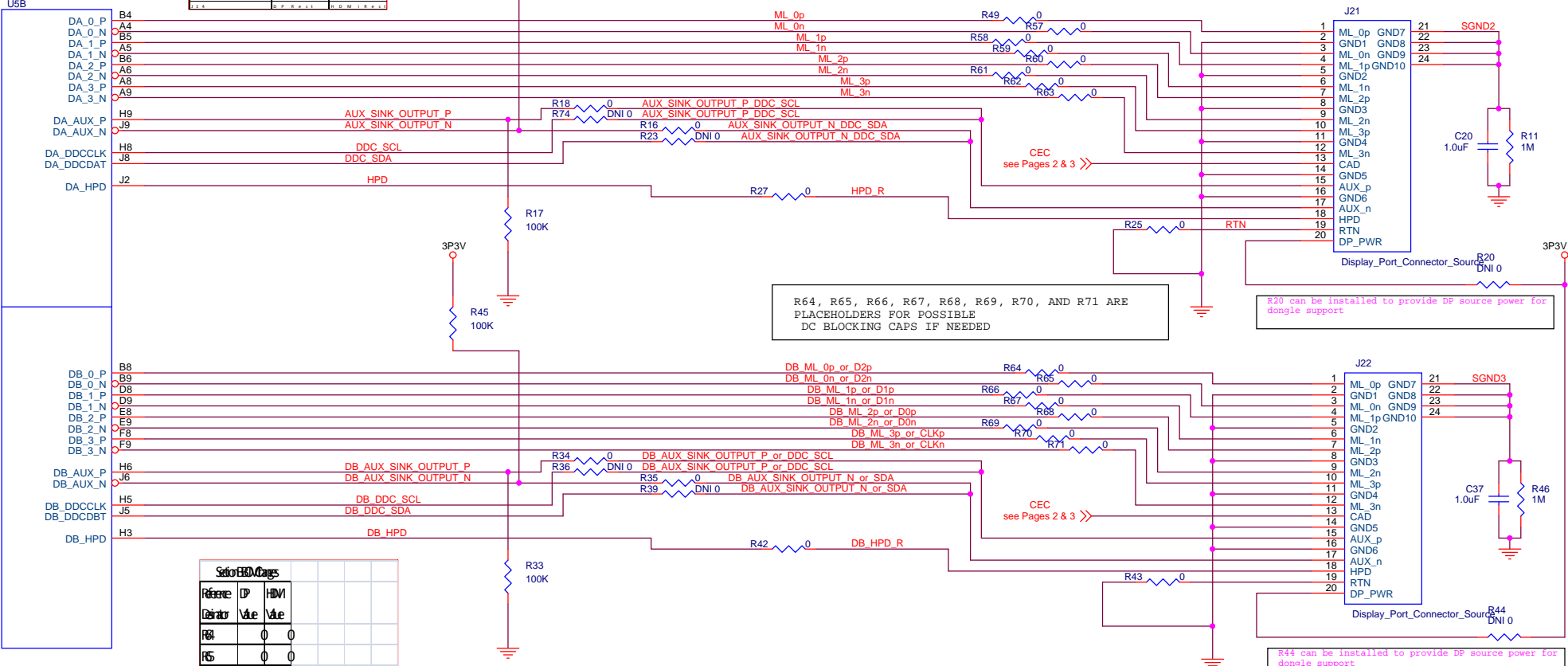
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R14	0	0
R17	0	0
R18	0	0
R23	0	0
R25	0	0
R27	0	0
R33	0	0
R34	0	0
R36	0	0
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R96	0	0
R97	0	0
R98	0	0
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R100	0	0

R49, R57, R58, R59, R60, R61, R62, AND R63 ARE PLACEHOLDERS FOR POSSIBLE DC BLOCKING CAPS IF NEEDED

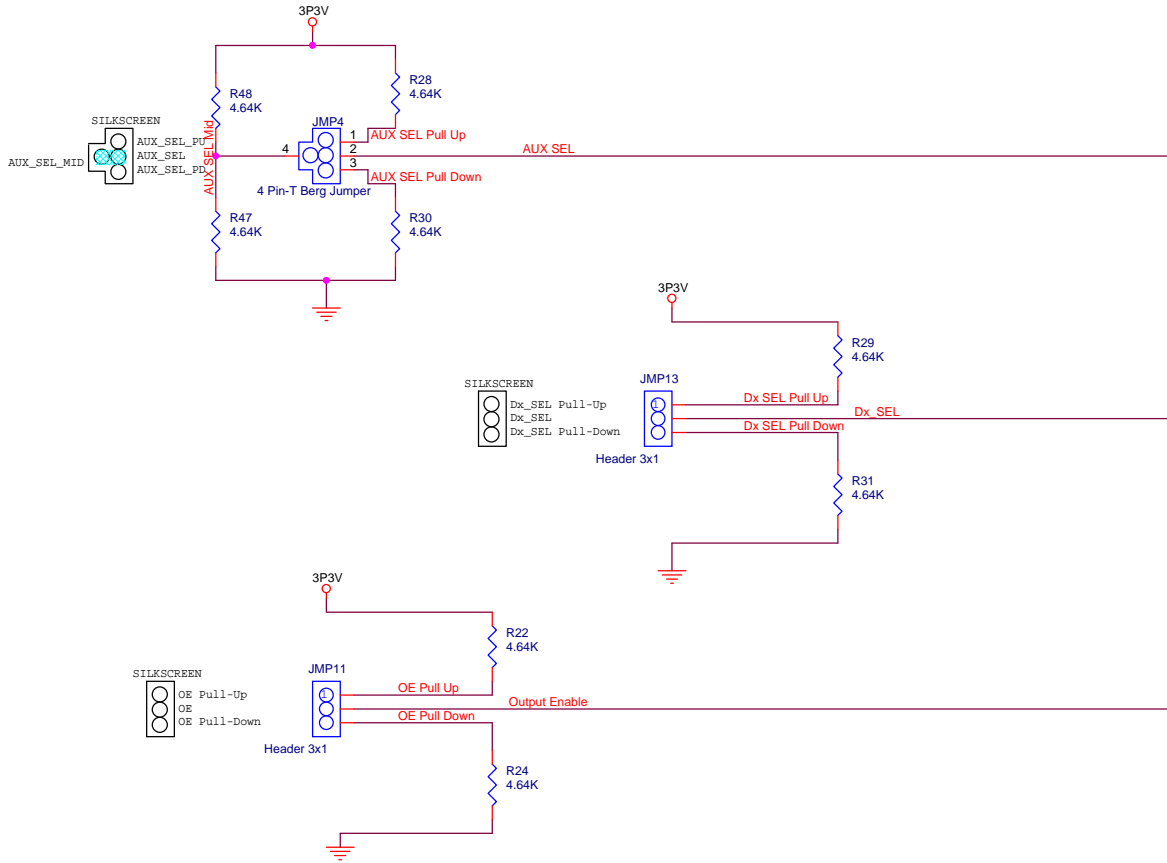
R64, R65, R66, R67, R68, R69, R70, AND R71 ARE PLACEHOLDERS FOR POSSIBLE DC BLOCKING CAPS IF NEEDED

R20 can be installed to provide DP source power for dongle support

R44 can be installed to provide DP source power for dongle support



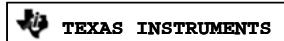
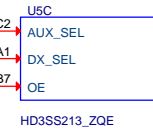
Reference	DP	HWI
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R25	0	0
R27	0	0
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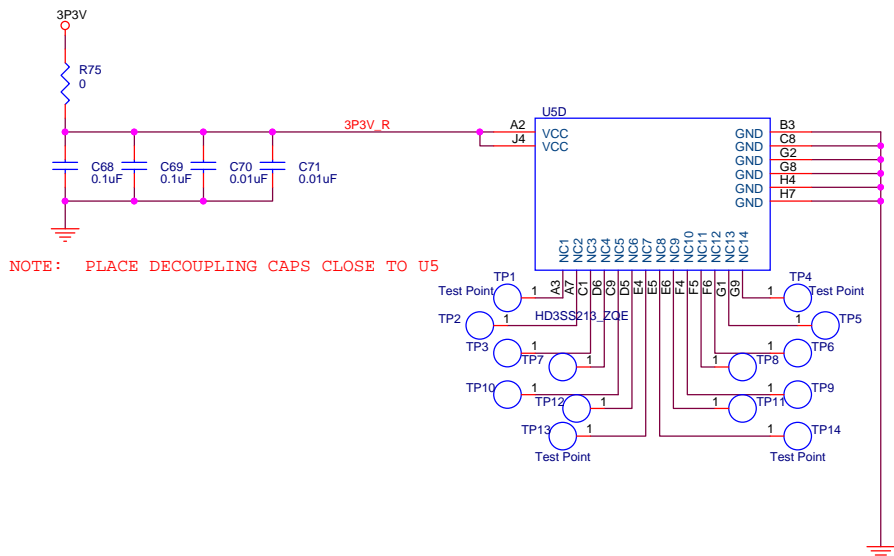
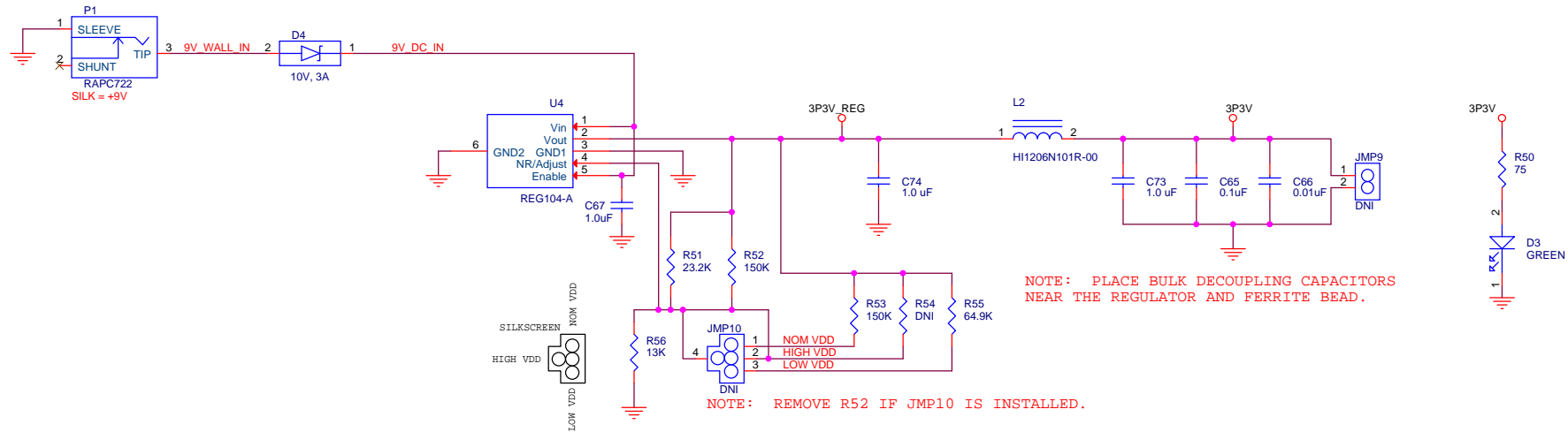


Switch Control Logic				
Control Lines	Switched I/O Pins			
Dx_SEL	DCz(p) Pin z=0, 1, 2 or 3	DCz(n) Pin z=0, 1, 2 or 3	HPDC Pin	CADC Pin (RSH)
L	DAz(p)	DAz(n)	HPDA	CADA
H	DBz(p)	DBz(n)	HPDB	CADB

OE pin - For normal operation, drive OE high. Driving the OE pin low will disable the switch to
 Note: The ports which are not selected by the Control Lines will be in High Impedance State.

AUX/DDC Switch Control Logic					
Control Lines		Switched I/O Pins			
AUX_SEL	Dx_SEL		AUXA	AUXB	AUXC
L	L	213/215	To/From AUXC	Z	To/From AUXA
L	H	213/215	Z	To/From AUXC	To/From AUXB
H	L	213/215	Z	Z	To/From DDCA
H	H	213/215	Z	Z	To/From DDCB
M		213	To/From DDCC	Z	Z





PAGE TITLE POWER			
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