Control Communication flow

Read byte
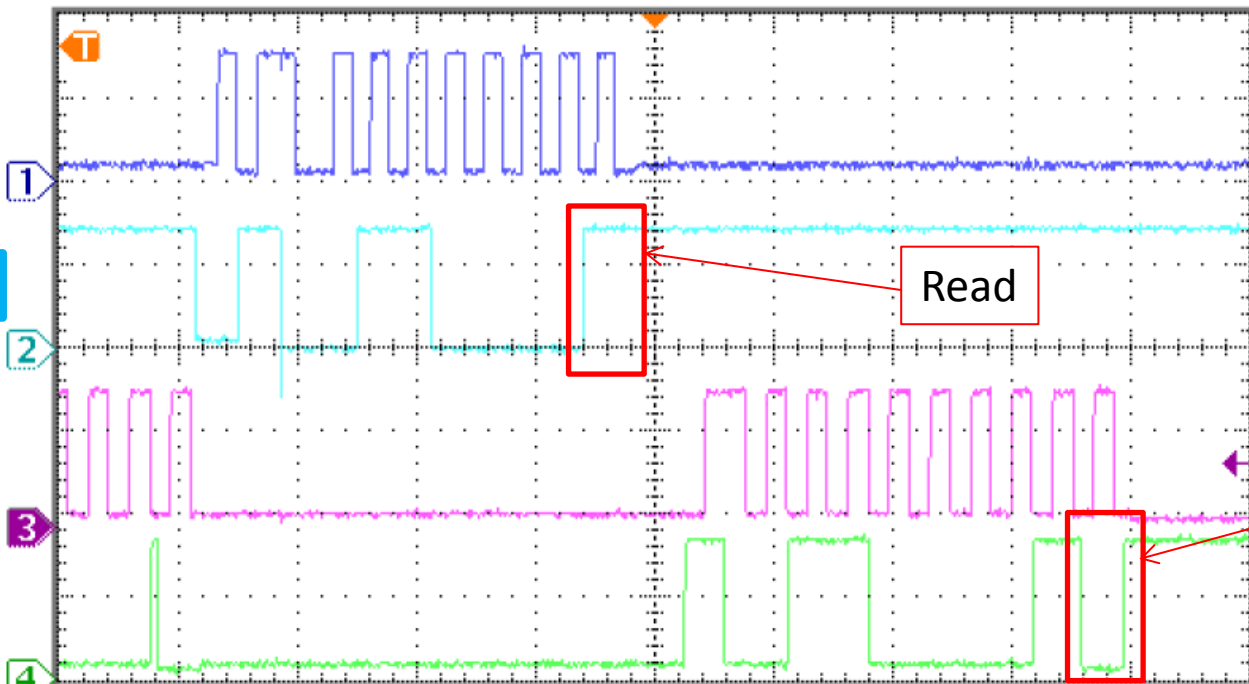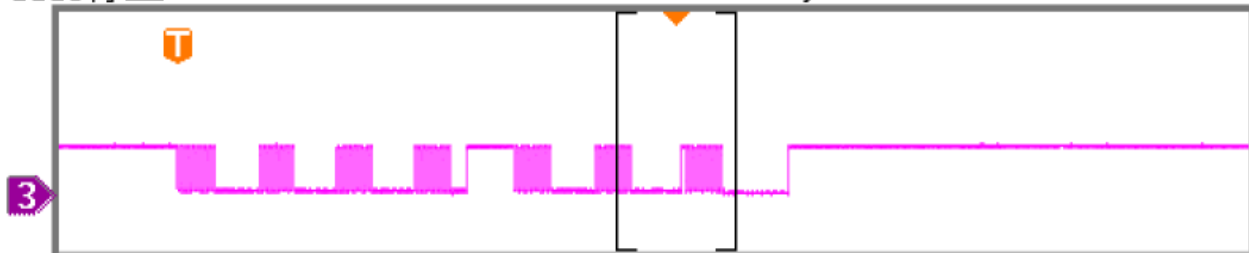
Clock Stretch

Tek 停止   M 400μs
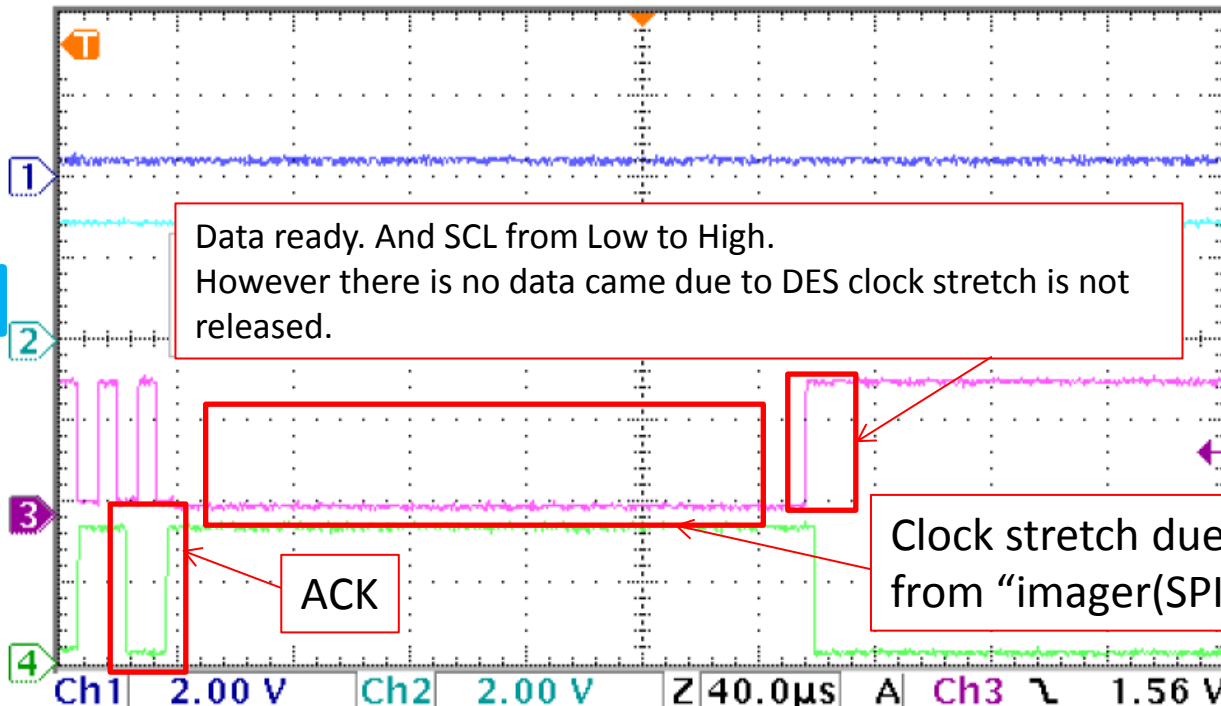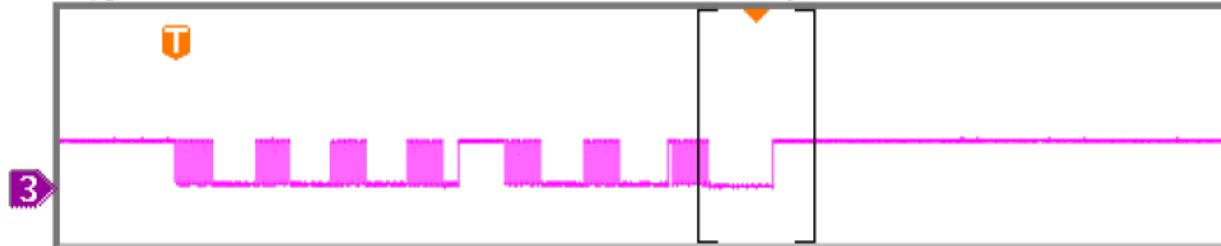
3

Ch4 周波数
20.66kHz

SCL of Des

Ch2 周波数
19.84kHz

Data ready. And SCL from Low to High.
However there is no data came due to DES clock stretch is not released.

SDA of Des

Ch1 周波数
52.07kHz

SCL of Ser

Clock stretch due to data is not ready from "imager(SPI) + MCU(SPI to I2C)"

ACK

SDA of Ser

| Ch1 | 2.00 V | Ch2 | 2.00 V | Z 40.0μs | A | Ch3 | 1.56 V |
| Ch3 | 2.00 V | Ch4 | 2.00 V | | | | |

T→▼ 1.99840ms

18 Mar 2015
23:29:49

HOST I2C

| S | SLAVE ADDRESS = VA | R/W |

←—————— SCL low ——————→

| A | OFFSET ADDRESS |

←—— SCL low ——→

| A | Sr | SLAVE ADDRESS = VA | R/W |

REMOTE I2C

| S | SLAVE ADDRESS = SA | R/W | A |

←—— SCL low ——→

| OFFSET ADDRESS | A |

←————— SCL low —————→

←————— SCL low —————→

| A | DATA1 | A |

←— SCL low —→

| DATA2 | Ā | P |

| Sr | SLAVE ADDRESS = SA | R/W | A | DATA1 |

←—— SCL low ——→

| A | DATA2 |

←—— SCL low ——→

| Ā | P |

SCL low

□ master to slave

□ slave to master
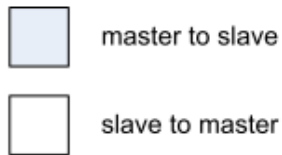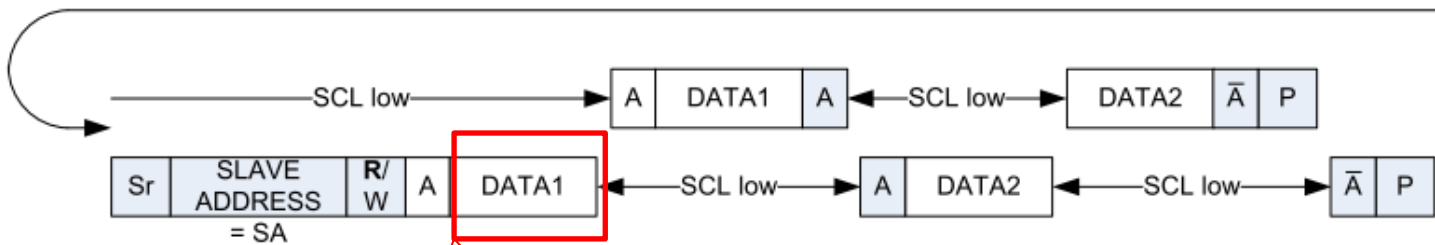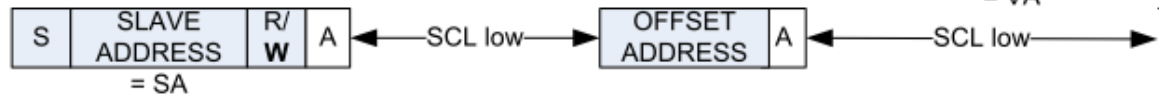
S = Start
Sr = Restart
A = Acknowledge
Ā = not acknowledge
P = Stop