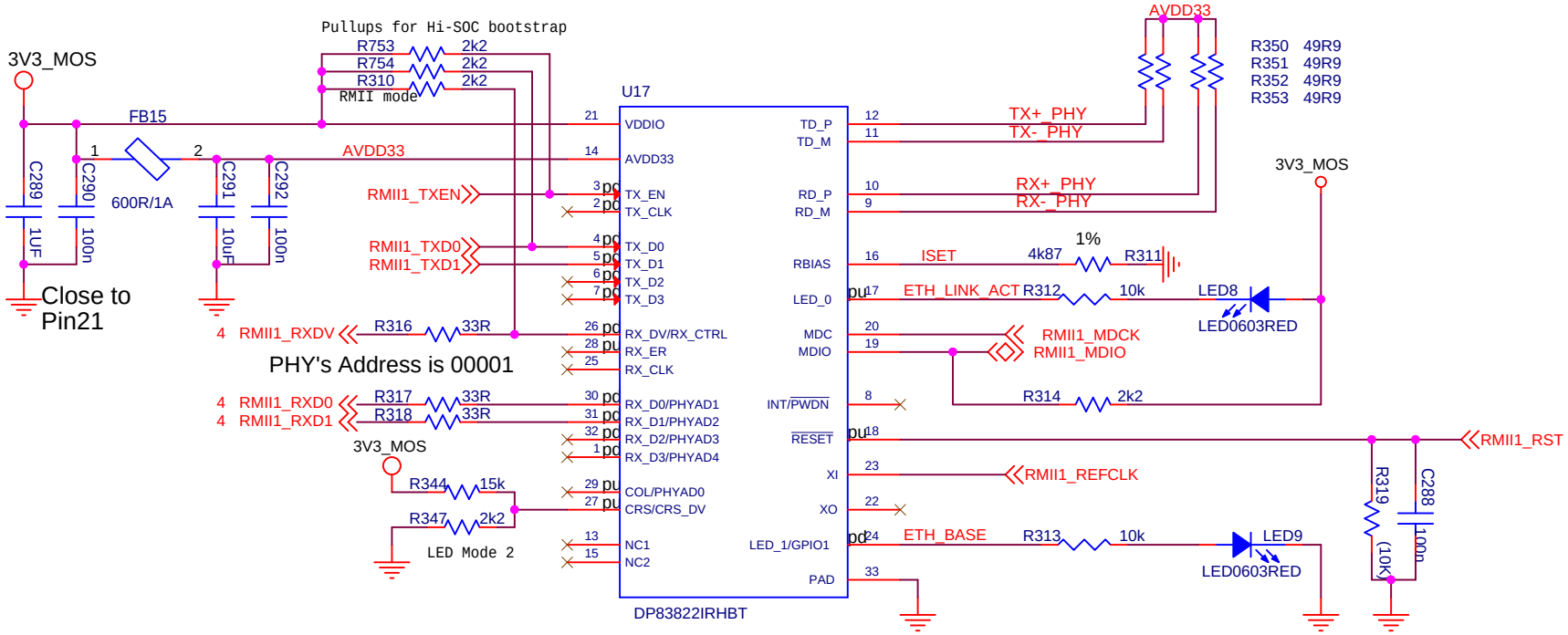
24 - Ethernet PHY



** Design guideline **

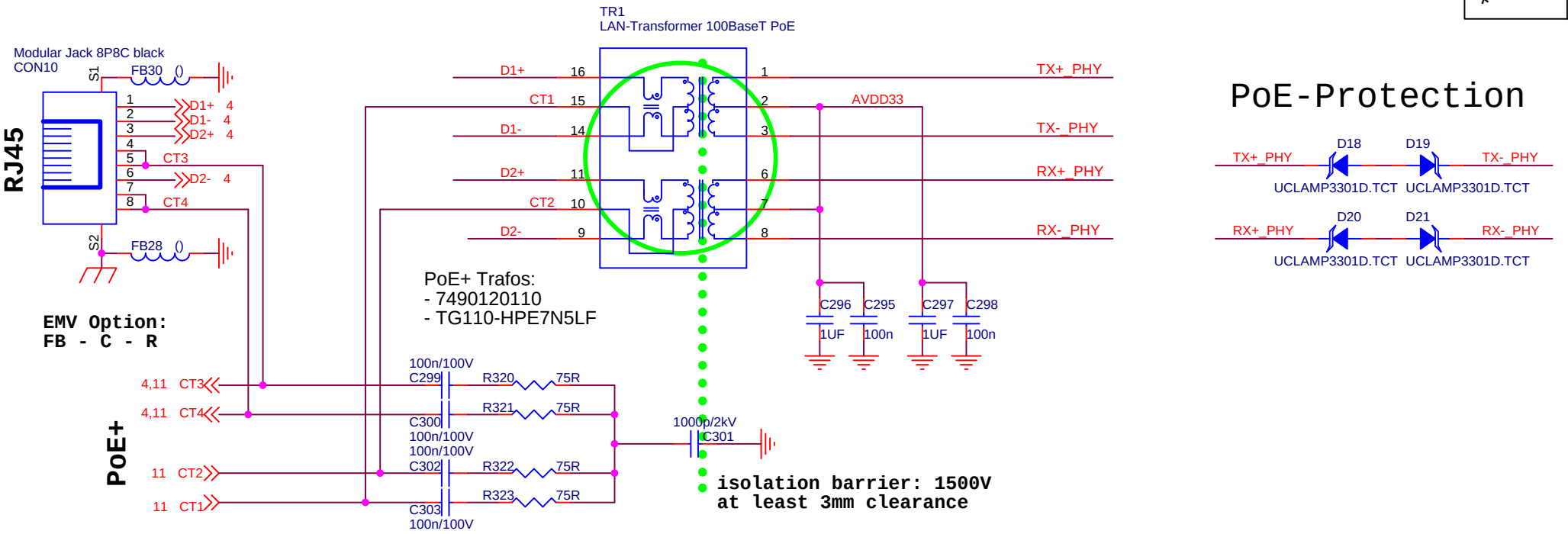
A.RMII interface

1.The REFCLK trace should be separated from other traces by GND

B.TX+_PHY/TX-_PHY & RX+_PHY/RX-_PHY differential pairs

1.Route as 100 Ohm differential impedence on TOP layer

2.Match trace length of differential pairs, 10 mils max within a pair.



PoE-Protection