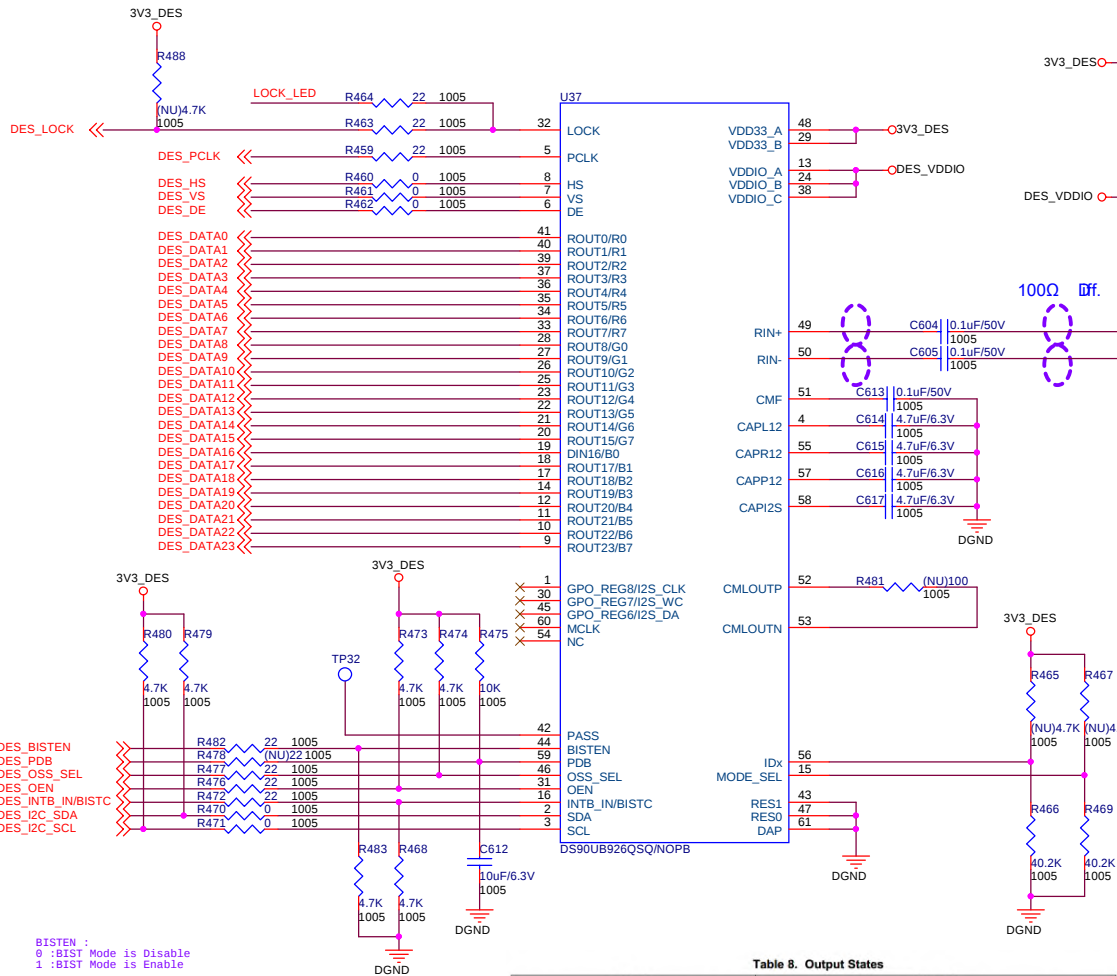




BISTEN :
0 :BIST Mode is Disable
1 :BIST Mode is Enable

BISTC :
0 :PCLK
1 :33MHz

PASS :
0 :One and more error
1 :Error Free

Table 8. Output States							
INPUTS				OUTPUTS			
SERIAL INPUT	PDB	OEN	OSS_SEL	LOCK	PASS	DATA, GPIO, I2S	CLK
X	0	X	X	Z	Z	Z	Z
X	1	0	0	L or H	L	L	L
X	1	0	1	L or H	Z	Z	Z
Static	1	1	0	L	L	L	L/OSC (Register bit enable)
Static	1	1	1	L	Previous Status	L	L
Active	1	1	0	H	L	L	L
Active	1	1	1	H	Valid	Valid	Valid

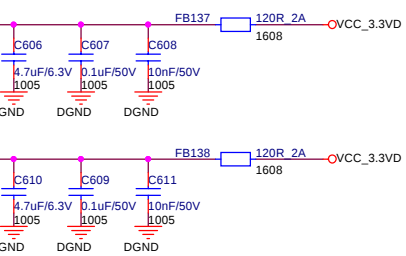


Table 10. Serial Control Bus Addresses for IDx					
NO.	IDEAL RATIO V_{R2} / V_{0033}	IDEAL V_{R2} (V)	SUGGESTED RESISTOR R1 kΩ (1% tol)	SUGGESTED RESISTOR R2 kΩ (1% tol)	ADDRESS 7'b APPENDED
1	0	0	Open	40.2	0x2C
2	0.123	0.406	124	17.4	0x2D

Table 9. Configuration Select (MODE_SEL)							
NO.	IDEAL RATIO V_{R4} / V_{0033}	IDEAL V_{R4} (V)	SUGGESTED RESISTOR R3 kΩ (1% tolerance)	SUGGESTED RESISTOR R4 kΩ (1% tolerance)	LFMODE ⁽¹⁾	Repeater ⁽²⁾	I2S CHANNEL B (18-bit Mode) ⁽³⁾
1	0	0	Open	40.2	L	L	L
2	0.123	0.407	115	16.2	L	L	H

