

Schematic Review Form

Customer/Project

Pin #	Name	Info	Violations	Description
1	VDD3V3		need at least 1 cap on each supply pin	3.3V supply voltage
2	EQ0		for repeater mode in GPIO mode , EQ0 need both PU and PD for future configuration	(See Table 5-2)
3	VIOSEL	PU to 1.8V, okay.		VIOSEL is used to select digital I/O input voltage for GPIOs, CROSS and I2C VIOSEL = VSS sets device into 1.2V I/O mode VIOSEL = VDD1V8 sets device into 1.8V I/O mode VIOSEL pin is real time control and not only latched at powered on reset. Be careful when this pin changes dynamically after power-on reset because the output voltage can change from 1.2V to 1.8V.
4	EQ1		for repeater mode in GPIO mode , EQ0 need both PU and PD for future configuration	(See Table 5-2)
5	RESETB	PU to 1.8V with a 100nF PD cap, okay.		Active Low Reset. After the RESETB deassertion, both repeaters are be enabled and in eUSB2 default mode awaiting configuration from eDSPr or eUSPr.

6	DPB	Okay		USB port B D+ pin
7	DNB	Okay		USB port B D- pin
8	GND	Okay		GND
9	DNA	Okay		USB port A D- pin
10	DPA	Okay		USB port A D+ pin
11	VDD1V8	Caps look good		1.8V analog supply voltage
12	EQ2/INT		for repeater mode in GPIO mode , EQ0 need both PU and PD for future configuration	I2C Mode: Open Drain active low level sensitive interrupt output to system non-I2C Mode: (See Table 5-2)
13	SCL	floating is ok for GPIO mode		I2C clock Open drain I/O.
14	SDA	floating is ok for GPIO mode		Bidirectional I2C data. Open drain I/O. Pulled up to I2C rail through an external resistor
15	VDD3V3		need at least 1 cap on each supply pin	3.3V supply voltage
16	eDP0	Okay.		eUSB2 port 0 D+ pin
17	eDN0	Okay.		eUSB2 port 0 D- pin
18	GND			GND
19	eDN1	Okay.		eUSB2 port 1 D- pin
20	eDP1	Okay.		eUSB2 port 1 D+ pin

Comments

in GPIO mode, only one side can be compensated for EQ setting either USB side or eUSB side.