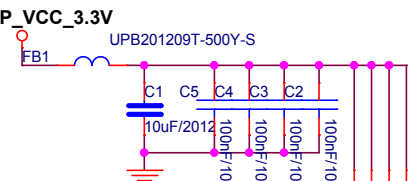
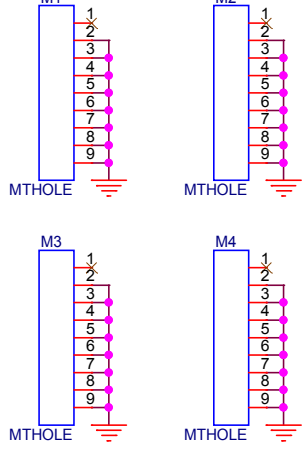
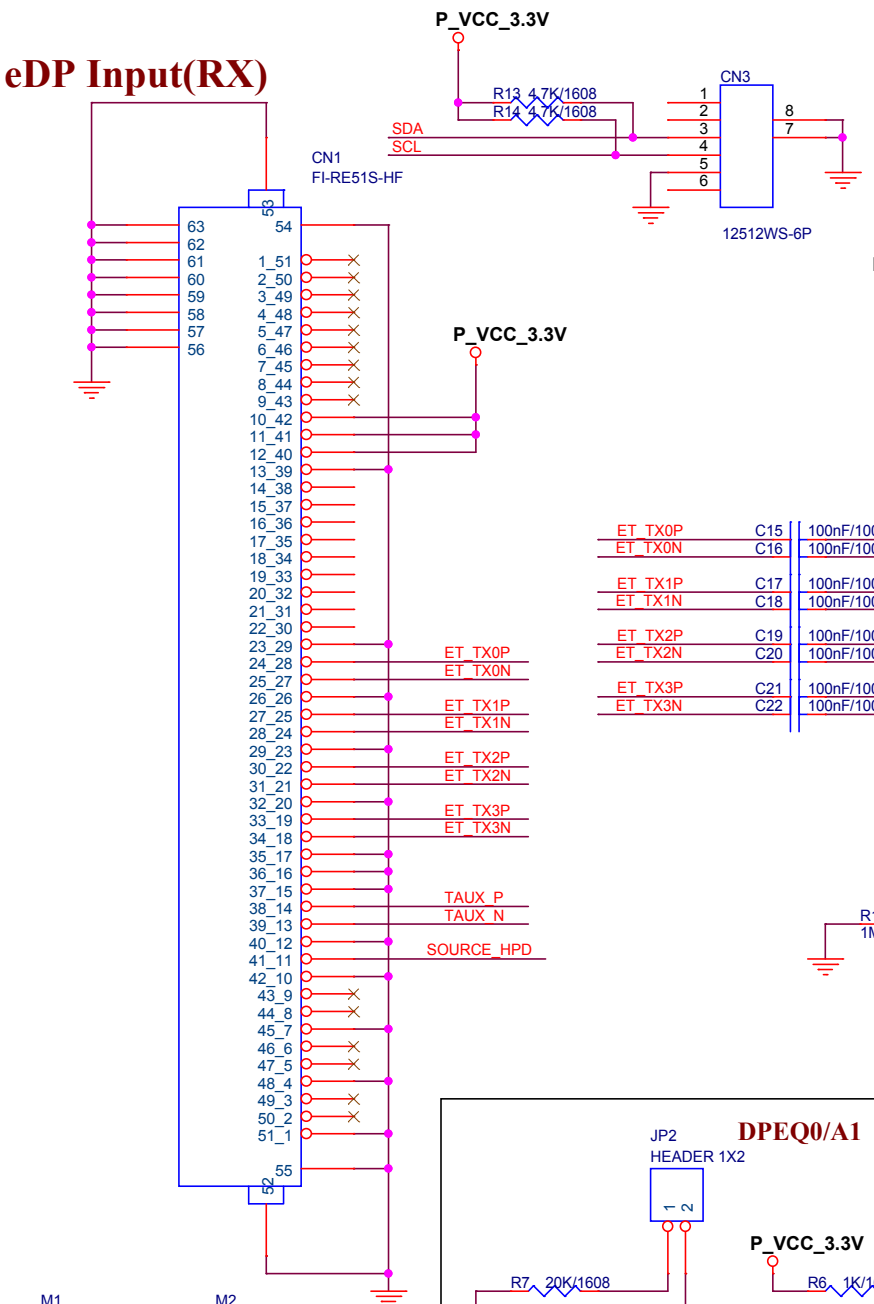
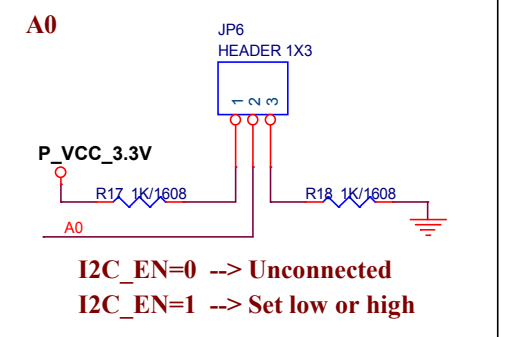
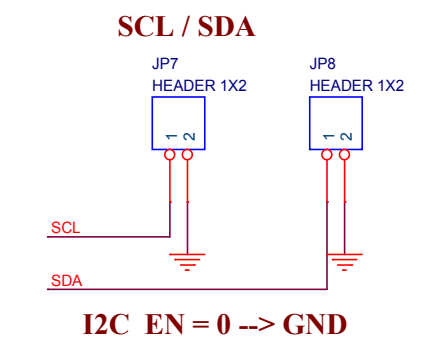
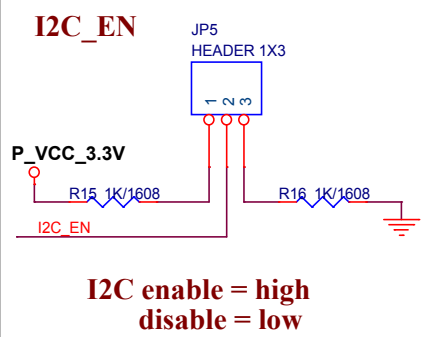
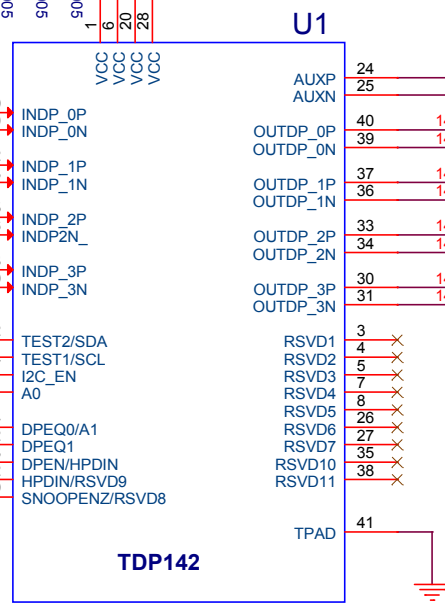
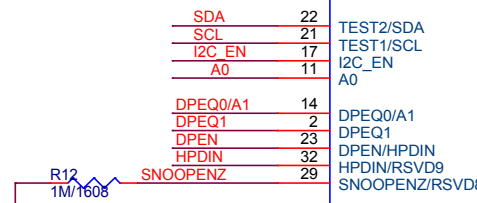


eDP Input(RX)



ET TX0P	C15	100nF/1005	142DP IN0P	9	INDP_0P
ET TX0N	C16	100nF/1005	142DP IN0N	10	INDP_0N
ET TX1P	C17	100nF/1005	142DP IN1P	12	INDP_1P
ET TX1N	C18	100nF/1005	142DP IN1N	13	INDP_1N
ET TX2P	C19	100nF/1005	142DP IN2P	15	INDP_2P
ET TX2N	C20	100nF/1005	142DP IN2N	16	INDP_2N
ET TX3P	C21	100nF/1005	142DP IN3P	18	INDP_3P
ET TX3N	C22	100nF/1005	142DP IN3N	19	INDP_3N



DisplayPort Output(TX)

