

[illegible]

The schematic diagram for SFP Port A illustrates the electrical connections for a Serial Fiber Port. It includes the following components and connections:

- Power and Grounding:**
 - VDD_3P3V and VDD_1P8V supply rails are shown at the top.
 - DGND (Digital Ground) is connected to the common ground of the port.
 - Resistors R61 (2.49k, 1%, 100mW) and R405 (>100R, 1%, 17mW) are used for current limiting and signal conditioning.
- Signal Traces:**
 - SN74LVC2G07DCKR (U6) is a 2V CMOS inverter used for signal inversion.
 - Signal traces include SFP1_TX_P0A0, SFP1_TX_P0A1, SFP1_TX_P0A2, SFP1_TX_P0A3, SFP1_TX_P0A4, SFP1_TX_P0A5, SFP1_TX_P0A6, SFP1_TX_P0A7, SFP1_TX_P0A8, SFP1_TX_P0A9, SFP1_TX_P0A10, SFP1_TX_P0A11, SFP1_TX_P0A12, SFP1_TX_P0A13, SFP1_TX_P0A14, SFP1_TX_P0A15, SFP1_TX_P0A16, SFP1_TX_P0A17, SFP1_TX_P0A18, SFP1_TX_P0A19, SFP1_TX_P0A20, SFP1_TX_P0A21, SFP1_TX_P0A22, SFP1_TX_P0A23, SFP1_TX_P0A24, SFP1_TX_P0A25, SFP1_TX_P0A26, SFP1_TX_P0A27, SFP1_TX_P0A28, SFP1_TX_P0A29, SFP1_TX_P0A30, SFP1_TX_P0A31, SFP1_TX_P0A32, SFP1_TX_P0A33, SFP1_TX_P0A34, SFP1_TX_P0A35, SFP1_TX_P0A36, SFP1_TX_P0A37, SFP1_TX_P0A38, SFP1_TX_P0A39, SFP1_TX_P0A40, SFP1_TX_P0A41, SFP1_TX_P0A42, SFP1_TX_P0A43, SFP1_TX_P0A44, SFP1_TX_P0A45, SFP1_TX_P0A46, SFP1_TX_P0A47, SFP1_TX_P0A48, SFP1_TX_P0A49, SFP1_TX_P0A50, SFP1_TX_P0A51, SFP1_TX_P0A52, SFP1_TX_P0A53, SFP1_TX_P0A54, SFP1_TX_P0A55, SFP1_TX_P0A56, SFP1_TX_P0A57, SFP1_TX_P0A58, SFP1_TX_P0A59, SFP1_TX_P0A60, SFP1_TX_P0A61, SFP1_TX_P0A62, SFP1_TX_P0A63, SFP1_TX_P0A64, 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