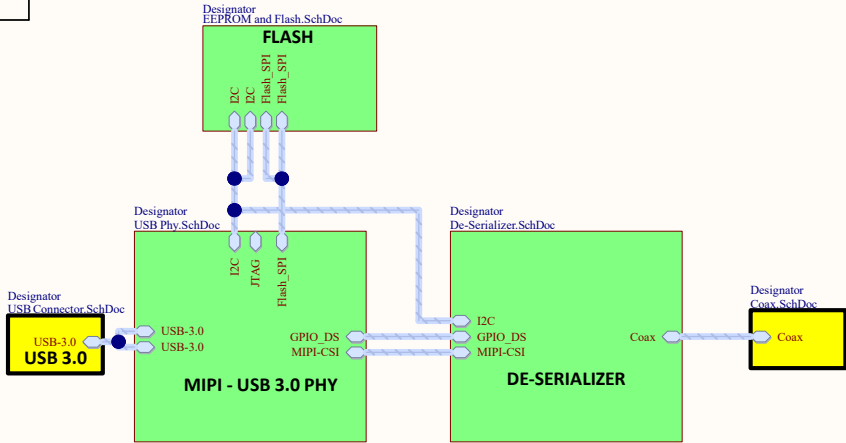


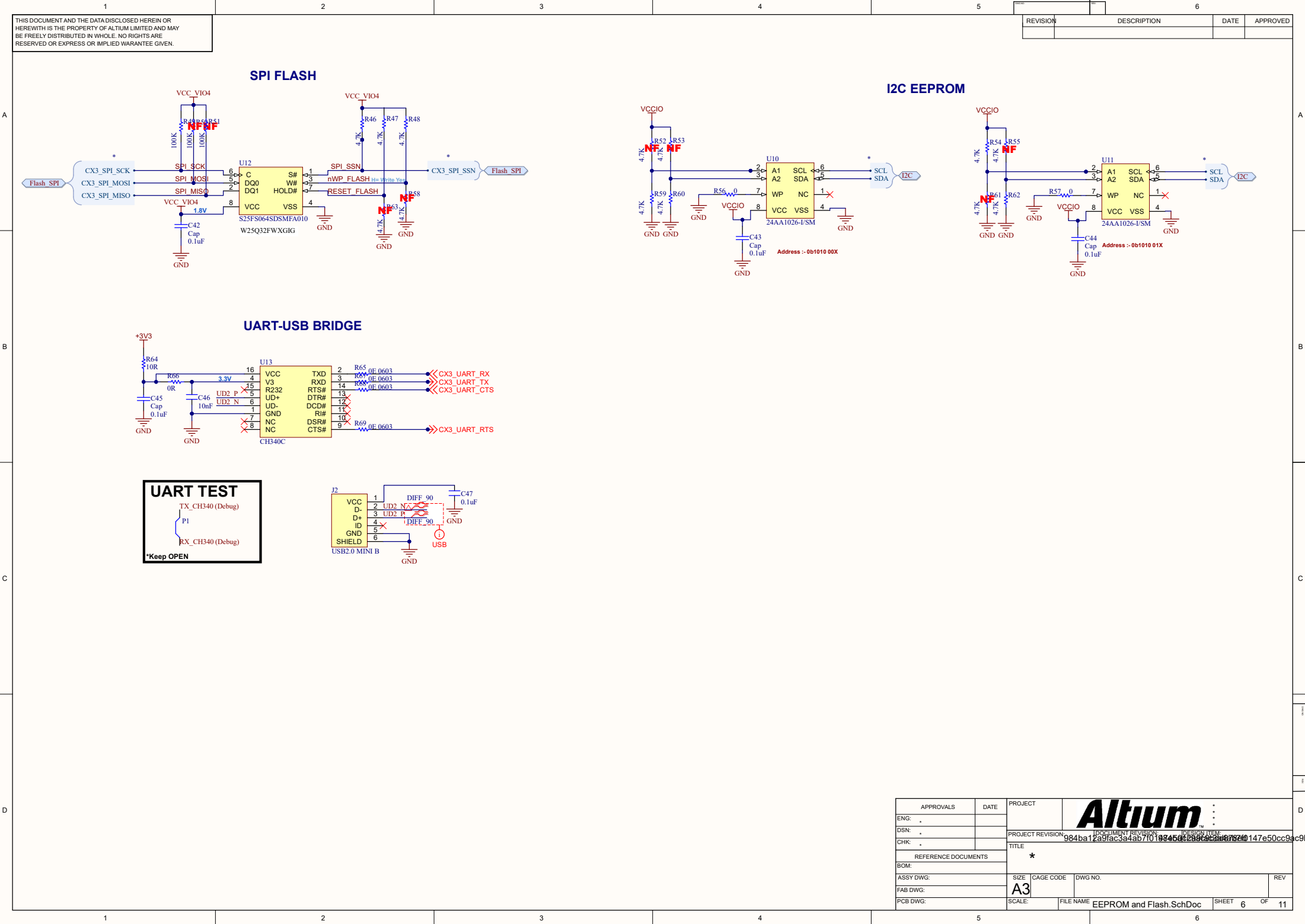
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APPROVALS	DATE	PROJECT	Altium	
ENG: *			.	
DSN: *			.	
CHK: *			.	
REFERENCE DOCUMENTS		TITLE		
BOM:		★		
ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:	A3			
PCB DWG:	SCALE:	FILE NAME	Main.SchDoc	
		SHEET	3	OF 11

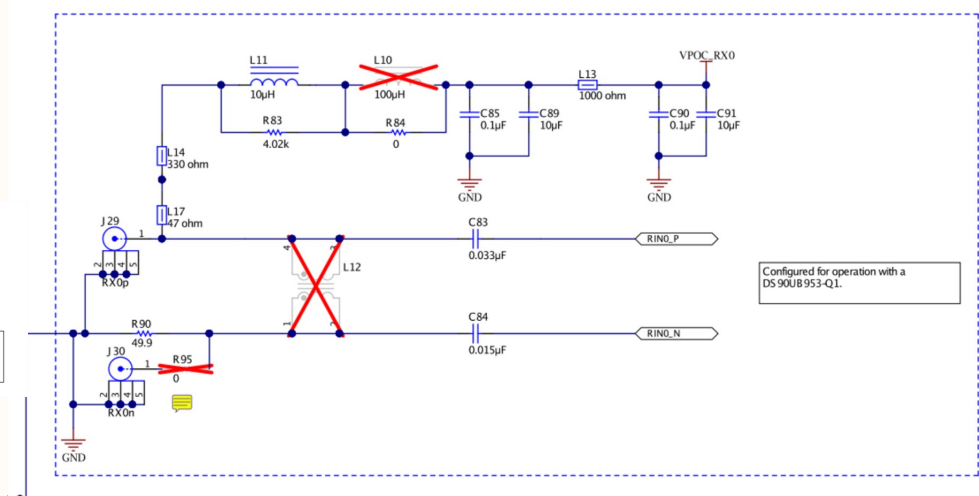
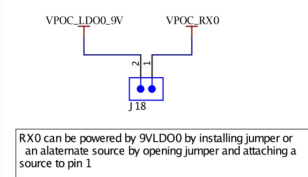
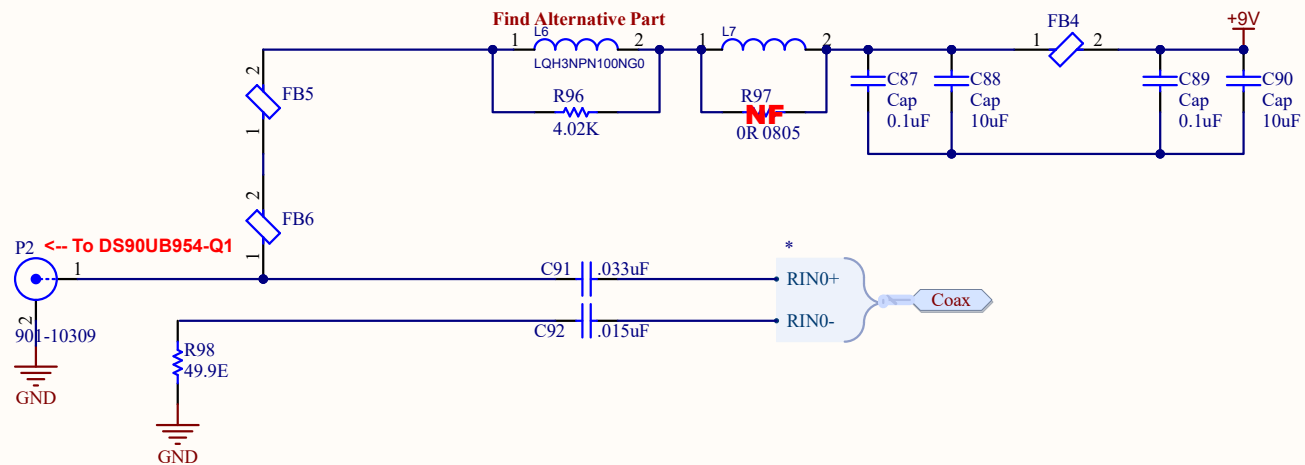
APPROVALS	DATE	PROJECT				D
ENG:	*	PROJECT REVISION		984ba12a9fac34ab701d7e50b08a59d37c1bb1aa1ba65d49c		
DSN:	*	DOCUMENT REVISION		984ba12a9fac34ab701d7e50b08a59d37c1bb1aa1ba65d49c		
CHK:	*	TITLE		★		
REFERENCE DOCUMENTS						
BOM:						
ASSY DWG:		SIZE	CAGE CODE	DWG NO.	REV	
FAB DWG:		A3				
PCB DWG:		SCALE:	FILE NAME	Power_De-Ser_SchDoc	SHEET	5 OF 11

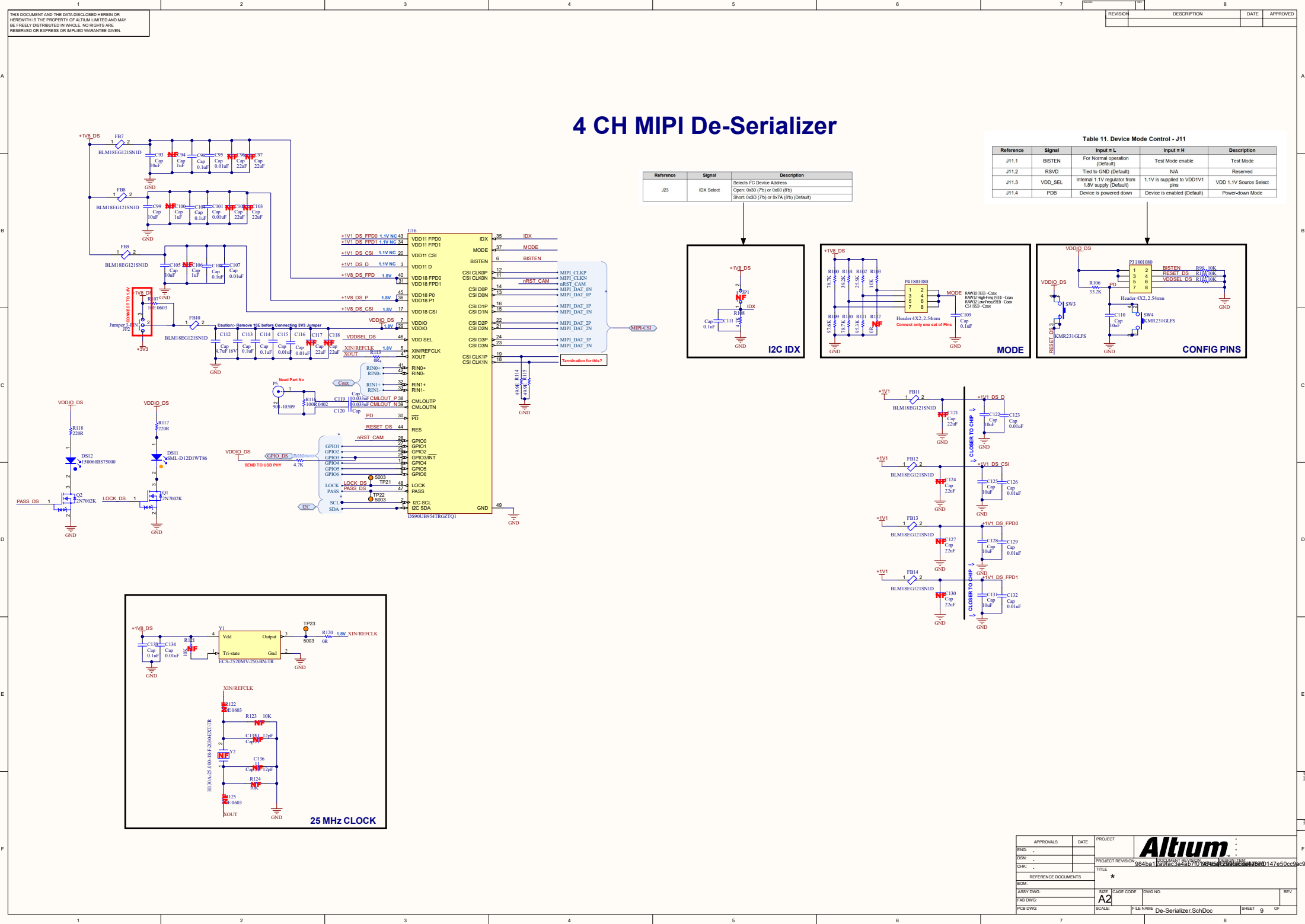


REVISION	DESCRIPTION	DATE	APPROVED

APPROVALS	DATE	PROJECT	Altium	
ENG: *				
DSN: *		PROJECT REVISION	DOCUMENT REVISION	DESIGN ITEM
CHK: *				
REFERENCE DOCUMENTS		TITLE		
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ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:	A3			
PCB DWG:	SCALE:	FILE NAME	EEPROM and Flash.SchDoc	SHEET 6 OF 11

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ENG: *				*			
DSN: *							
CHK: *				PROJECT REVISION:		DOCUMENT REVISION:	
				984ba12a9fac344ab7f0186256da85b9b0b579ed41b38de1ae		DESIGN ITEM:	
				TITLE			
REFERENCE DOCUMENTS							
BOM:							
ASSY DWG:							
FAB DWG:				SIZE		CAGE CODE	
PCB DWG:				A3		DWG NO.	
		SCALE:		FILE NAME		SHEET	
				USB Phy.SchDoc		7 OF 11	



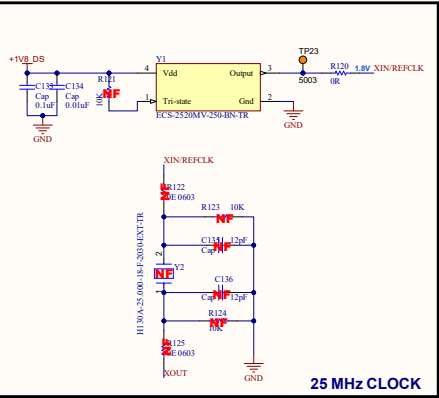
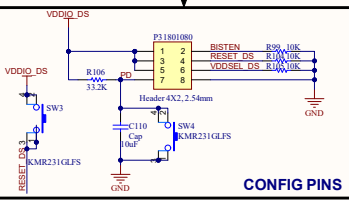
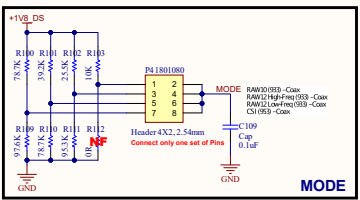
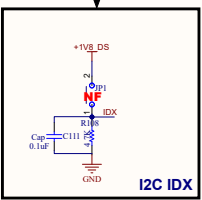


4 CH MIPI De-Serializer

Reference	Signal	Description
J23	IDX Select	Selects I2C Device Address Open: 0x5D (7b) or 0x60 (8b) Short: 0x5D (7b) or 0x7A (8b) (Default)

Table 11. Device Mode Control - J11

Reference	Signal	Input = L	Input = H	Description
J11.1	BISTEN	For Normal operation (Default)	Test Mode enable	Test Mode
J11.2	RSVD	Tied to GND (Default)	N/A	Reserved
J11.3	VDD_SEL	Internal 1.1V regulator from 1.8V supply (Default)	1.1V is supplied to VDD1V1 pins	VDD 1.1V Source Select
J11.4	PDB	Device is powered down	Device is enabled (Default)	Power-down Mode



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FAB DWG:		A2		
PCB DWG:		SCALE	FILE NAME	SHEET 9 OF

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A



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ABCD