

Signal description

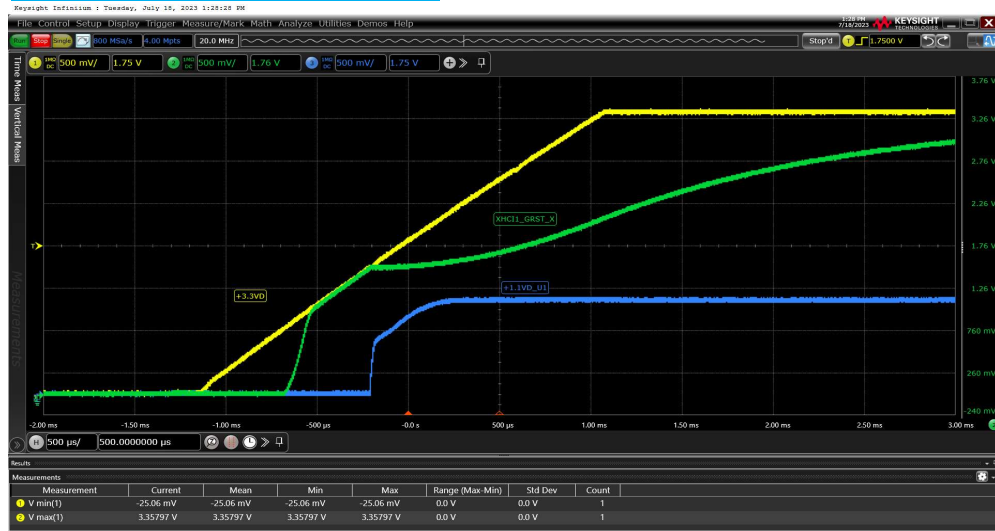
CH1 : VDD33 (+3.3VD)

CH2 : XHCI1_GRST_X
CH3 : VDD11 (+1.1VD_U1)

Power supply input to VDD33 pin of TUSB7320.

Signal input to the GRST pin of the TUSB7320.

Power supply input to VDD11 pin of TUSB7320.



In order to confirm the influence of TUSB7320,
External components for controlling XHCI1_GRST_X are disconnected.

