



Test Report

Overall Result: FAIL

Test Configuration Details	
Application	
Name	D9020DPHC MIPI D-PHY Test
Version	3.90.0.0
Device Description	
High Speed Data Rate(Mbps)	1450
CTS Version	v1.2
ZID	100 ohm
CLoad	50pF
Fixture Setup	Auto Load Switching
Test Session Details	
Infiniium SW Version	06.50.00906
Infiniium Model Number	DSOV084A
Infiniium Serial Number	MY57220101
Debug Mode Used	No
Compliance Limits	MIPI D-PHY Test Limit v1.2 (official)
Probe (Channel 1)	Model: 1169B Serial: US55390644 Head: N5425A/B Atten: Calibrated (24 OCT 2022 19:25:55), Using Cal Atten (3.4646E+00) Skew: Calibrated (24 OCT 2022 19:26:17), Using Cal Skew
Probe (Channel 2)	Model: 1169B Serial: US55390676 Head: N5425A/B Atten: Calibrated (24 OCT 2022 18:56:29), Using Cal Atten (3.2403E+00) Skew: Calibrated (24 OCT 2022 18:56:39), Using Cal Skew
Probe (Channel 3)	Model: 1169B Serial: US55390699 Head: N5425A/B Atten: Calibrated (24 OCT 2022 19:00:25), Using Cal Atten (3.2236E+00) Skew: Calibrated (24 OCT 2022 19:00:35), Using Cal Skew
Probe (Channel 4)	Model: 1169B Serial: US55390726 Head: N5425A/B Atten: Calibrated (24 OCT 2022 19:02:31), Using Cal Atten (3.1286E+00) Skew: Calibrated (24 OCT 2022 19:02:42), Using Cal Skew
Last Test Date	2022-10-24 20:21:21 UTC +08:00

Summary of Results

Test Statistics	
Failed	9
Passed	23
Total	32

Margin Thresholds	
Warning	< 2 %
Critical	< 0 %

Pass	# Failed	# Trials	Test Name	Actual Value	Margin	Pass Limits
✓	0	1	1.3.7 HS Data TX Static Common Mode Voltage(Vcmtx)	169.96 mV	20.0 %	150.00 mV <= VALUE <= 250.00 mV
✓	0	1	1.3.8 HS Data TX Vcmtx Mismatch	1.99 mV	60.2 %	VALUE < 5.00 mV
✓	0	1	1.3.10 HS Data TX Common-Level Variations Above 450MHz (VCMTX(HF))	8.38 mV	44.1 %	VALUE < 15.00 mV
✗	1	1	1.3.9 HS Data TX Common-Level Variations Between 50-450MHz (VCMTX(LF))	27.39 mV	-9.6 %	VALUE < 25.00 mV
✓	0	1	1.3.4 HS Data TX Differential Voltage(VOD0 Pulse)	-224.67 mV	34.9 %	-270.00 mV <= VALUE <= -140.00 mV
✗	1	1	1.3.4 HS Data TX Differential Voltage(VOD1 Pulse)	276.20 mV	-4.8 %	140.00 mV <= VALUE <= 270.00 mV
✗	1	1	1.3.5 HS Data TX Differential Voltage Mismatch (Pulse)	51.54 mV	-268.1 %	VALUE < 14.00 mV
✓	0	1	1.3.6 HS Data TX Single Ended Output High Voltage(VOHHS Pulse)	328.47 mV	8.8 %	VALUE <= 360.00 mV
✓	0	1	1.4.17 HS Clock Instantaneous (UInst) (Max)	690 ps	94.5 %	VALUE < 12.500 ns
✓	0	1	1.3.3 HS Entry: DATA TX THS- PREPARE+THS- ZERO	183.03 ns	21.0 %	VALUE >= TXTHSPrepareTHSZero_LimitMin s

✗	1	1	1.3.11 HS Data TX 20%-80% Rise Time (tR)[Burst Data]	1.644 ns	-650.7 %	VALUE < DataRiseTime_LimitMax s
✗	1	1	1.3.12 HS Data TX 80%-20% Fall Time (tF)[Burst Data]	4.891 ns	-213E+01 %	VALUE < DataFallTime_LimitMax s
✓	0	1	1.4.9 HS Clock TX Common-Level Variations Between 50-450MHz (VCMTX(LF))	6.11 mV	75.6 %	VALUE < 25.00 mV
✓	0	1	1.4.7 HS Clock TX Static Common Mode Voltage(Vcmtx)	189.23 mV	39.2 %	150.00 mV <= VALUE <= 250.00 mV
✓	0	1	1.4.8 HS Clock TX Vcmtx Mismatch	440 µV	91.2 %	VALUE < 5.00 mV
✓	0	1	1.4.10 HS Clock TX Common-Level Variations Above 450MHz (VCMTX(HF))	4.99 mV	66.7 %	VALUE < 15.00 mV
✓	0	1	1.4.4 HS Clock TX Differential Voltage(VOD0 Pulse)	-211.13 mV	45.3 %	-270.00 mV <= VALUE <= -140.00 mV
✓	0	1	1.4.4 HS Clock TX Differential Voltage(VOD1 Pulse)	206.63 mV	48.7 %	140.00 mV <= VALUE <= 270.00 mV
✓	0	1	1.4.5 HS Clock TX Differential Voltage Mismatch (Pulse)	4.50 mV	67.9 %	VALUE < 14.00 mV
✓	0	1	1.4.6 HS Clock TX Single Ended Output High Voltage(VOHHS Pulse)	309.03 mV	14.2 %	VALUE <= 360.00 mV
✗	1	1	1.4.11 HS Clock TX 20%-80% Rise Time (tR)[Continuous Clock, Burst Data]	598 ps	-173.1 %	VALUE < CLKRiseTime_LimitMax s
✗	1	1	1.4.12 HS Clock TX 80%-20% Fall Time (tF)[Continuous Clock, Burst Data]	573 ps	-161.6 %	VALUE < CLKFallTime_LimitMax s
✓	0	1	1.4.17 HS Clock Instantaneous (Uinst) (Min)	552 ps	552.0 %	VALUE >= Uinst_Min_Limit s
✗	1	1	1.4.18 Clock Lane HS Clock Delta UI (UI variation)	-11.70 %	-67.0 %	UIVariant_Limit_Min % <= VALUE <= UIVariant_Limit_Max %
✓	0	1	1.3.1 HS Entry: DATA TLPX	55.26 ns	10.5 %	VALUE >= 50.00 ns

✓	0	1	1.3.2 HS Entry: DATA TX THS-PREPARE	50.48 ns	17.3 %	THSPrepare_LimitMin s <= VALUE <= THSPrepare_LimitMax s
✓	0	1	1.3.13 HS Exit: DATA TX THS-TRAIL	84.13 ns	34.1 %	VALUE >= TXTHSTrail_LimitMin s
✓	0	1	1.3.14 HS Exit: DATA TX TREOT	-3.56 ns	110.2 %	VALUE <= 35.00 ns
✓	0	1	1.3.15 HS Exit: DATA TX TEOT	80.57 ns	28.4 %	VALUE <= TXTEOT_LimitMax s
✓	0	1	1.3.16 HS Exit: DATA TX THS-EXIT	7.10990 µs	701E+01 %	VALUE >= 100.00 ns
✗	1	1	1.5.4 Data-to-Clock Skew (TSKEW(TX)) (Max,Min)	500 mUinst	-75.0 %	MinMaxTSkewTest_LimitMin Uinst <= VALUE <= MinMaxTSkewTest_LimitMax Uinst
✓	0	1	1.5.4 Data-to-Clock Skew (TSKEW(TX)) (Mean)	-9 mUinst	47.8 %	MeanTSkewTest_LimitMin Uinst <= VALUE <= MeanTSkewTest_LimitMax Uinst

Report Detail

[Next](#)

✓ 1.3.7 HS Data TX Static Common Mode Voltage(Vcmtx) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.7)

Test Summary: Pass Test Description: HS transmit static common-mode voltage.

Pass Limits:[150.00 mV to 250.00 mV]811_Vcmtx(Worst)169.96 mV

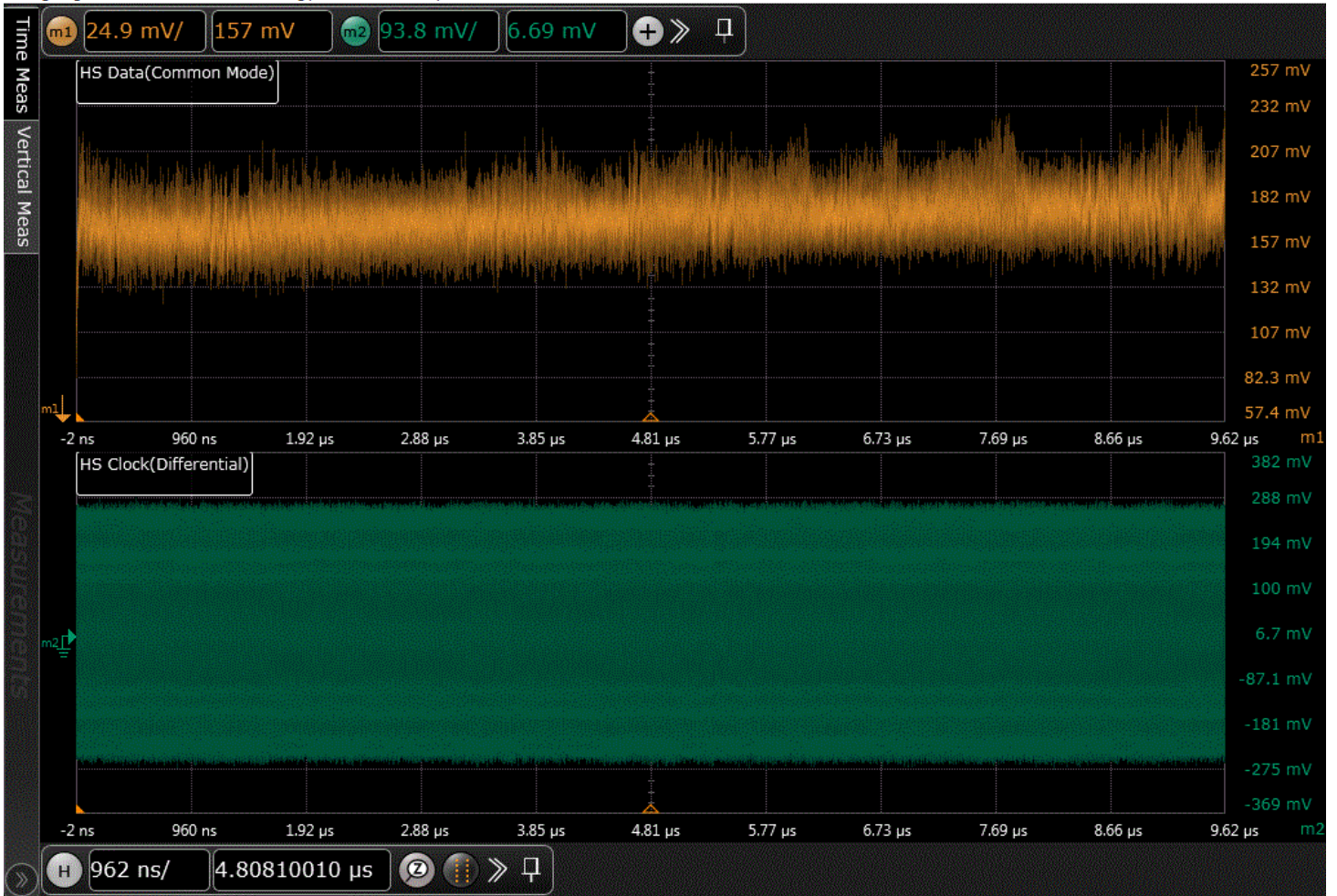
Result Details:

Result Details

HS Data TX Common Mode Voltage(See image)**Vcmtx(Differential-1)**173.94 mV**Number of Vcmtx(Differential-1) Measured**4.816000 k**Vcmtx(Differential-0)**169.96 mV**Number of Vcmtx(Differential-0) Measured**10.570000 k**ZID**100 ohm**DataLane**Lane2**Number of HS Burst**1 Trial 1

Trial 1: HS Data TX Common Mode Voltage

Keysight Infiniium : Monday, October 24, 2022 8:09:28 PM

[Top Previous](#)[Next](#)

✓ 1.3.8 HS Data TX Vcmtx Mismatch Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.8)

Test Summary: Pass Test Description: VCMTX mismatch when output is Differential-1 or Differential-0.

Pass Limits:< 5.00 mV812_Vcmtx Mismatch1.99 mV

Result Details:

Result Details

Vcmtx(Differential-1)173.94 mVVcmtx(Differential-0)169.96 mVZID100

ohmDataLaneLane2Number of HS Burst1

[Top Previous](#)[Next](#)

✓ 1.3.10 HS Data TX Common-Level Variations Above 450MHz (VCMTX(HF)) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.3.10)

Test Summary: Pass Test Description: Common-level variations above 450MHz.

Pass Limits:< 15.00 mV818_Vcmtx(HF)8.38 mV

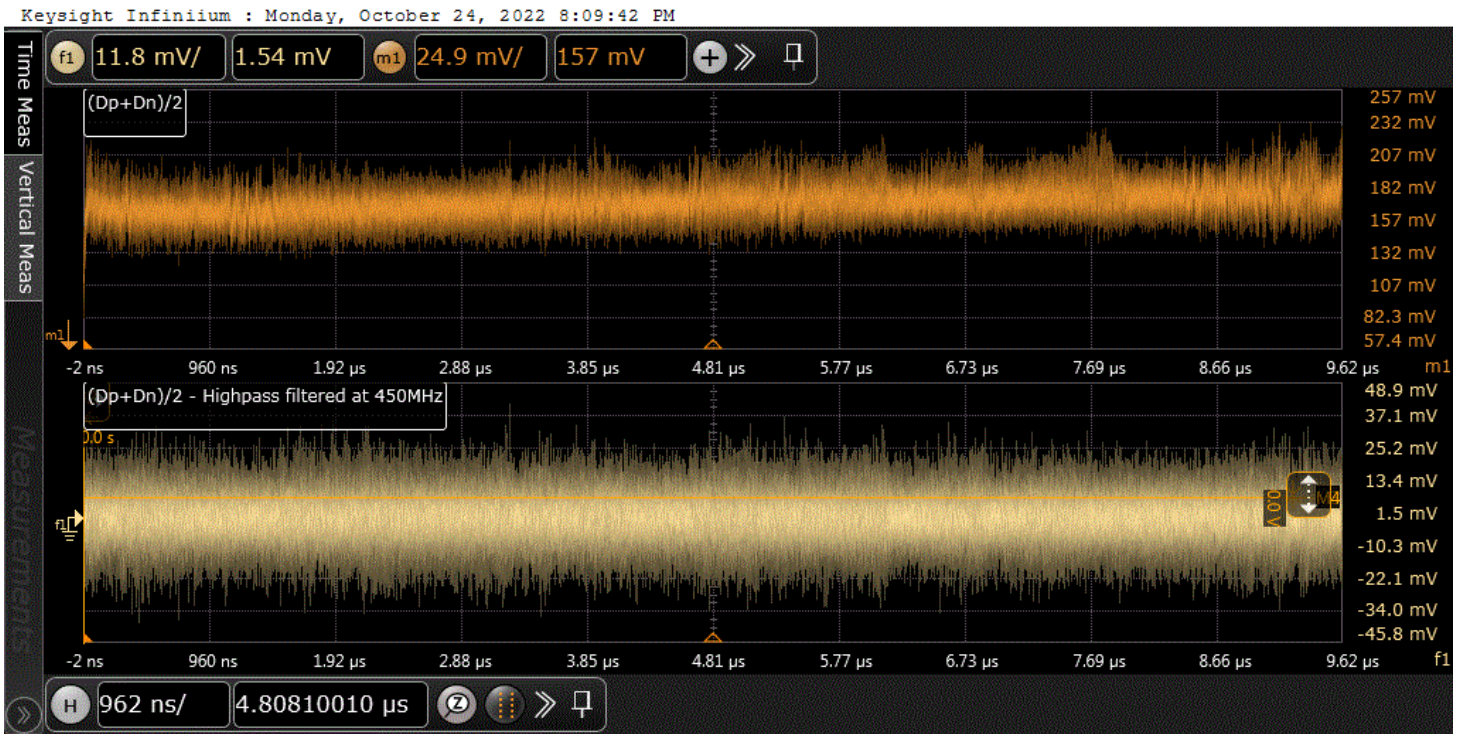
Result Details:

Result Details

Common Level Variations(>450Mhz)(See image)ZID100 ohmDataLaneLane2Number of HS Burst1

Trial 1

Trial 1: Common Level Variations(>450Mhz)



[Top](#) [Previous](#)
[Next](#)

✖ 1.3.9 HS Data TX Common-Level Variations Between 50-450MHz (VCMTX(LF)) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.3.9)

Test Summary: FAIL Test Description: Common-level variation between 50-450MHz.

Pass Limits:< 25.00 mV819_Vcmtx(LF)27.39 mV

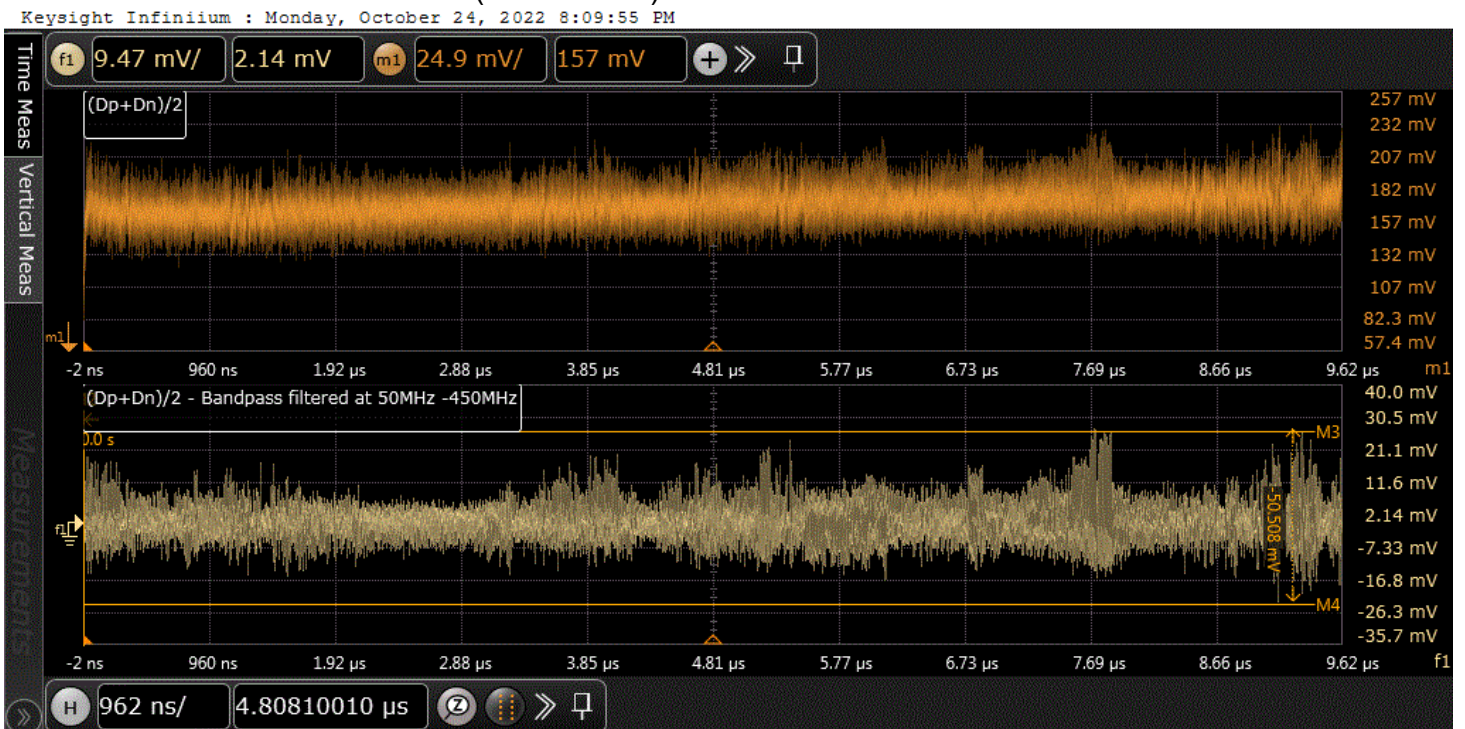
Result Details:

Result Details

Common Level Variations(50-450MHz)(See image)	ZID	100 ohm	Data	Lane	Lane2	Number of HS
Burst1						

Trial 1

Trial 1: Common Level Variations(50-450MHz)



[Top Previous](#)
[Next](#)

✓ 1.3.4 HS Data TX Differential Voltage(VOD0 Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.4)

Test Summary: Pass **Test Description:** HS transmit differential voltage. This test will measure the VOD0 based on the reference data patterns(100000) of differential signal.

Pass Limits:[-270.00 mV to -140.00 mV]8131_Vod0(Mean)-224.67 mV

Result Details:

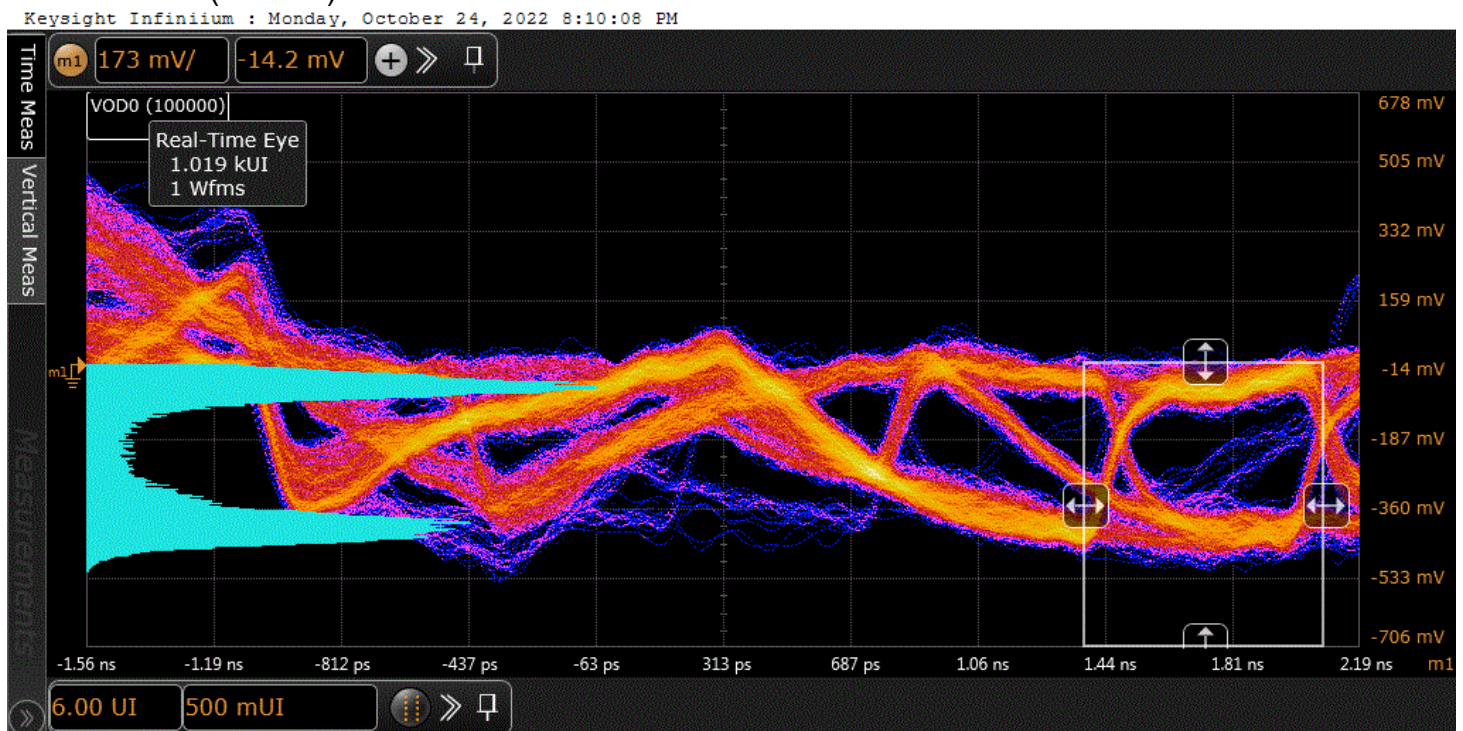
Result Details

VOD0 (100000)(See image)**Vod(Differential-0)**-224.67 mV**Number of measurement**1.019000

kZID100 ohm**DataLane**Lane2**Number of HS Burst**1

Trial 1

Trial 1: VOD0 (100000)


[Top Previous](#)
[Next](#)

✗ 1.3.4 HS Data TX Differential Voltage(VOD1 Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.4)

Test Summary: FAIL **Test Description:** HS transmit differential voltage. This test will measure the VOD1 based on the reference data patterns(011111) of differential signal.

Pass Limits:[140.00 mV to 270.00 mV]8132_Vod1(Mean)276.20 mV

Result Details:

Result Details

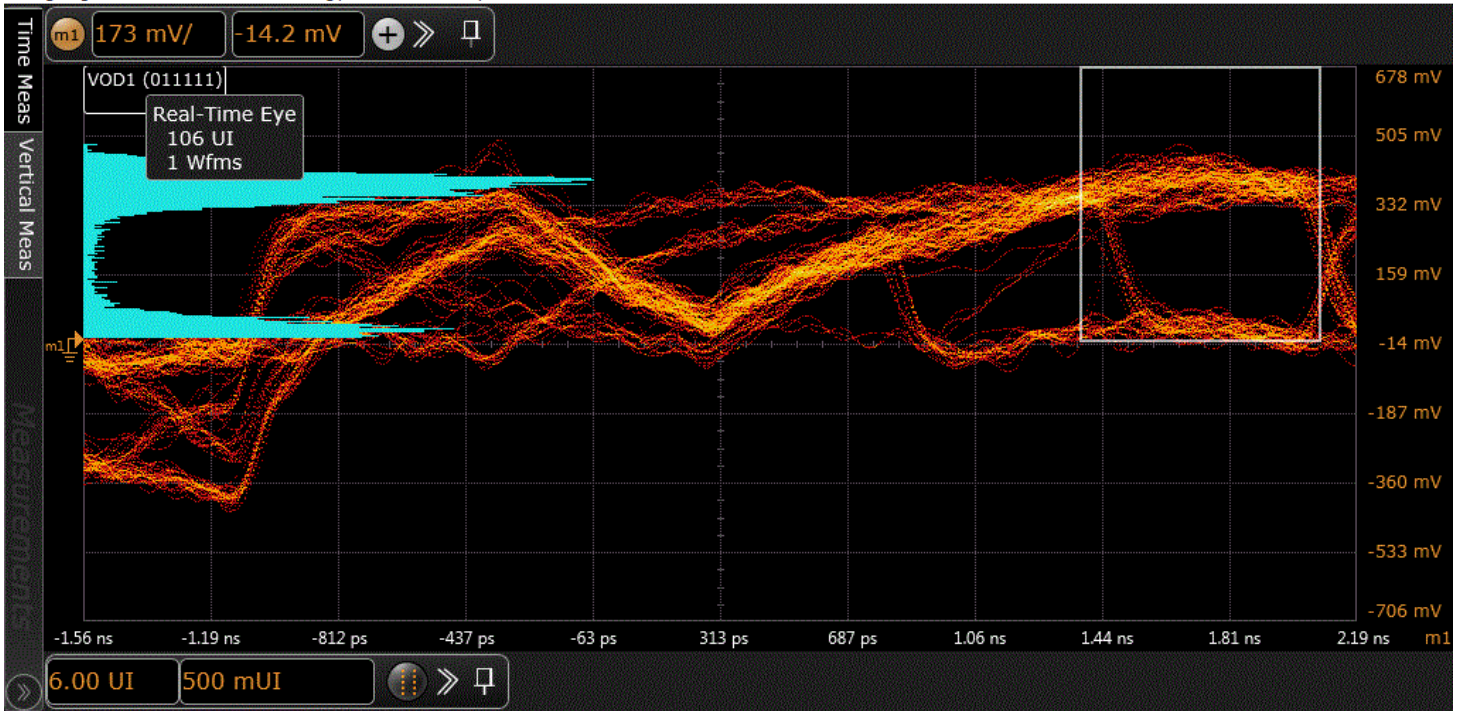
VOD1 (011111)(See image)**Vod(Differential-1)**276.20 mV**Number of measurement**106.000**ZID**100

ohm**DataLane**Lane2**Number of HS Burst**1

Trial 1

Trial 1: VOD1 (011111)

Keysight Infiniium : Monday, October 24, 2022 8:10:21 PM


[Top](#)
[Previous](#)
[Next](#)

✗ 1.3.5 HS Data TX Differential Voltage Mismatch (Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.5)

Test Summary: FAIL Test Description: VOD mismatch when output is Differential-1 or Differential-0.

Pass Limits:< 14.00 mV8141_Vod Mismatch51.54 mV

Result Details:

Result Details

Vod(Differential-1)276.20 mV**Vod(Differential-0)**-224.67 mV**ZID**100 ohm**DataLane**Lane2**Number of HS Burst**1

[Top](#)
[Previous](#)
[Next](#)

✓ 1.3.6 HS Data TX Single Ended Output High Voltage(VOHHS Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.3.6)

Test Summary: Pass Test Description: HS Single Ended output high voltage. This test will measure the VOHHS based on the reference pulses(100000) of single-ended Dp and Dn signals.

Pass Limits:<= 360.00 mV8151_Vohhs(Worst)328.47 mV

Result Details:

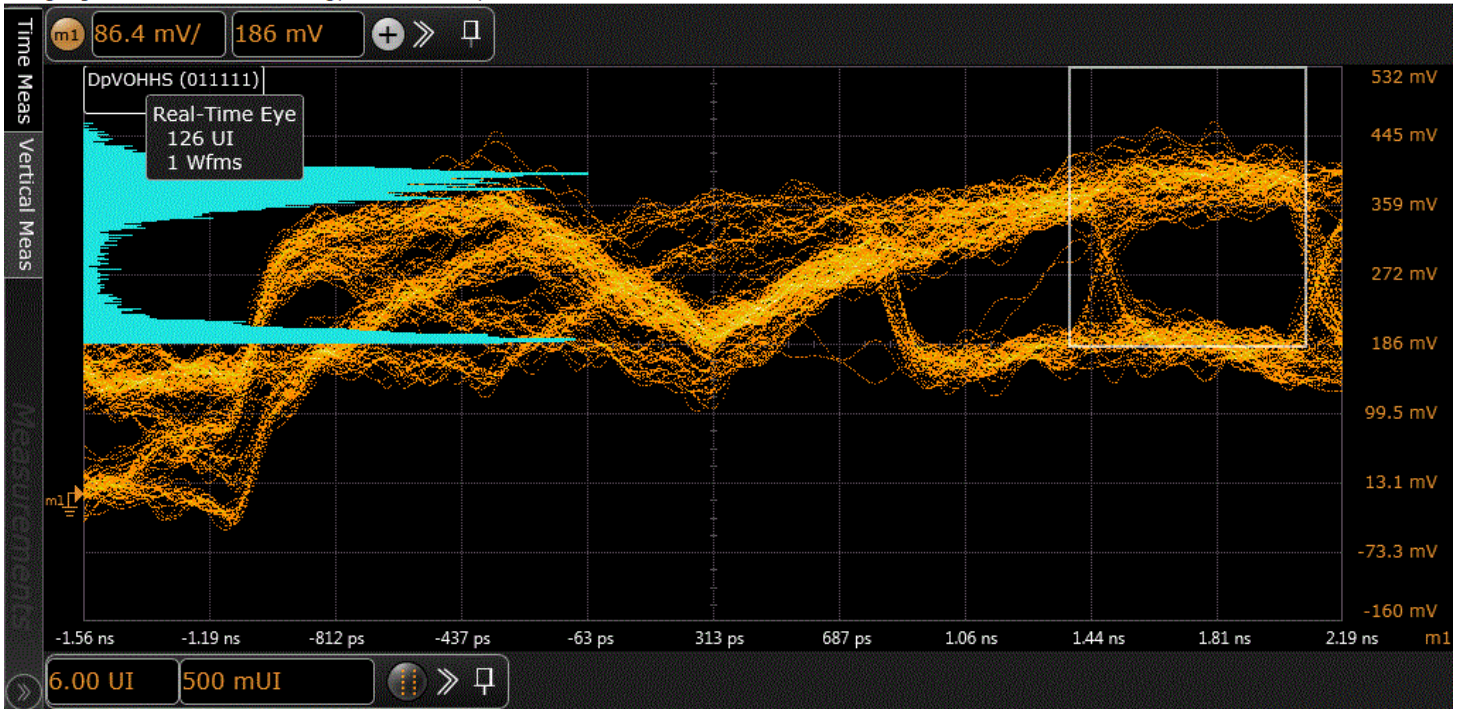
Result Details

DpVOHHS (011111)(See image)**DnVOHHS (011111)**(See image)**Vohhs(Dp)**328.47 mV**Vohhs(Dn)**313.59 mV**Number of measurement Vohhs(Dp)**126.000**Number of measurement Vohhs(Dn)**491.000**ZID**100 ohm**DataLane**Lane2**Number of HS Burst**1

Trial 1

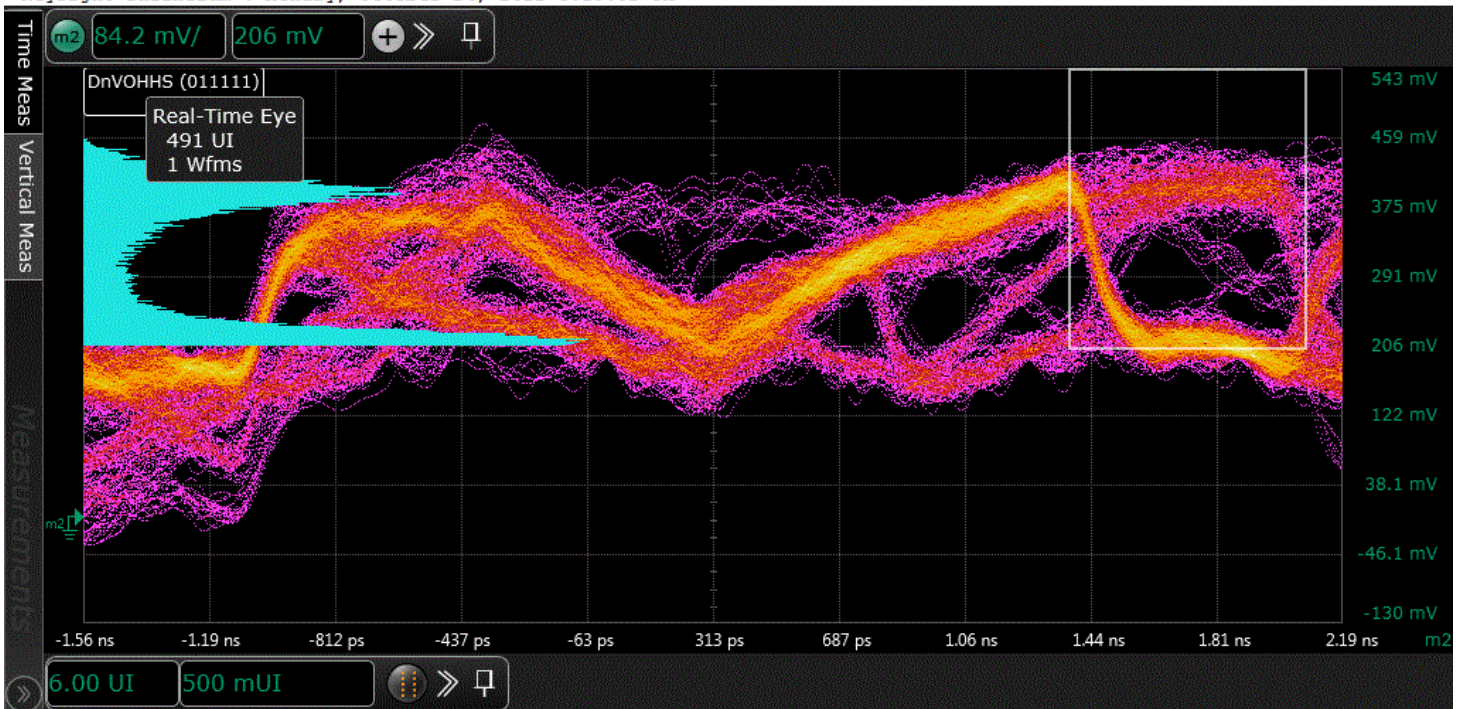
Trial 1: DpVOHHS (011111)

Keysight Infiniium : Monday, October 24, 2022 8:10:36 PM



Trial 1: DnVOHHS (011111)

Keysight Infiniium : Monday, October 24, 2022 8:10:52 PM

[Top Previous](#)[Next](#)

✓ 1.4.17 HS Clock Instantaneous (UIinst)(Max) Reference: D-PHY Specification v1.2 Section 10.1 Table 29, CTS v1.2(Test 1.4.17)

Test Summary: Pass Test Description: Maximum UI instantaneous of HS Clock.

Pass Limits: < 12.500 ns911_UIinst(Max)690 ps

Result Details:

Result Details

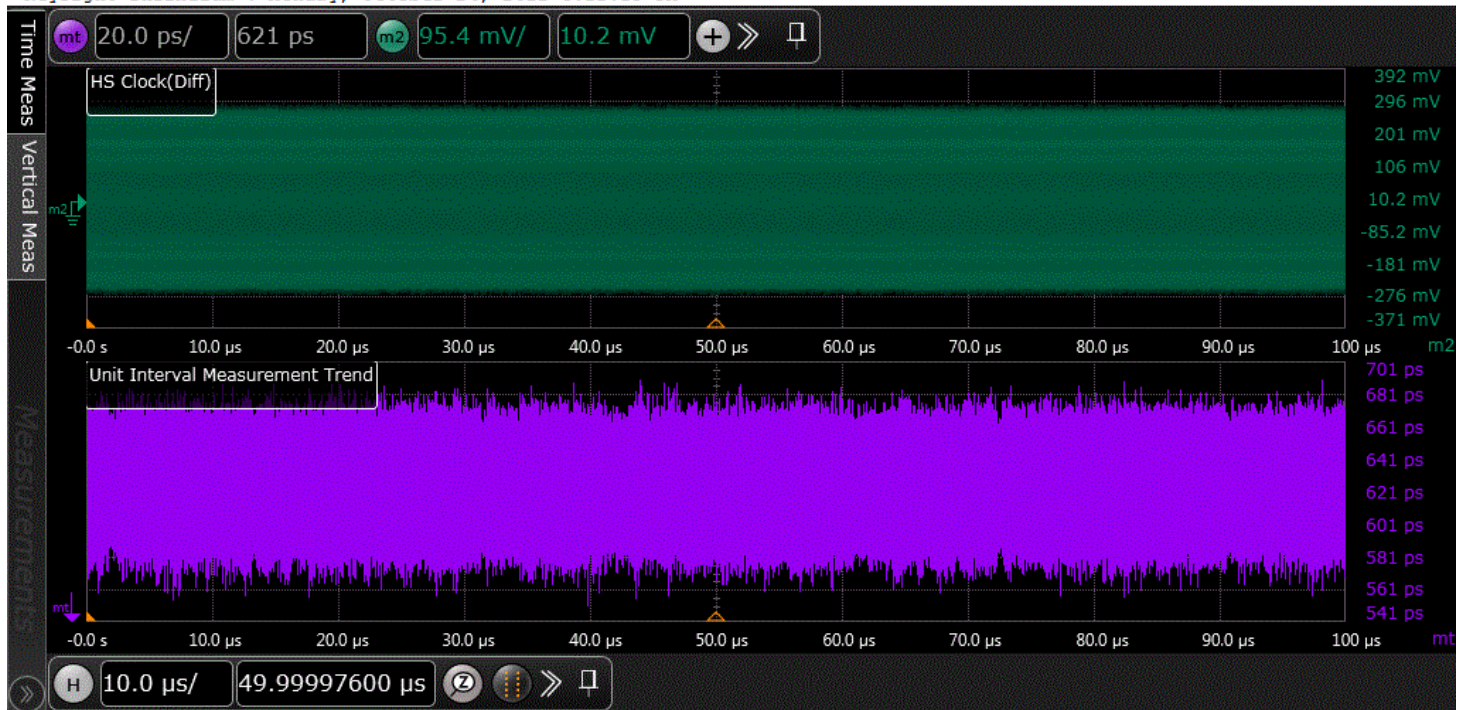
UIInst(See image)UIINST(Max)690.120 psUIINST(Mean)624.997 psNumber of UI159.999000

kZID100 ohmDataLaneLane2Number of HS Burst1

Trial 1

Trial 1: UIInst

Keysight Infiniium : Monday, October 24, 2022 8:11:18 PM

[Top Previous](#)[Next](#)

✓ 1.3.3 HS Entry: DATA TX THS-PREPARE+THS-ZERO Reference: D-PHY Specification v1.2

Section 6.9 Table 14, CTS v1.2(Test 1.3.3)

Test Summary: Pass Test Description: THS-PREPARE+Time to drive HS-0 before Sync sequence. TXTHSPrepereTHSZero_LimitMin is based on 145ns+10*UI.

Pass Limits:>= TXTHSPrepereTHSZero_LimitMin s558_THS-PREPARE+THS-ZERO183.03 ns

Result Details:

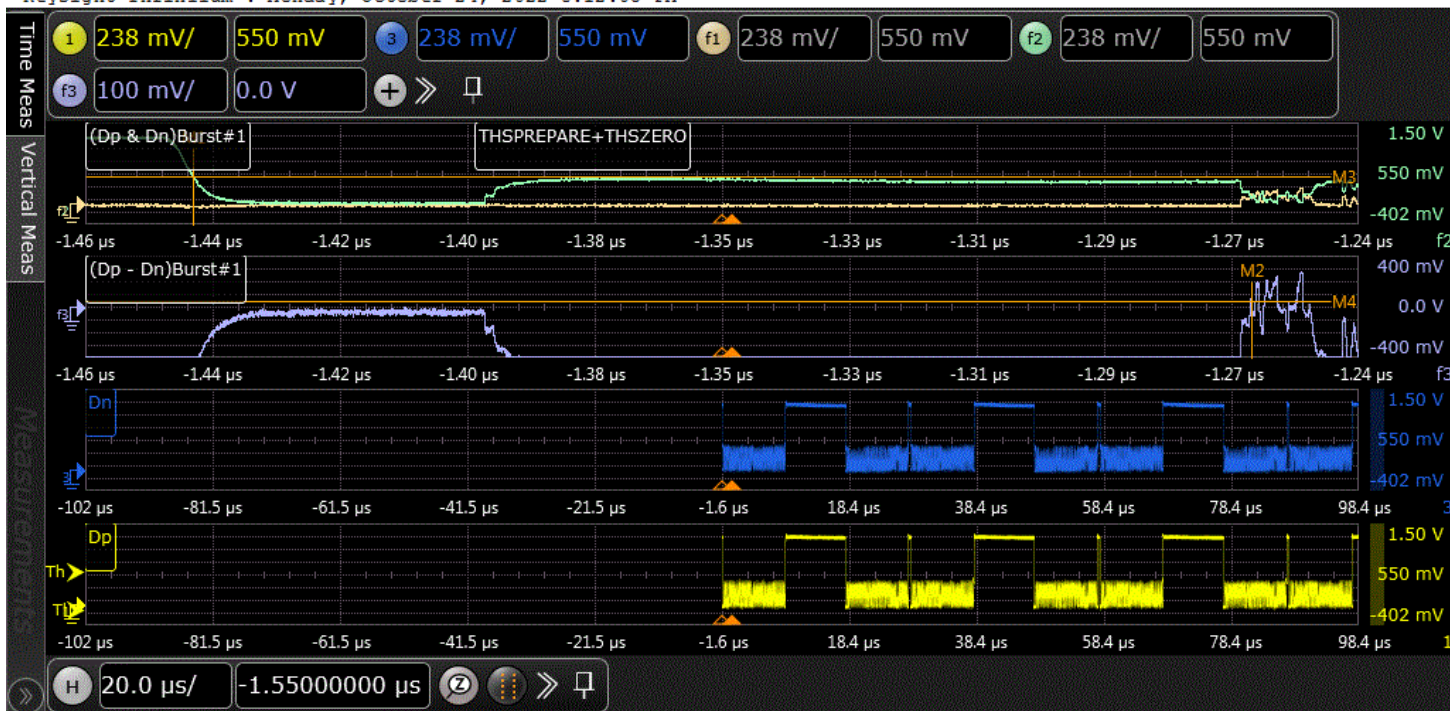
Result Details

THSPREPARE_THSZERO(See image)UIINST(Mean)624.997 psVHS_ZERO-426.39 mVZID100 ohmDataLaneLane2PassLimit Min (TXTHSPrepereTHSZero_LimitMin)151.25 nsNumber of HS burst1

Trial 1

Trial 1: THSPREPARE_THSZERO

Keysight Infiniium : Monday, October 24, 2022 8:12:03 PM


[Top Previous](#)
[Next](#)

✖ 1.3.11 HS Data TX 20%-80% Rise Time (tR)[Burst Data] Reference: D-PHY Specification v1.2
Section 9.1.1 Table 20, CTS v1.2(Test 1.3.11)

Test Summary: FAIL **Test Description:** This test is to verify that the 20%-80% rise time of the HS Differential signal is less than the maximum conformance limit(DataRiseTime_LimitMax). For CTS v1.2, the maximum conformance limit(DataRiseTime_LimitMax) is $0.3 \cdot UI$ for Datarate ≤ 1 Gbps, $0.35 \cdot UI$ for Datarate > 1 Gbps and ≤ 1.5 Gbps, $0.4 \cdot UI$ for Datarate > 1.5 Gbps. This test will measure the rise time based on the reference data patterns(000111) of differential signal. This test is applicable for Burst Data signal only. The VHS_ZERO level measured from Data Lane will be used to calculate 20/80% reference voltage for rise time measurement in this test.

Pass Limits:< DataRiseTime_LimitMax s81101_Rise Time1.644 ns

Result Details:

Result Details

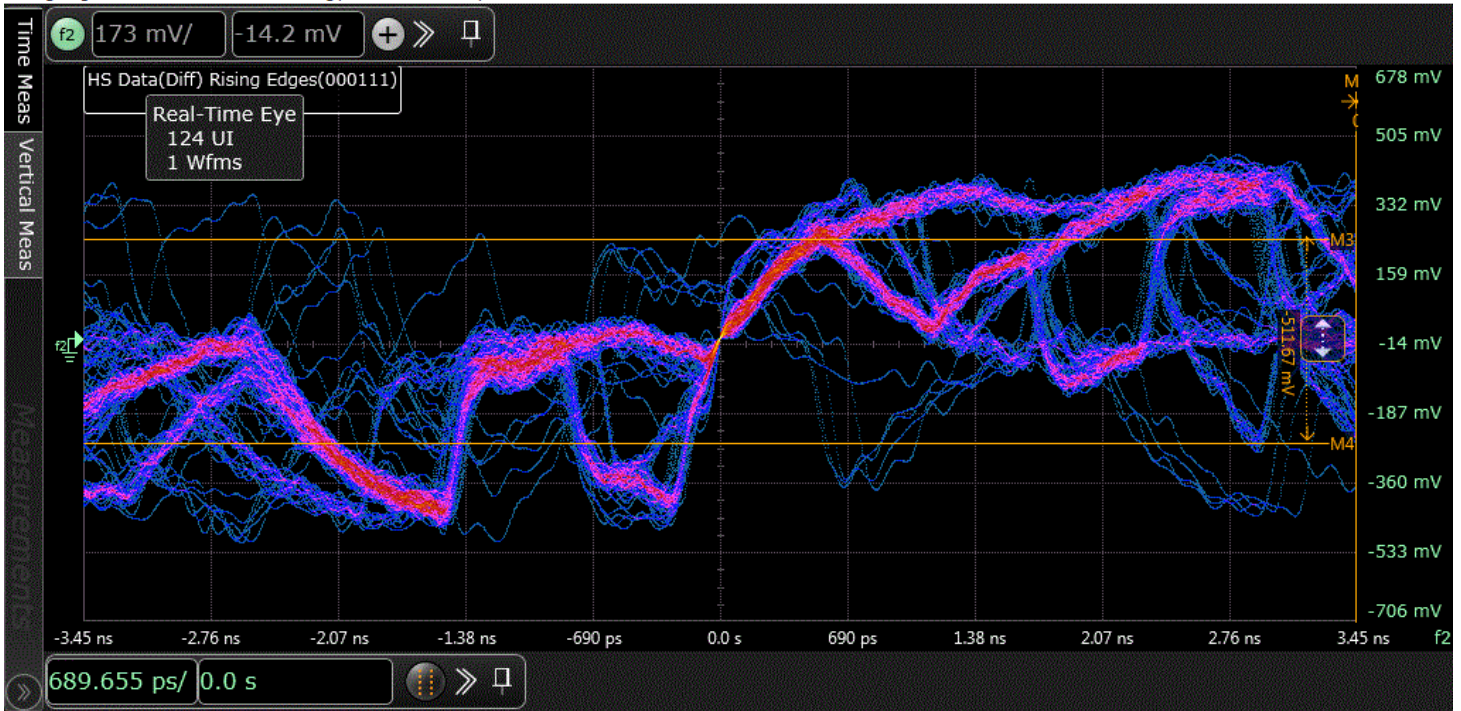
HS TX Risetime(See image)**Transition Time Measurement Lower Threshold(%)**20**Vod(1)**426.39 mV**Vod(0)**-426.39 mV**UIINST(Mean)**624.997 ps**Number of Measurement**108**ZID**100

ohm**DataLane**Lane2**PassLimit Max (DataRiseTime_LimitMax)**219 ps**Number of HS Burst**1

Trial 1

Trial 1: HS TX Risetime

Keysight Infiniium : Monday, October 24, 2022 8:12:29 PM


[Top Previous](#)
[Next](#)

✖ 1.3.12 HS Data TX 80%-20% Fall Time (tF)[Burst Data] Reference: D-PHY Specification v1.2
Section 9.1.1 Table 20, CTS v1.2 (Test 1.3.12)

Test Summary: FAIL **Test Description:** This test is to verify that the 80%-20% fall time of the HS differential signal is less than the maximum conformance limit (DataFallTime_LimitMax). For CTS v1.2, the maximum conformance limit (DataFallTime_LimitMax) is $0.3 \cdot UI$ for Datarate ≤ 1 Gbps, $0.35 \cdot UI$ for Datarate > 1 Gbps and ≤ 1.5 Gbps, $0.4 \cdot UI$ for Datarate > 1.5 Gbps. This test will measure the fall time based on the reference data patterns (111000) of differential signal. This test is applicable for Burst Data signal only. The VHS_ZERO level measured from Data Lane will be used to calculate 20/80% reference voltage for fall time measurement in this test.

Pass Limits: $< \text{DataFallTime_LimitMax}$ s81111_Fall Time 4.891 ns

Result Details:

Result Details

HS TX Falltime (See image) **Transition Time Measurement Lower Threshold (%)** 20 **Vod(1)** 426.39

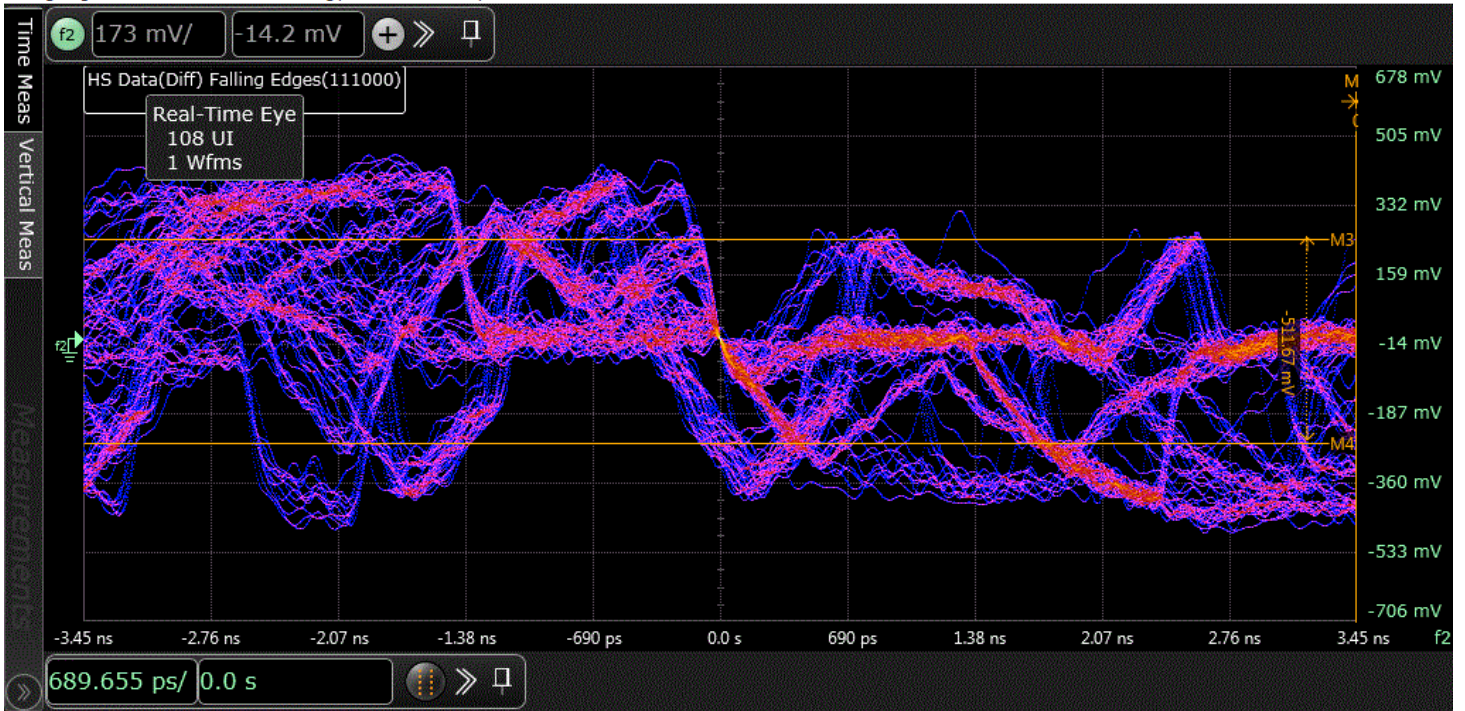
mV **Vod(0)** -426.39 mV **UI INST (Mean)** 624.997 ps **Number of Measurement** 47 **ZID** 100

ohm **Data Lane Lane 2 Pass Limit Max (DataFallTime_LimitMax)** 219 ps **Number of HS Burst** 1

Trial 1

Trial 1: HS TX Falltime

Keysight Infiniium : Monday, October 24, 2022 8:12:50 PM


[Top Previous](#)
[Next](#)

✓ 1.4.9 HS Clock TX Common-Level Variations Between 50-450MHz (VCMTX(LF)) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.4.9)

Test Summary: Pass Test Description: Common-level variation between 50-450MHz. This test is only available for CTS v1.2 and CTS v2.0 and v2.1.

Pass Limits: < 25.00 mV 1820_Vcmtx(LF) 6.11 mV

Result Details:

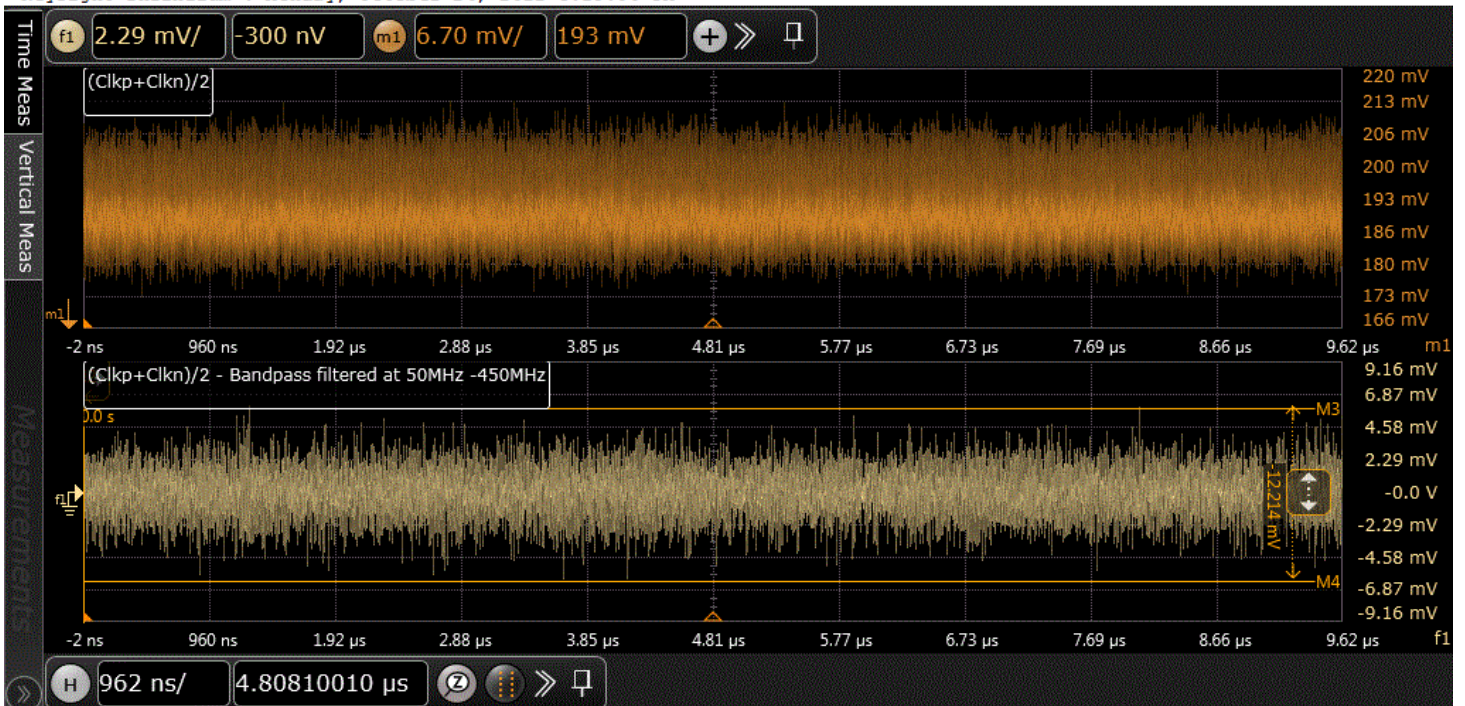
Result Details

Common Level Variations(50-450MHz)(See image) ZID100 ohm Number of HS Burst1

Trial 1

Trial 1: Common Level Variations(50-450MHz)

Keysight Infiniium : Monday, October 24, 2022 8:13:04 PM



[Top](#) [Previous](#)
[Next](#)

✓ 1.4.7 HS Clock TX Static Common Mode Voltage(Vcmtx) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.4.7)

Test Summary: Pass Test Description: HS transmit static common-mode voltage for Clock.

Pass Limits:[150.00 mV to 250.00 mV]1811_Vcmtx(Worst)189.23 mV

Result Details:

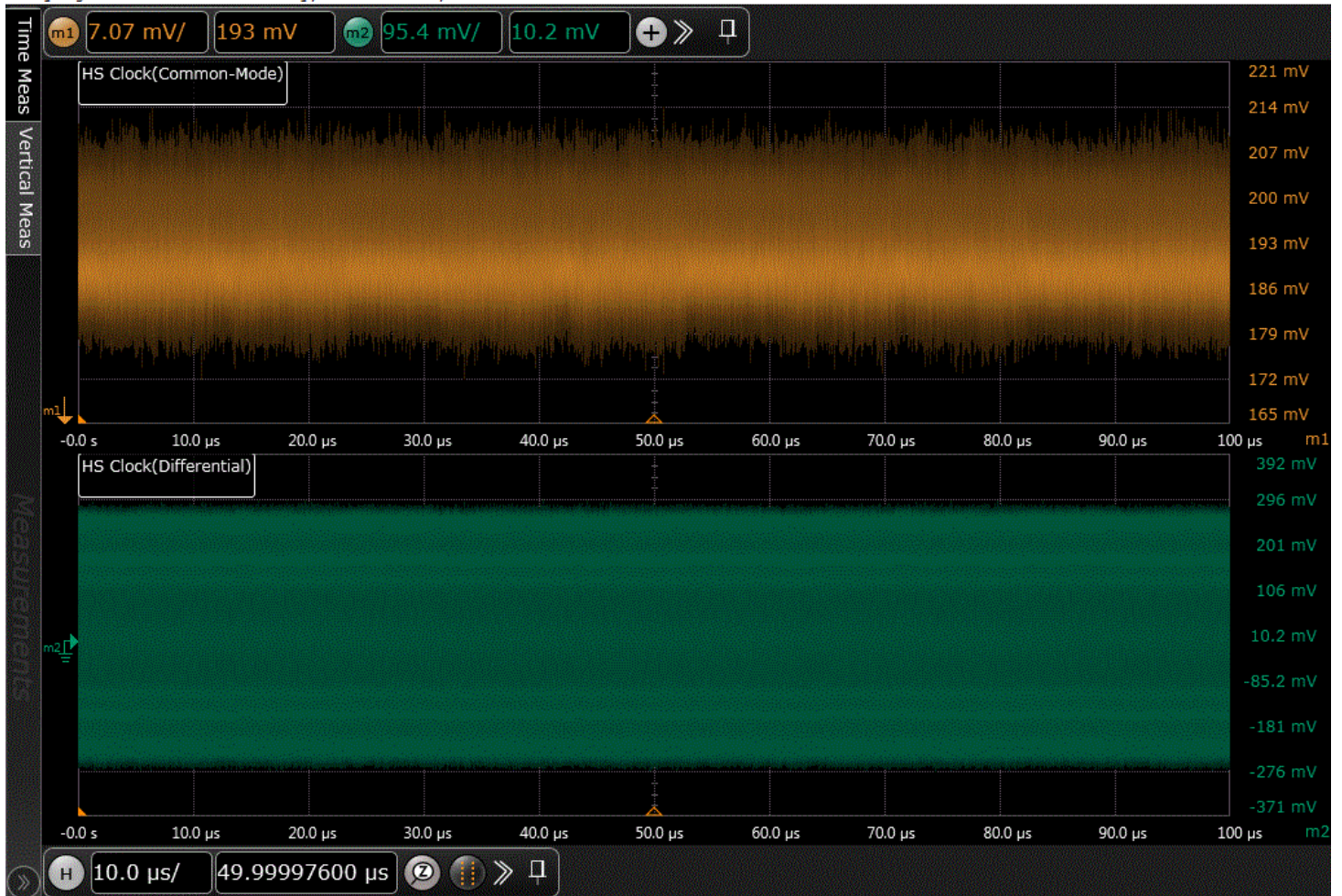
Result Details

HS Clock TX Common Mode Voltage(See image)**Vcmtx(Differential-1)**190.11 mV**Number of Vcmtx(Differential-1) Measured**79.999000 **kVcmtx(Differential-0)**189.23 mV**Number of Vcmtx(Differential-0) Measured**80.000000 **kZID**100 ohm**Number of HS Burst**1

Trial 1

Trial 1: HS Clock TX Common Mode Voltage

Keysight Infiniium : Monday, October 24, 2022 8:13:16 PM



[Top](#) [Previous](#)
[Next](#)

✓ 1.4.8 HS Clock TX Vcmtx Mismatch Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.4.8)

Test Summary: Pass Test Description: VCMTX mismatch when output is Differential-1 or Differential-0.

Pass Limits:< 5.00 mV1812_Vcmtx Mismatch440 μV

Result Details:

Result Details

Vcmtx(Differential-1)190.11 mV**Vcmtx(Differential-0)**189.23 mV**ZID**100 ohm**Number of HS Burst**1

[Top Previous](#)
[Next](#)

✓ 1.4.10 HS Clock TX Common-Level Variations Above 450MHz (VCMTX(HF)) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.4.10)

Test Summary: Pass Test Description: Common-level variations above 450MHz.

Pass Limits:< 15.00 mV1818_Vcmtx(HF)4.99 mV

Result Details:

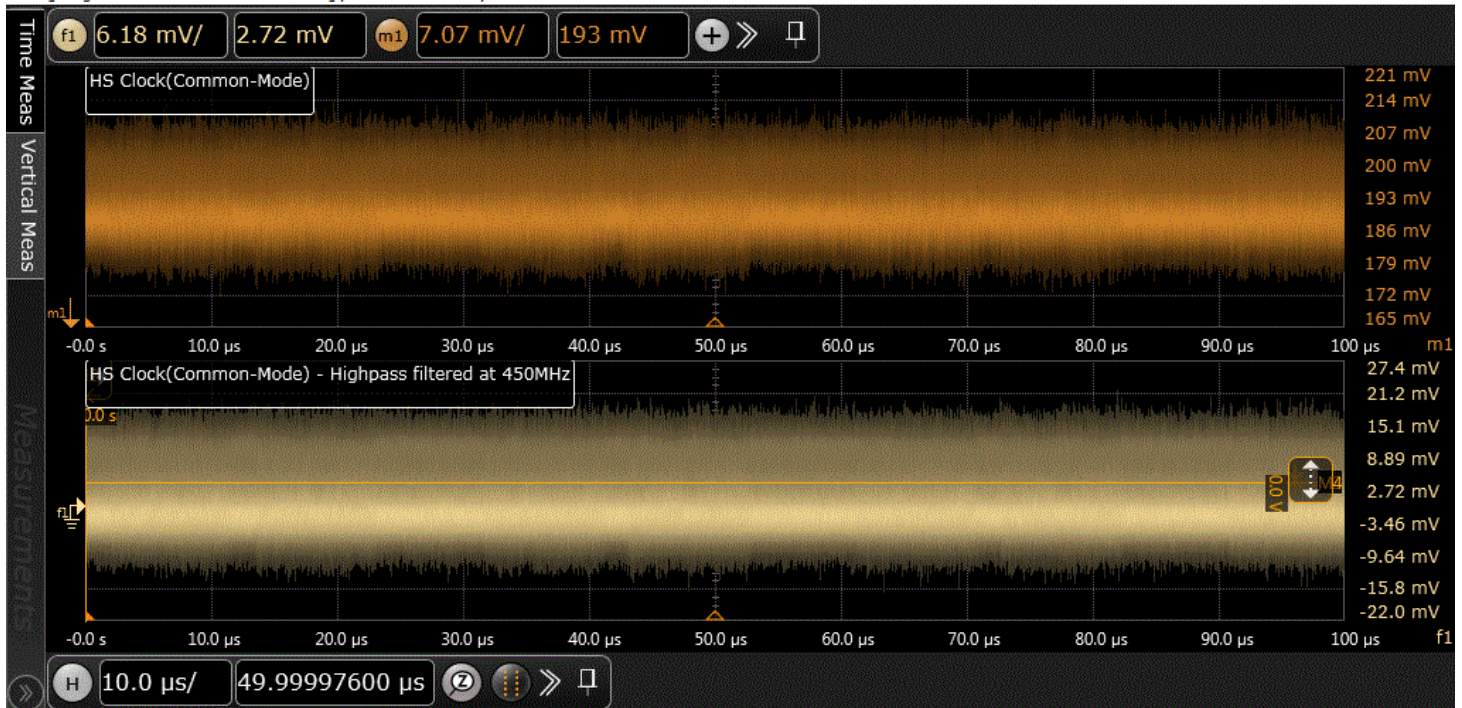
Result Details

Common Level Variations(>450Mhz)(See image)**ZID100 ohmNumber of HS Burst1**

Trial 1

Trial 1: Common Level Variations(>450Mhz)

Keysight Infiniium : Monday, October 24, 2022 8:13:30 PM


[Top Previous](#)
[Next](#)

✓ 1.4.4 HS Clock TX Differential Voltage(VOD0 Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.4.4)

Test Summary: Pass Test Description: HS clock transmitter differential voltage.

Pass Limits: [-270.00 mV to -140.00 mV]18131_Vod0(Mean)-211.13 mV

Result Details:

Result Details

Clock VOD0 (10)(See image)**Vod(Differential-0)-211.13 mVNumber of measurement80.000000**

kZID100 ohmNumber of HS Burst1

Trial 1

Trial 1: Clock VOD0 (10)

Keysight Infiniium : Monday, October 24, 2022 8:13:47 PM


[Top](#)
[Previous](#)
[Next](#)

✓ 1.4.4 HS Clock TX Differential Voltage(VOD1 Pulse) Reference: D-PHY Specification v1.2 Section 9.1.1 Table 19, CTS v1.2(Test 1.4.4)

Test Summary: Pass Test Description: HS clock transmitter differential voltage.

Pass Limits:[140.00 mV to 270.00 mV]18132_Vod1(Mean)206.63 mV

Result Details:

Result Details

Clock VOD1 (01)(See image)**Vod(Differential-1)**206.63 mV**Number of measurement**80.000000
kZID100 ohm**Number of HS Burst**1

Trial 1

Trial 1: Clock VOD1 (01)

Keysight Infiniium : Monday, October 24, 2022 8:14:04 PM



Next

Test Summary: Pass Test Description: VOD mismatch when output is Differential-1 or Differential-0.

Result Details:

Result Details

Vod(Differential-1)206.63 mV Vod(Differential-0)-211.13 mV ZID100 ohm Number of HS Burst1

Next

Test Summary: Pass Test Description: HS Single Ended output high voltage.

Result Details:

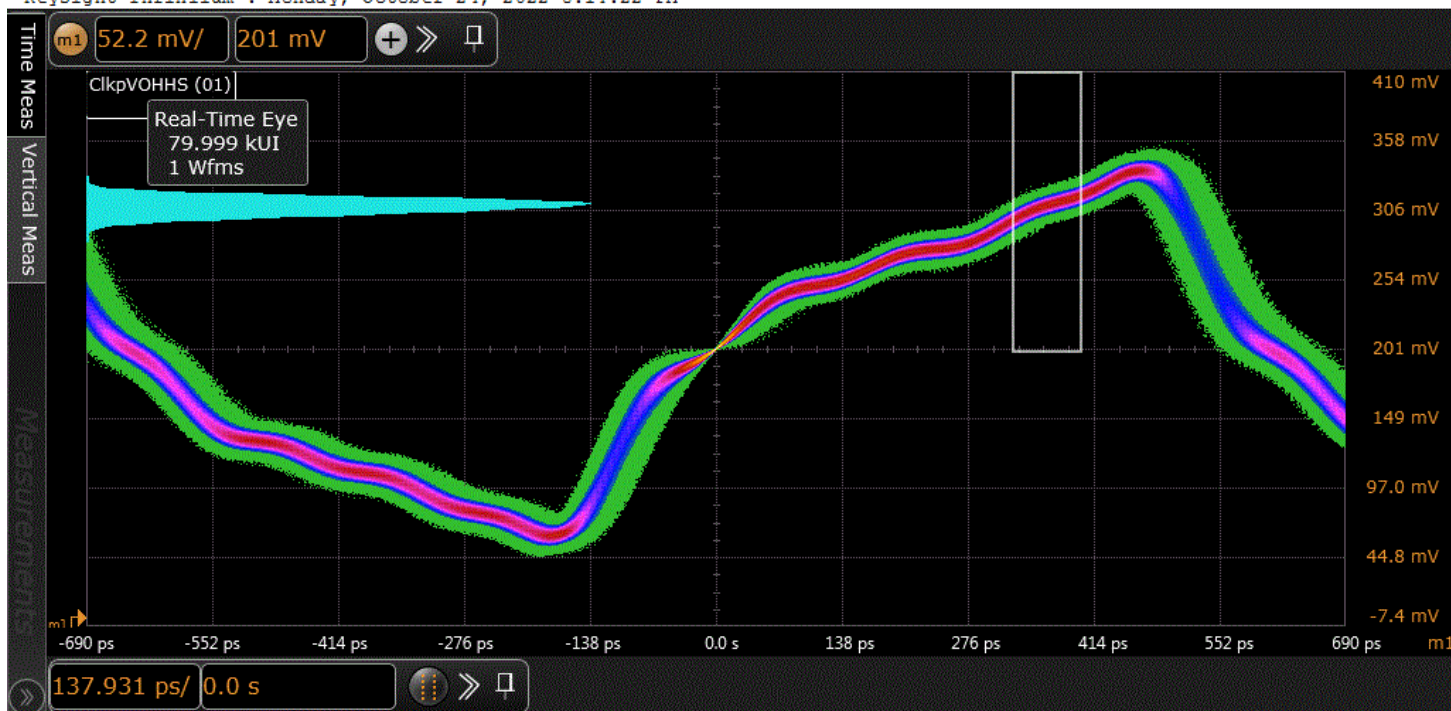
Result Details

ClkpVOHHS (01)(See image)**ClknVOHHS (01)**(See image)**Vohhs(Clkp)**309.03
mV**Vohhs(Clkn)**294.81 mV**Number of measurement Vohhs(Clkp)**79.999000 k**Number of measurement Vohhs(Clkn)**80.000000 k**ZID**100 ohm**Number of HS Burst**1

Trial 1

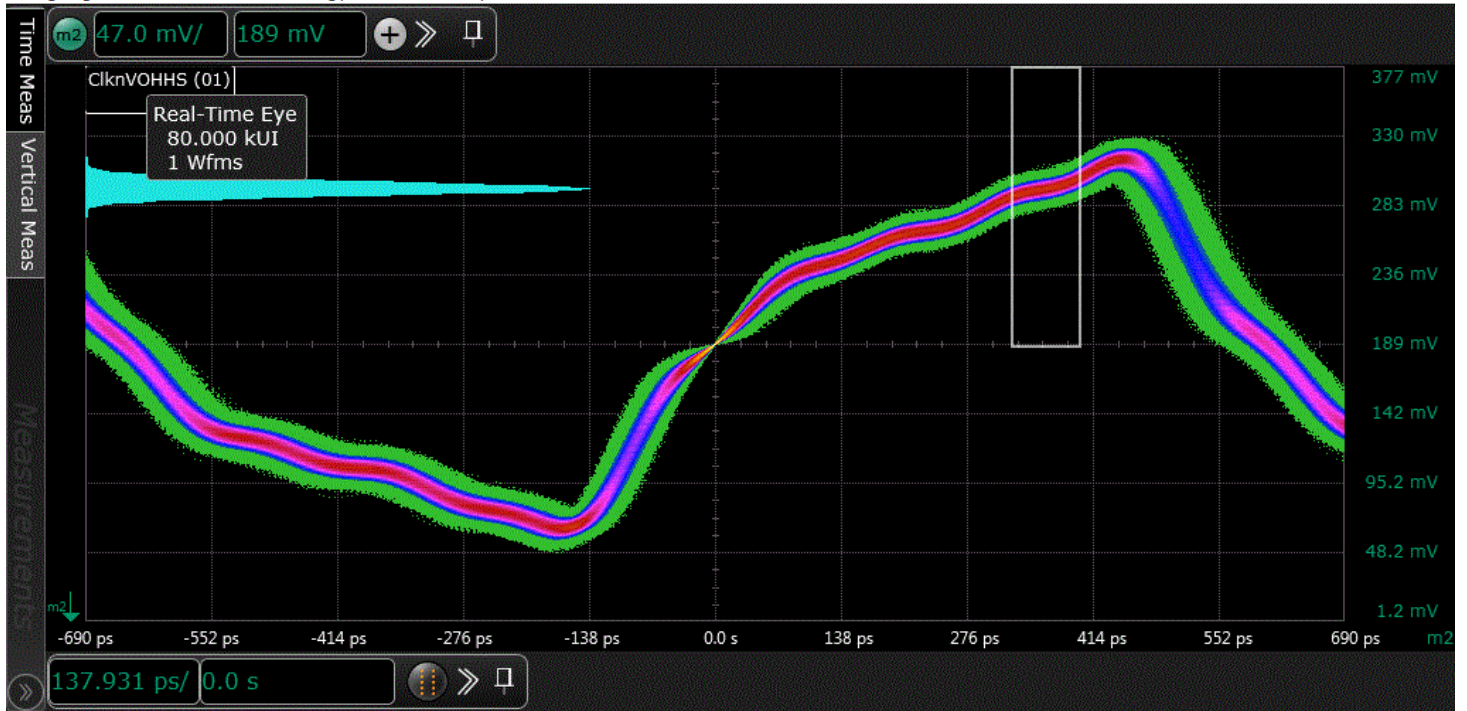
Trial 1: ClkpVOHHS (01)

Keysight Infiniium : Monday, October 24, 2022 8:14:22 PM



Trial 1: ClknVOHHS (01)

Keysight Infiniium : Monday, October 24, 2022 8:14:40 PM


[Top Previous](#)
[Next](#)

✖ 1.4.11 HS Clock TX 20%-80% Rise Time (tR)[Continuous Clock, Burst Data] Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.4.11)

Test Summary: FAIL **Test Description:** This test is to verify that the 20%-80% rise time of the HS differential signal is less than the maximum conformance limit(CLK RiseTime_LimitMax). For CTS v1.2, the maximum conformance limit(CLK RiseTime_LimitMax) is $0.3 \cdot UI$ for Datarate ≤ 1 Gbps, $0.35 \cdot UI$ for Datarate > 1 Gbps and ≤ 1.5 Gbps, $0.4 \cdot UI$ for Datarate > 1.5 Gbps. This test is applicable for Continuous Clock and Burst Data signal only. The VHS_ZERO level measured from Data Lane will be used to calculate 20/80% reference voltage for rise time measurement in this test.

Pass Limits: $< CLK RiseTime_LimitMax$ s181102_Rise Time598 ps

Result Details:

Result Details

HS Clock TX Risetime(See image)Transition Time Measurement Lower

Threshold(%)20 **Vod(1)**426.39 mV **Vod(0)**-426.39 mV **UIINST(Mean)**624.997 ps **Number of Measurement**29.850 k **ZID**100 ohm **PassLimit Max (CLK RiseTime_LimitMax)**219 ps **Number of HS Burst**1

Trial 1

Trial 1: HS Clock TX Risetime

Keysight Infiniium : Monday, October 24, 2022 8:15:07 PM


[Top Previous](#)
[Next](#)

✖ 1.4.12 HS Clock TX 80%-20% Fall Time (tF)[Continuous Clock, Burst Data] Reference: D-PHY Specification v1.2 Section 9.1.1 Table 20, CTS v1.2(Test 1.4.12)

Test Summary: FAIL **Test Description:** This test is to verify that the 80%-20% fall time of the HS differential signal is less than the maximum conformance limit(CLKFallTime_LimitMax). For CTS v1.2, the maximum conformance limit(CLKFallTime_LimitMax) is $0.3 \cdot UI$ for Datarate ≤ 1 Gbps, $0.35 \cdot UI$ for Datarate > 1 Gbps and ≤ 1.5 Gbps, $0.4 \cdot UI$ for Datarate > 1.5 Gbps. This test is applicable for Continuous Clock and Burst Data signal only. The VHS_ZERO level measured from Data Lane will be used to calculate 20/80% reference voltage for fall time measurement in this test.

Pass Limits: < CLKFallTime_LimitMax s181112_Fall Time573 ps

Result Details:

Result Details

HS Clock TX Falltime(See image)Transition Time Measurement Lower

Threshold(%)20Vod(1)426.39 mVVod(0)-426.39 mVUIINST(Mean)624.997 psNumber of Measurement29.851 kZID100 ohmPassLimit Max (CLKFallTime_LimitMax)219 psNumber of HS Burst1

Trial 1

Trial 1: HS Clock TX Falltime

Keysight Infiniium : Monday, October 24, 2022 8:15:35 PM

[Top Previous](#)[Next](#)

✓ 1.4.17 HS Clock Instantaneous (U_{inst})(Min) Reference: D-PHY Specification v1.2 Section 10.1 Table 29, CTS v1.2(Test 1.4.17)

Test Summary: Pass Test Description: Minimum UI instantaneous (Min) of HS Clock.

Pass Limits: $\geq$ U_{inst_Min_Limit} s914_U_{inst}(Min)552 ps

Result Details:

Result Details

UIINST(Min)551.870 psUIINST(Mean)624.997 psNumber of UI159.999000 kZID100

ohmDataLaneLane2PassLimit Min (U_{inst_Min_Limit})0.000000000000 sNumber of HS Burst1

[Top Previous](#)[Next](#)

✗ 1.4.18 Clock Lane HS Clock Delta UI (UI variation) Reference: D-PHY Specification v1.2 Section 10.1 Table 29, CTS v1.2(Test 1.4.18)

Test Summary: FAIL Test Description: This test is to verify that the Clock lane HS Clock Delta UI is in between the minimum conformance limit(U_{Variant_Limit_Min}) and the maximum conformance limit(U_{Variant_Limit_Max}). For CTS v1.1, U_{Variant_Limit_Min} is -10% and U_{Variant_Limit_Max} is 10% for Datarate $\leq$ 1Gbps, U_{Variant_Limit_Min} is -5% and U_{Variant_Limit_Max} is 5% for Datarate > 1Gbps. For CTS v1.2, U_{Variant_Limit_Min} is -10% and U_{Variant_Limit_Max} is 10% for Datarate $\leq$ 1Gbps, U_{Variant_Limit_Min} is -5% and U_{Variant_Limit_Max} is 5% for Datarate > 1Gbps and $\leq$ 1.5Gbps .

Pass Limits:[U_{Variant_Limit_Min} % to U_{Variant_Limit_Max} %]1911_U_{Variant}(Min)-11.70 %

Result Details:

Result Details

UIINST(Min)551.870 psUIINST(Max)690.120 psUIINST(Mean)624.997 psNumber of UI159.999000

kDataLaneLane2UIVariant_min-11.700 %UIVariant_max10.420 %ZID100 ohmPassLimit Min (U_{Variant_Limit_Min})-5.00 %PassLimit Max (U_{Variant_Limit_Max})5.00 %Number of HS Burst1

[Top Previous](#)[Next](#)

✓ 1.3.1 HS Entry: DATA TLPX Reference: D-PHY Specification v1.2 Section 6.9 Table 14, CTS v1.2(Test 1.3.1)

Test Summary: Pass Test Description: Length of any Low-Power state period.

Pass Limits: ≥ 50.00 ns511_TLPX55.26 ns

Result Details:

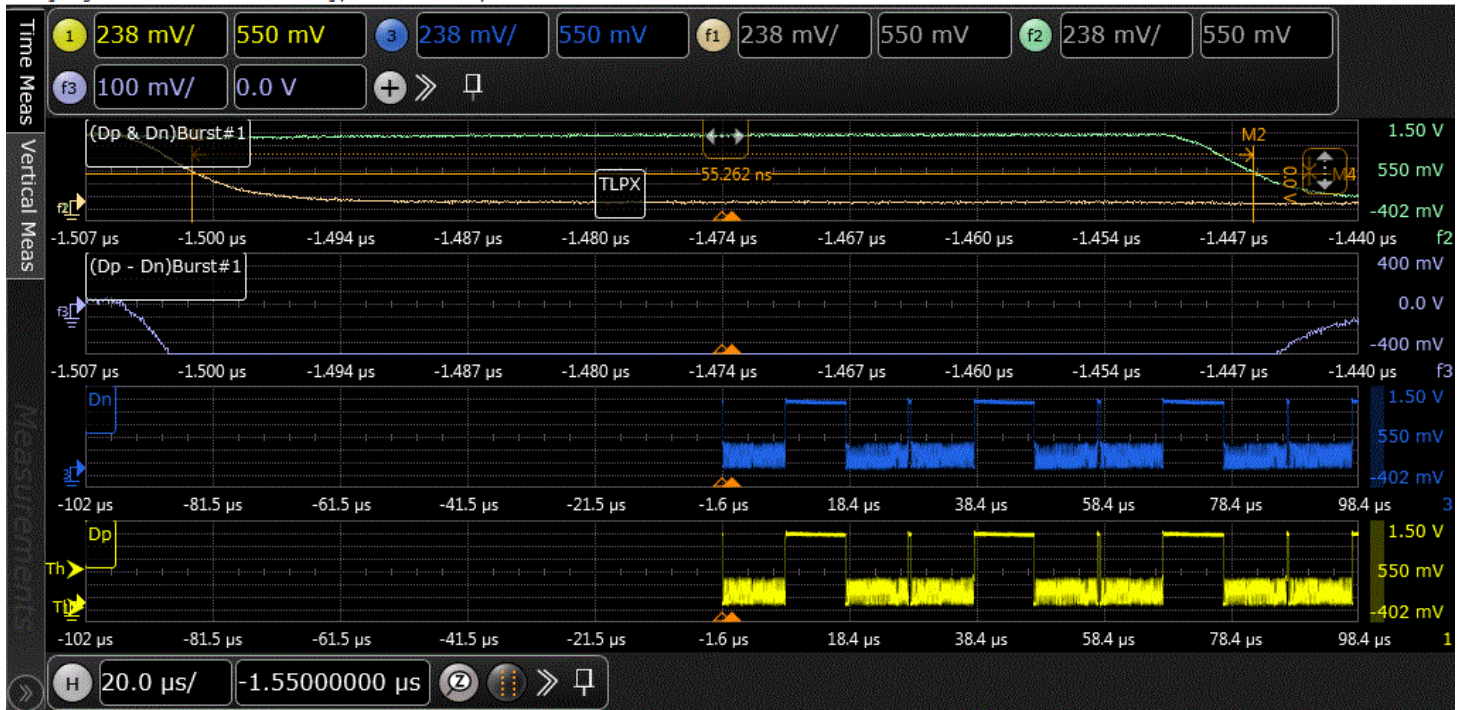
Result Details

TLPX(See image)ZID100 ohmDataLaneLane2Number of HS burst1

Trial 1

Trial 1: TLPX

Keysight Infiniium : Monday, October 24, 2022 8:11:54 PM



[Top Previous](#)
[Next](#)

✓ 1.3.2 HS Entry: DATA TX THS-PREPARE Reference: D-PHY Specification v1.2 Section 6.9 Table 14, CTS v1.2(Test 1.3.2)

Test Summary: Pass Test Description: Time to drive LP-00 to prepare for HS Transmission.

THSPrepare_LimitMin is based on $40\text{ns} + 4 \cdot U_I$. THSPrepare_LimitMax is based on $85\text{ns} + 6 \cdot U_I$.

Pass Limits:[THSPrepare_LimitMin s to THSPrepare_LimitMax s]557_THS-PREPARE50.48 ns

Result Details:

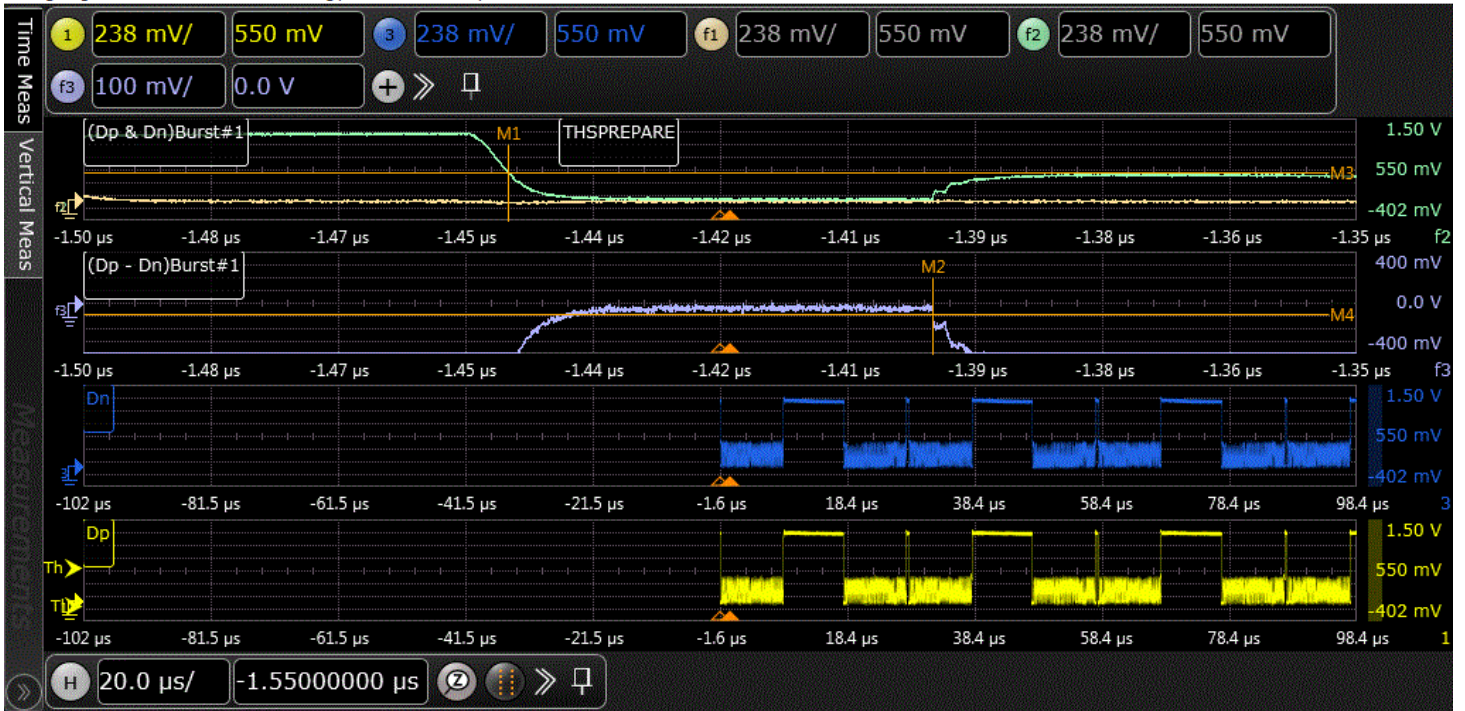
Result Details

THSPREPRE(See image)UIINST(Mean)624.997 psZID100 ohmDataLaneLane2PassLimit Min (THSPrepare_LimitMin)42.50 nsPassLimit Max (THSPrepare_LimitMax)88.75 nsNumber of HS burst1

Trial 1

Trial 1: THSPREPRE

Keysight Infiniium : Monday, October 24, 2022 8:11:58 PM


[Top Previous](#)
[Next](#)

✓ 1.3.13 HS Exit: DATA TX THS-TRAIL Reference: D-PHY Specification v1.2 Section 6.9 Table 14, CTS v1.2(Test 1.3.13)

Test Summary: Pass **Test Description:** Time to drive flipped differential state after last payload data bit of a HS transmission burst. TXTHSTrail_LimitMin is based on $60\text{ns} + n \cdot 4 \cdot \text{UI}$. The THS-SKIP parameter is useful to avoid glitch problem during THS-TRAIL measurement. Any transition on the Data Lane in THS-SKIP time interval will be ignored when finding last payload data bit of HS transmission burst. The default value of THS-SKIP is set to 0s to prevent invalid THS-TRAIL measurement.

Pass Limits: $\geq$ TXTHSTrail_LimitMin s546_THS-TRAIL84.13 ns

Result Details:

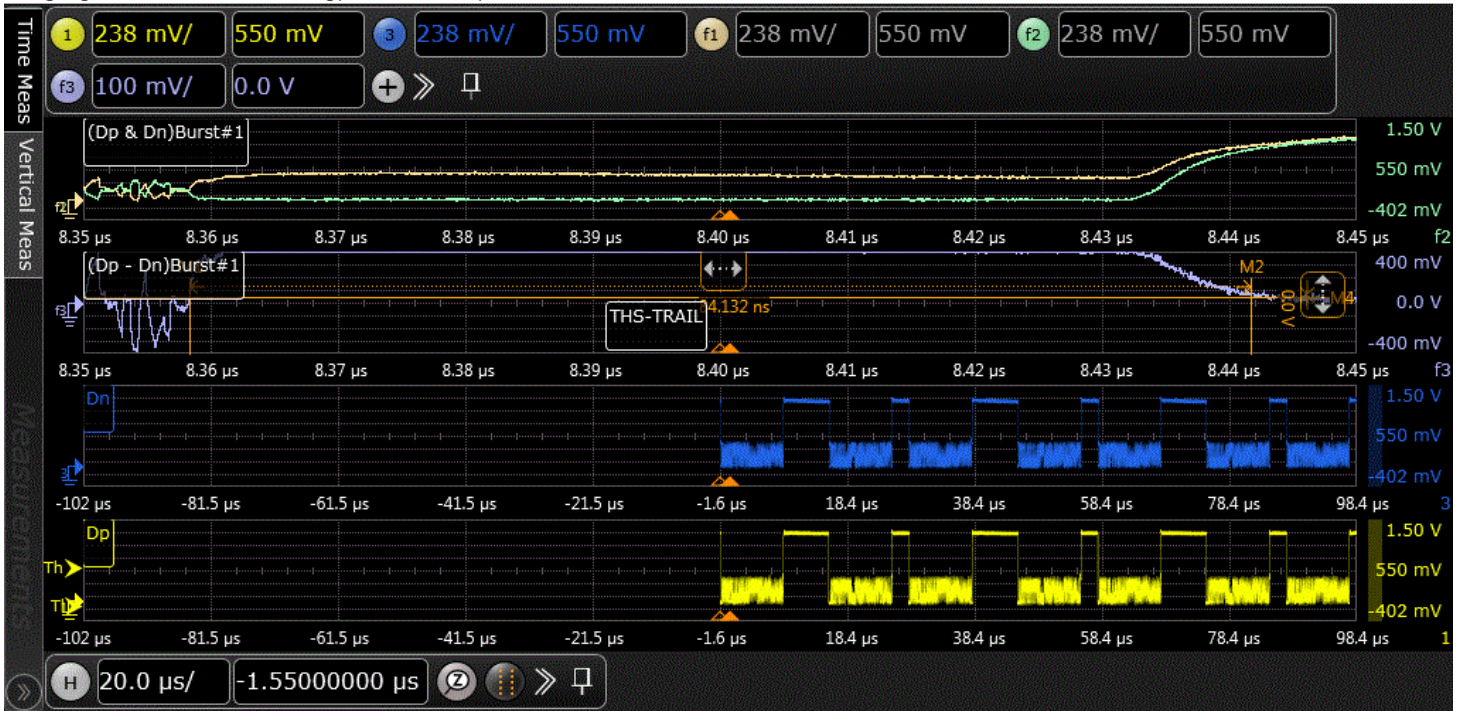
Result Details

THSTRAIL(See image)**THS-SKIP(s)**0.00000000000000 s**ZID**100 ohm**DataLane**Lane2**PassLimit Min**
(TXTHSTrail_LimitMin)62.76 ns**Number of HS burst**1

Trial 1

Trial 1: THSTRAIL

Keysight Infiniium : Monday, October 24, 2022 8:16:14 PM


[Top Previous](#)
[Next](#)

✓ 1.3.14 HS Exit: DATA TX TREOT Reference: D-PHY Specification v1.2 Section 9.1.2 Table 22, CTS v1.2(Test 1.3.14)

Test Summary: Pass Test Description: 30%-85% rise time and fall time

Pass Limits: ≤ 35.00 ns549_TREOT-3.56 ns

Result Details:

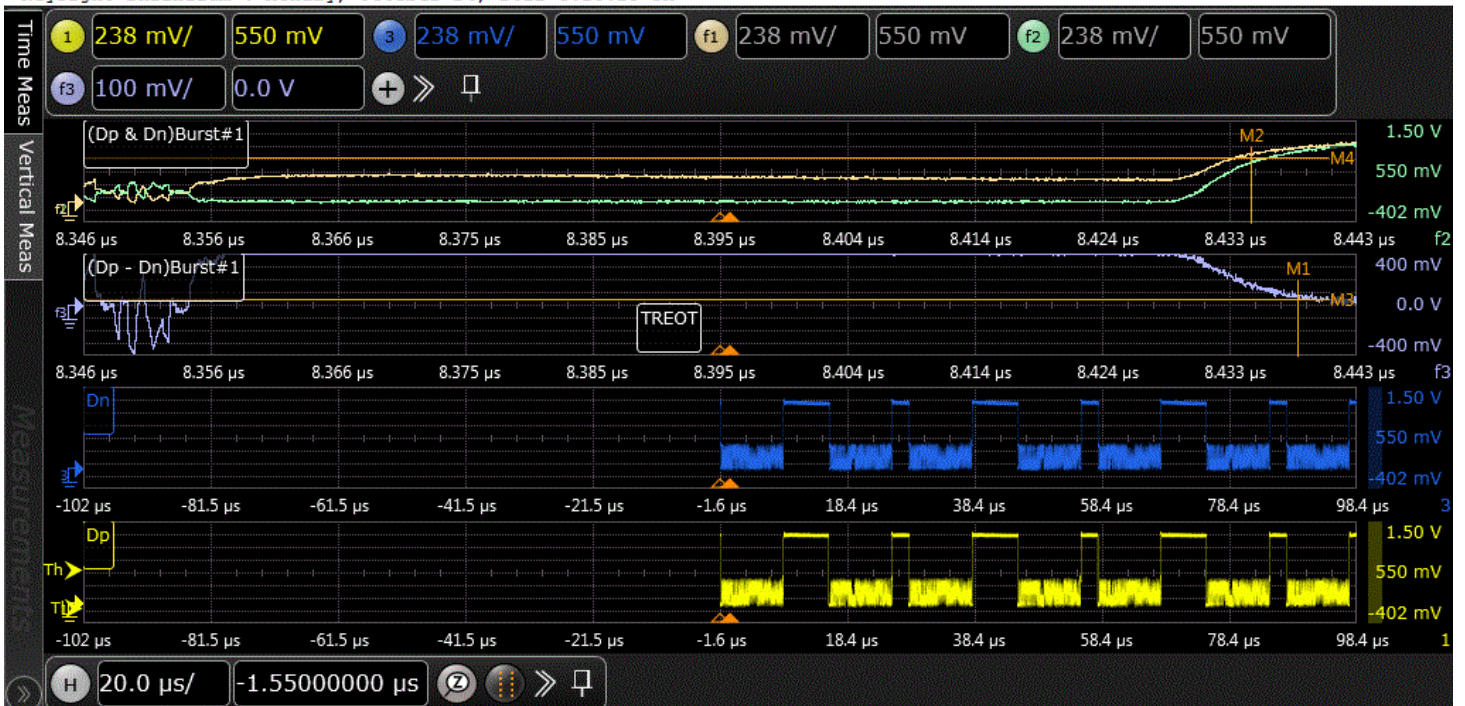
Result Details

TREOT(See image)ZID100 ohmDataLaneLane2Number of HS burst1

Trial 1

Trial 1: TREOT

Keysight Infiniium : Monday, October 24, 2022 8:16:23 PM



[Top Previous](#)
[Next](#)

✓ 1.3.15 HS Exit: DATA TX TEOT Reference: D-PHY Specification v1.2 Section 6.9 Table 14, CTS v1.2(Test 1.3.15)

Test Summary: Pass **Test Description:** Time from start of THS-TRAIL period to start of LP-11 state. TXTEOT_LimitMax is based on $105\text{ns} + n \cdot 12 \cdot \text{UI}$.

Pass Limits: $\leq \text{TXTEOT_LimitMax}$ s547_TEOT80.57 ns

Result Details:

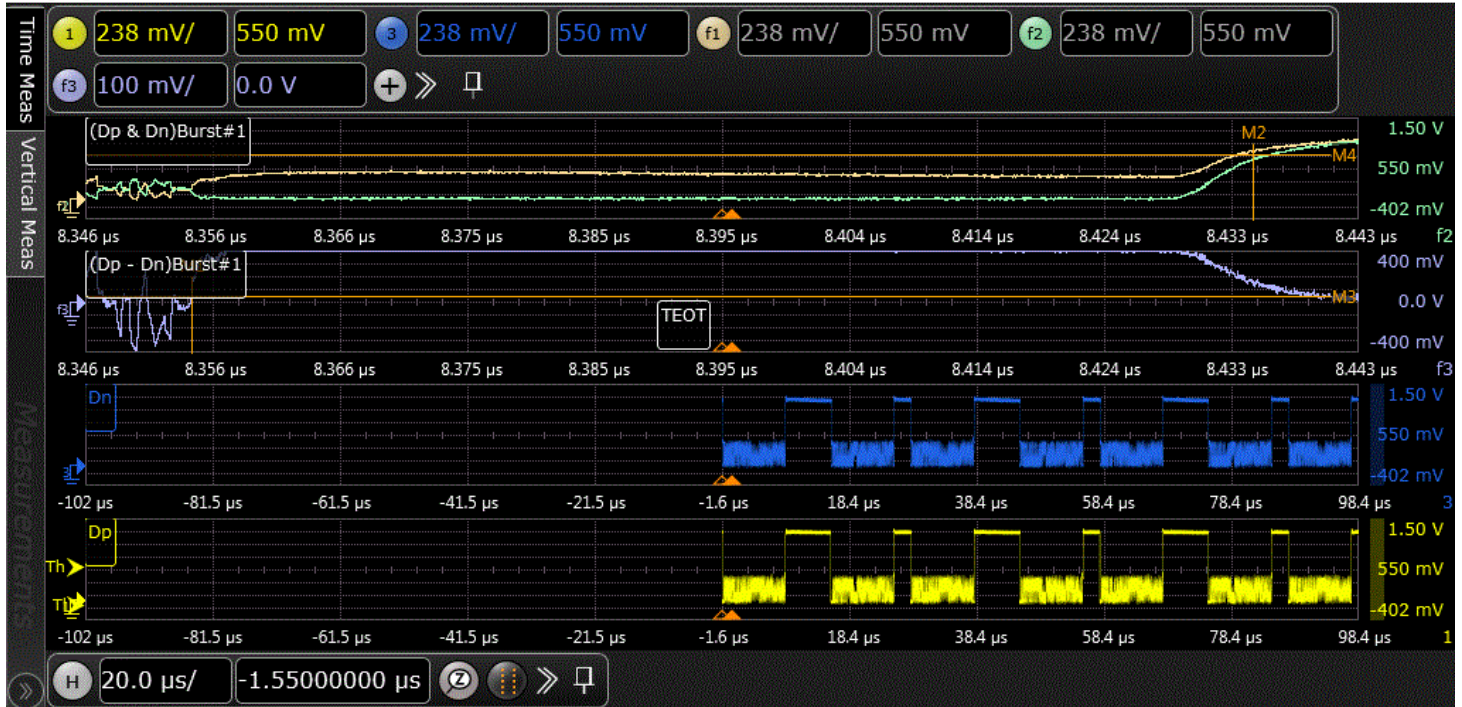
Result Details

TEOT(See image)**UIINST(Mean)**624.997 ps**ZID**100 ohm**DataLane**Lane2**PassLimit Max**
(TXTEOT_LimitMax)112.50 ns**Number of HS burst**1

Trial 1

Trial 1: TEOT

Keysight Infiniium : Monday, October 24, 2022 8:16:19 PM


[Top Previous](#)
[Next](#)

✓ 1.3.16 HS Exit: DATA TX THS-EXIT Reference: D-PHY Specification v1.2 Section 6.9 Table 14, CTS v1.2(Test 1.3.16)

Test Summary: Pass **Test Description:** Time to drive LP-11 after HS burst.

Pass Limits: ≥ 100.00 ns548_THS-EXIT7.10990 μs

Result Details:

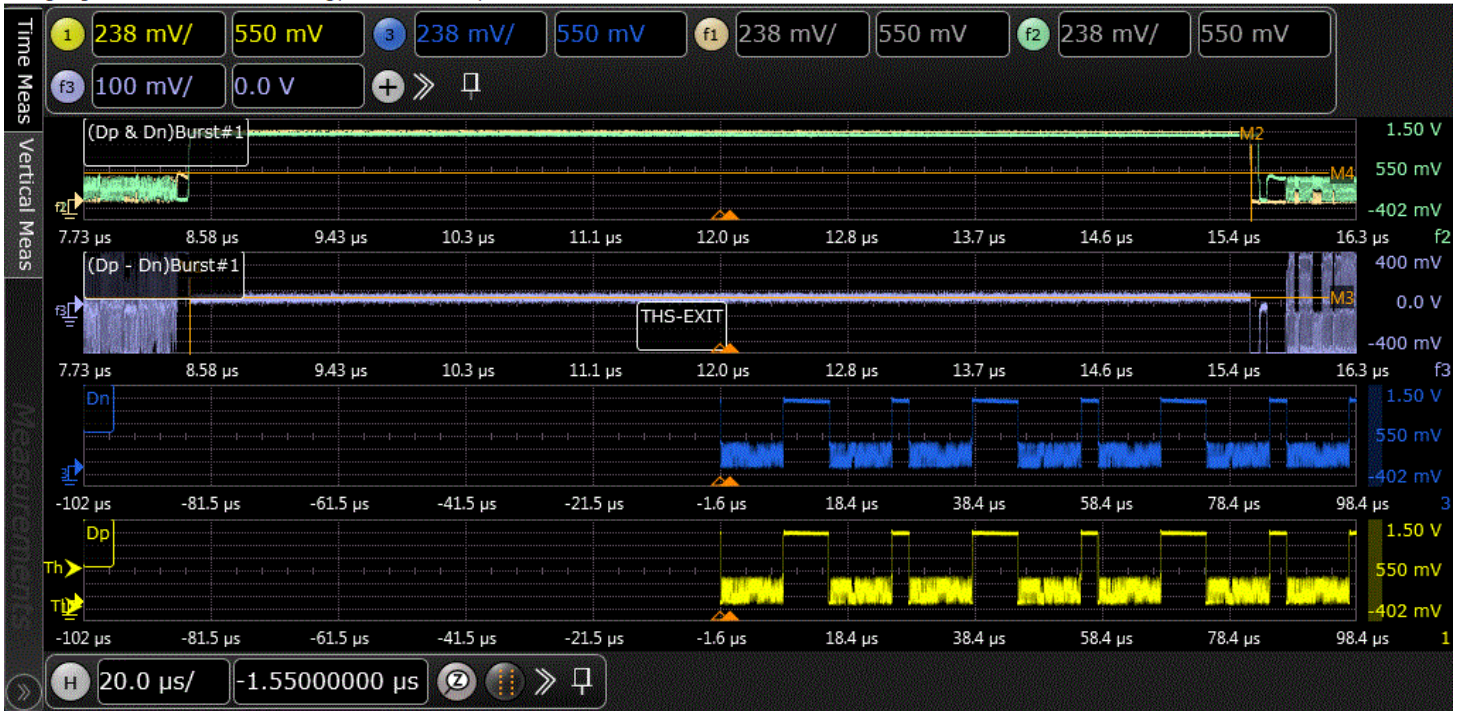
Result Details

THSEXIT(See image)**ZID**100 ohm**DataLane**Lane2**Number of HS burst**1

Trial 1

Trial 1: THSEXIT

Keysight Infiniium : Monday, October 24, 2022 8:16:27 PM


[Top Previous](#)
[Next](#)

✖ 1.5.4 Data-to-Clock Skew (TSKEW(TX))(Max,Min) Reference: D-PHY Specification v1.2 Section 10.2.1 Table 30, CTS v1.2(Test 1.5.4)

Test Summary: FAIL Test Description: Data to Clock Skew [measured at transmitter].

Pass Limits:[MinMaxTSkewTest_LimitMin Ulnst to MinMaxTSkewTest_LimitMax

Ulnst]913_Tskew(Worst)500 mUlnst

Result Details:

Result Details

LeftCrossing(See image)UIINST(Mean)625.0 psTSkew(Min)-312.5 psTSkew(Max)312.5 psNo Of

Measurement15.387000 kZID100 ohmDataLaneLane2PassLimit Min

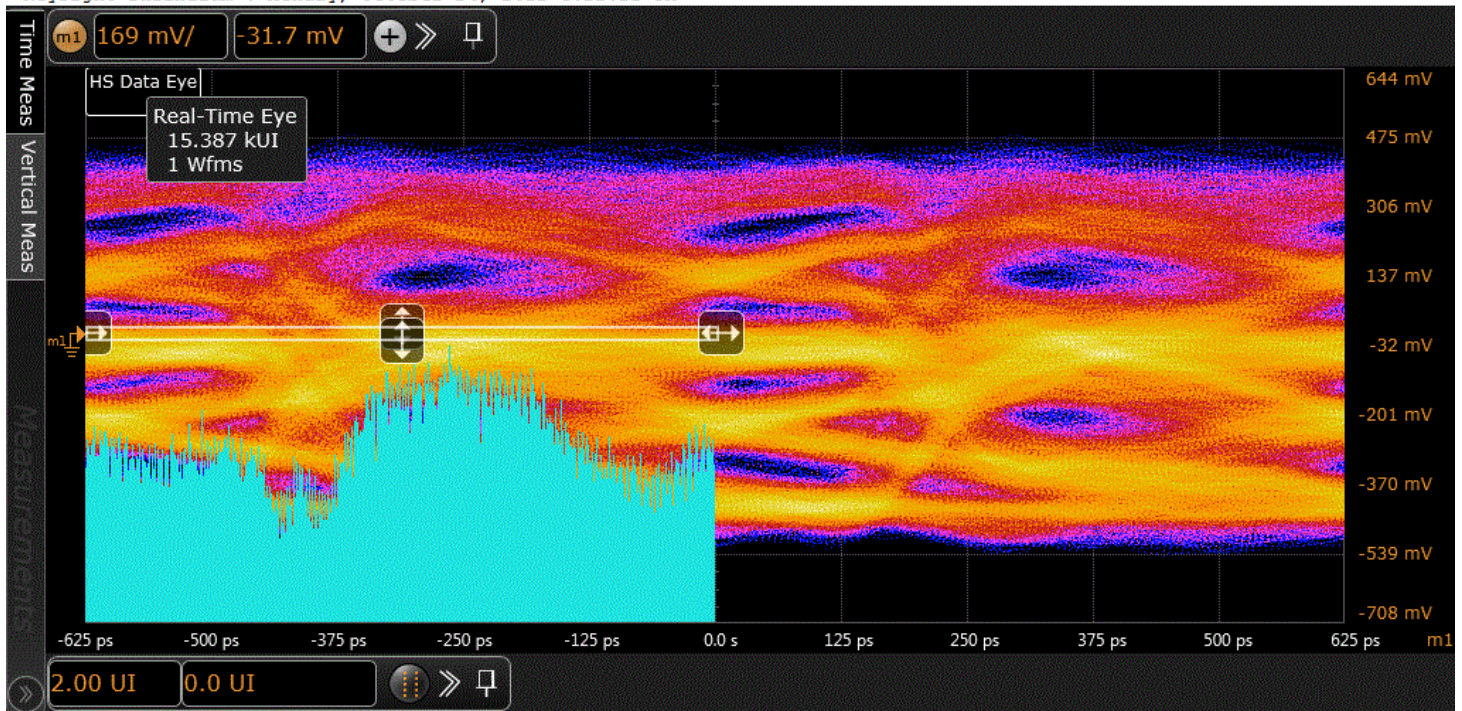
(MinMaxTSkewTest_LimitMin)-200 mUlnstPassLimit Max (MinMaxTSkewTest_LimitMax)200

mUlnstNumber of HS Burst1

Trial 1

Trial 1: LeftCrossing

Keysight Infiniium : Monday, October 24, 2022 8:21:21 PM


[Top Previous](#)
[Next](#)

✓ 1.5.4 Data-to-Clock Skew (TSKEW(TX))(Mean) Reference: D-PHY Specification v1.2 Section 10.2.1 Table 30, CTS v1.2(Test 1.5.4)

Test Summary: Pass Test Description: Data to Clock Skew [measured at transmitter].

Pass Limits:[MeanTSkewTest_LimitMin UInst to MeanTSkewTest_LimitMax

UInst]9131_Tskew(Mean)-9 mUInst

Result Details:

Result Details

UINST(Mean)625.0 psTSkew(Mean)-5.5 psNo Of Measurement15.387000 kZID100

ohmDataLaneLane2PassLimit Min (MeanTSkewTest_LimitMin)-200 mUInstPassLimit Max (MeanTSkewTest_LimitMax)200 mUInstNumber of HS Burst1

[Top Previous](#)