

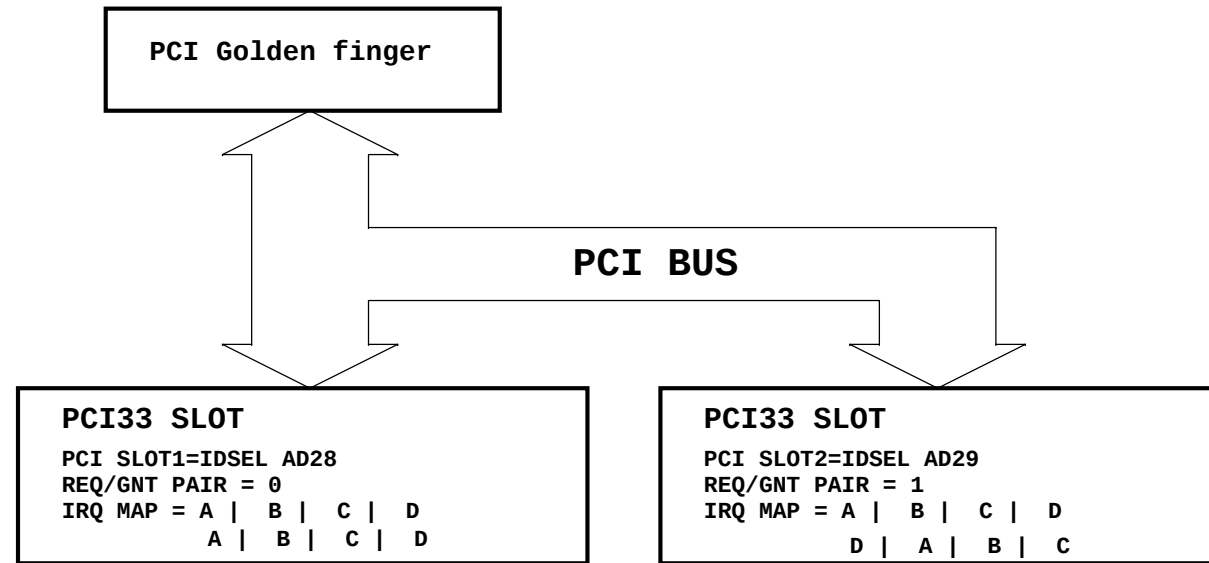
NISK36P2 REV:B

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Design Reference Document			Reference Schematic		Reference Chipset Datasheet	
			Reference Design Guide		SPEC Version	
Reversion			Revision History			
From	To	Date				
NONE	10AX	04/16				
10AX	10BX	06/05				
10BX	10CX	08/16				
10CX	20BX	20140731				

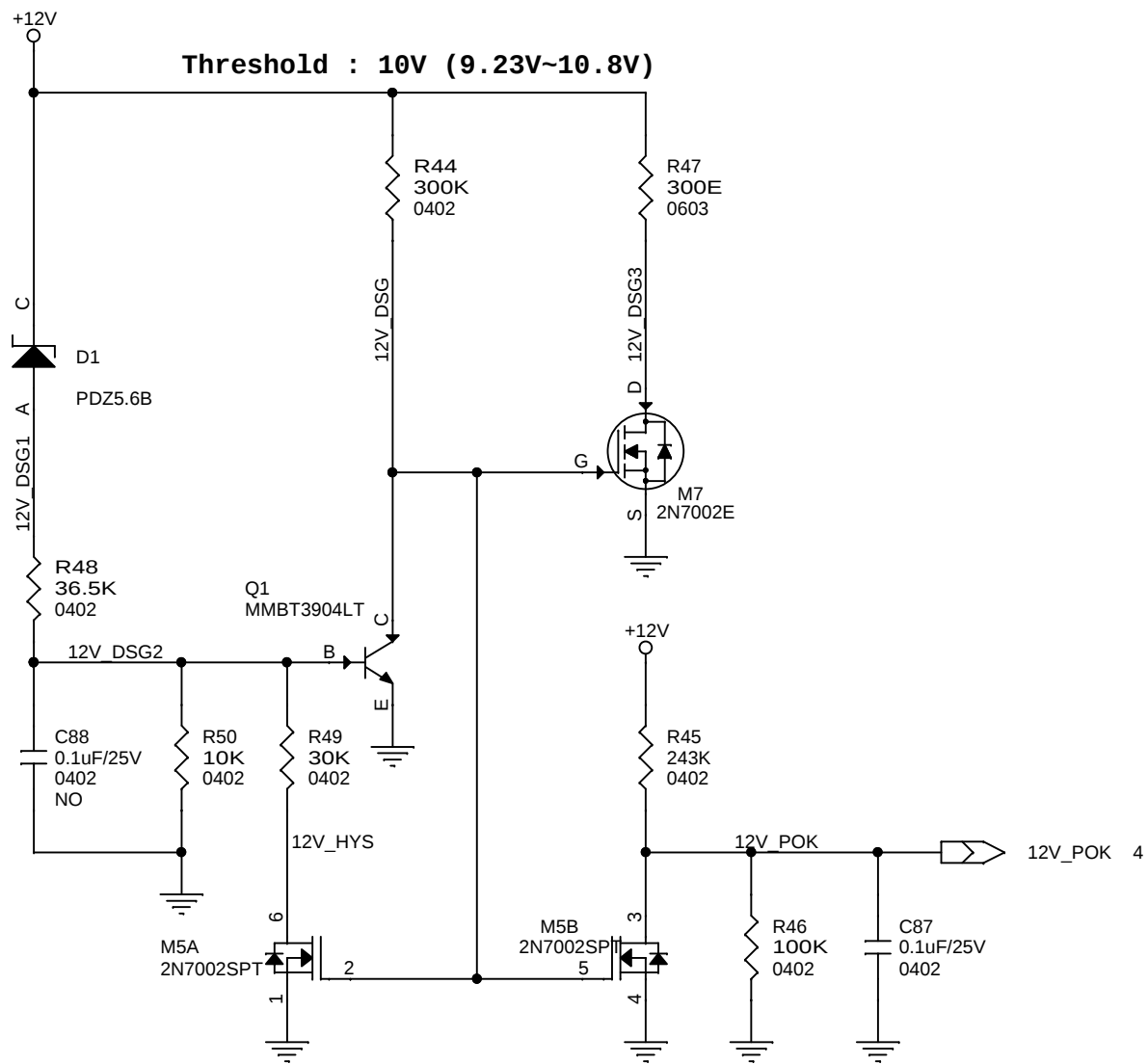
Modify list	Modify list
1. 4/13 modify L1	
2 .4/14 Change CN1/CN2 4SIPICSL15X00 to 4SIPICSL33X00 R64,R65改0402	
3 .4/19 DEL RN2 新增四顆R78->PCI_M66EN, R79->PCI_IRDY#, R80->PCI_PRST#, R81->PCI_ACK64#	
4 .4/25 page 9 修改文字面 page 4, M1,M2 由CSD17308改 CSD17308Q3	
5 .6/05 RN1,RN2,RN3,RN4,RN5 由33 OHM 改 10K OHM , R9,R10,R12,R26,R59,R58 由33 OHM 改 10K	
6.08/16 L1 由4IC4701S04X00改4IC4700S16X00	
7.2014/07/31 進版B 將所有螺絲定位孔不接GND	
8.08/14 ADD C107,C108兩顆預留	

NISK31P2 Block Diagram



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Title Block Diagram			
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Title

12V_POK

Size
A

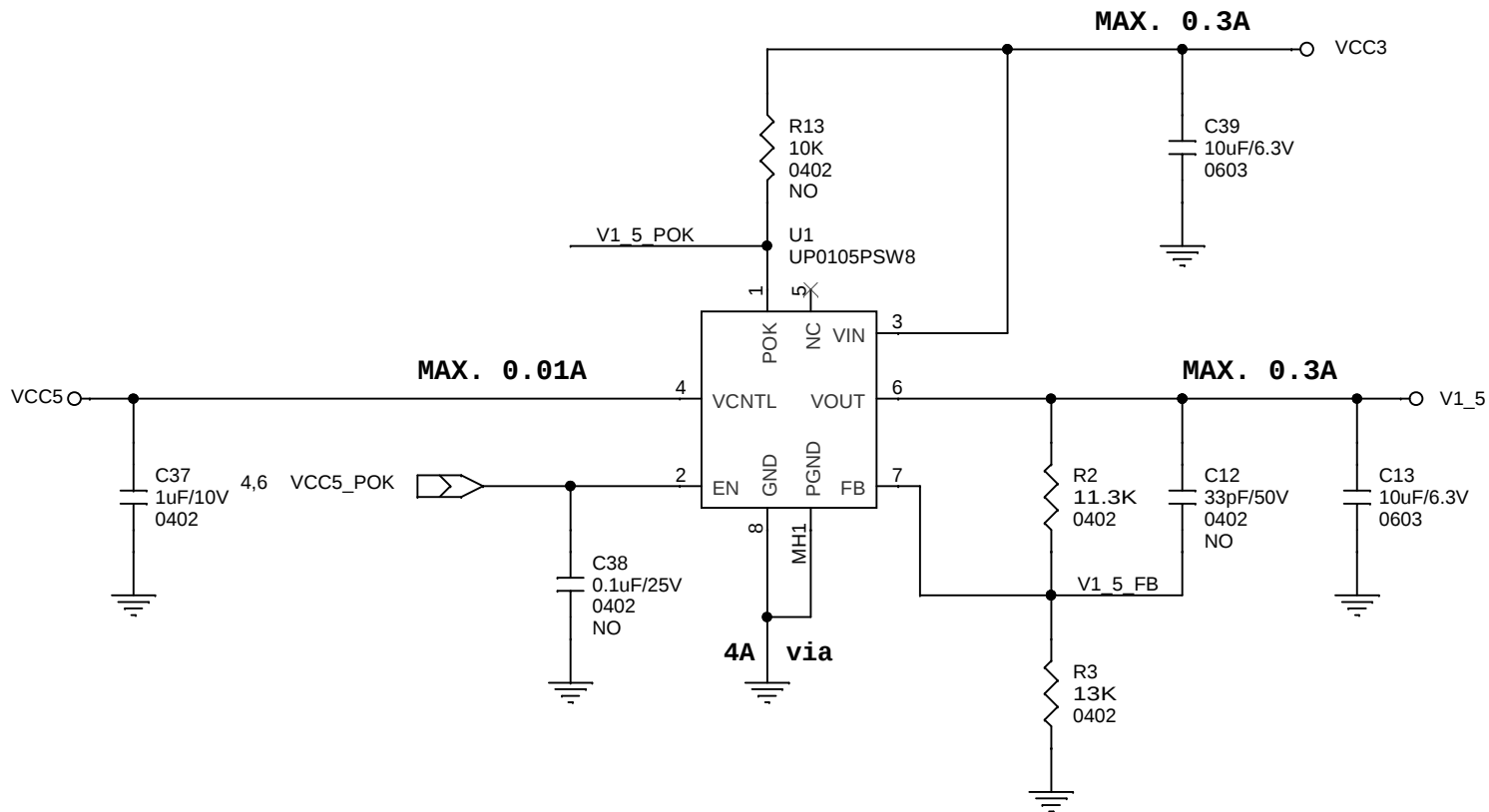
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V1_5

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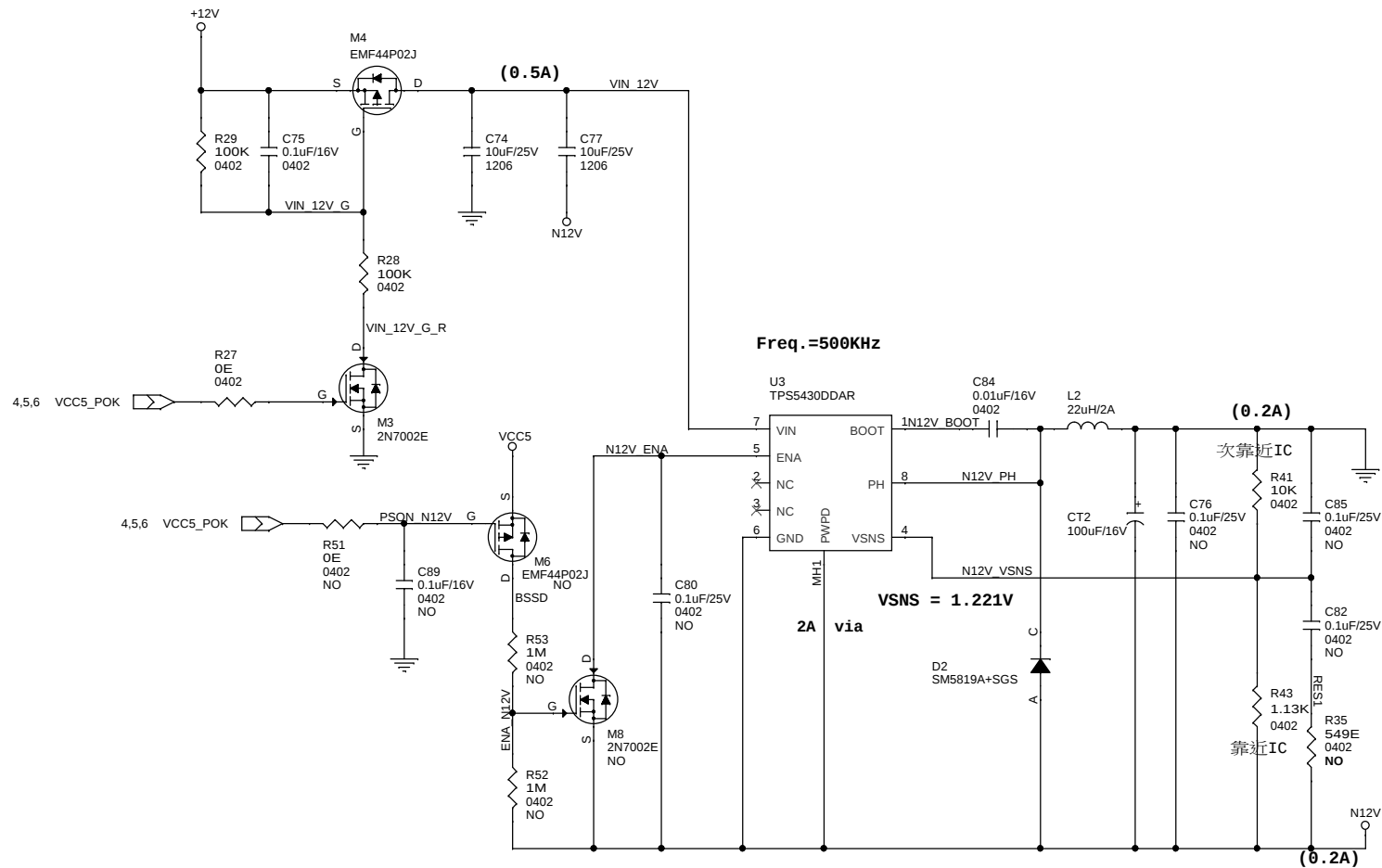
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Title

N12V

Size
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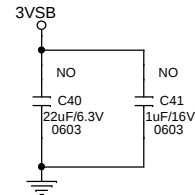
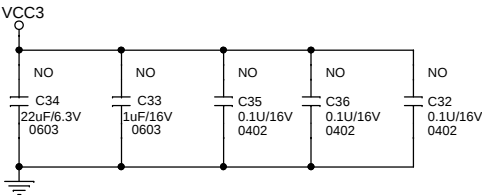
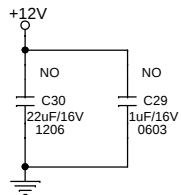
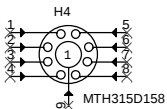
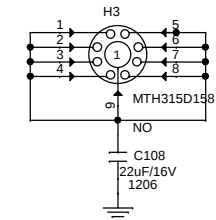
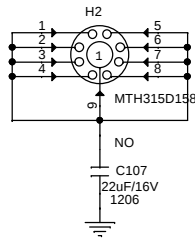
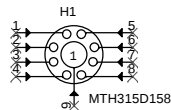
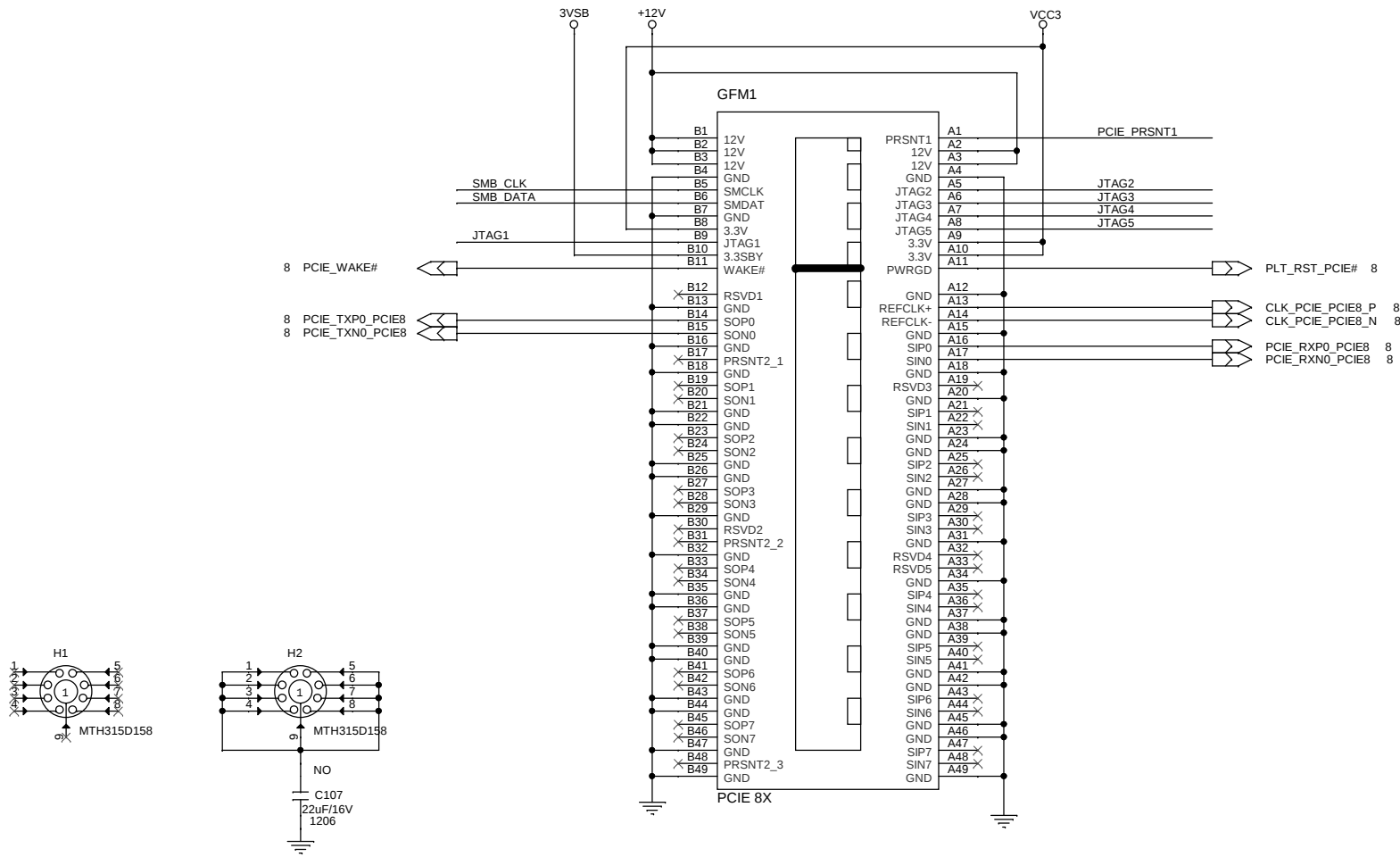
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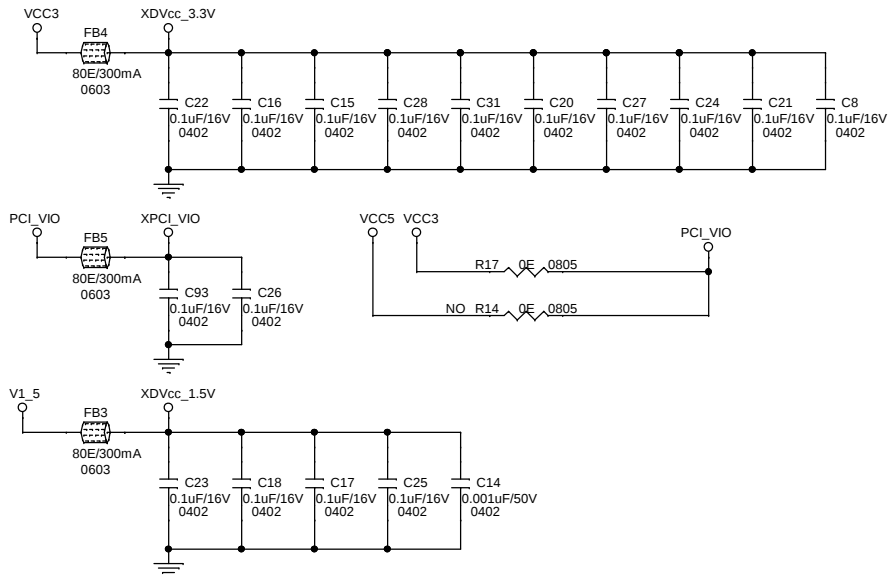
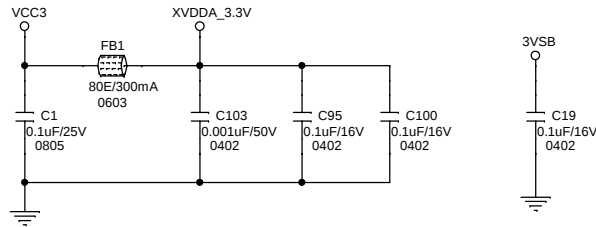
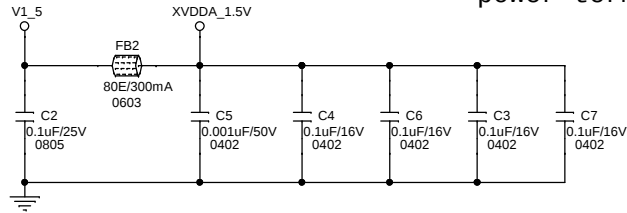
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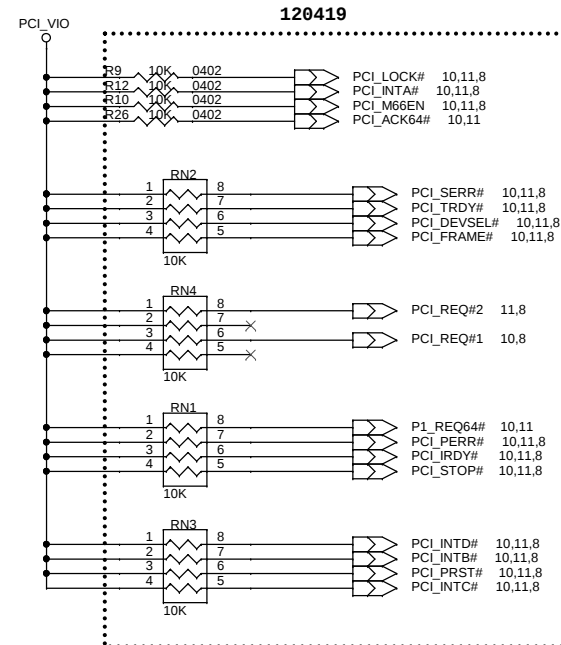
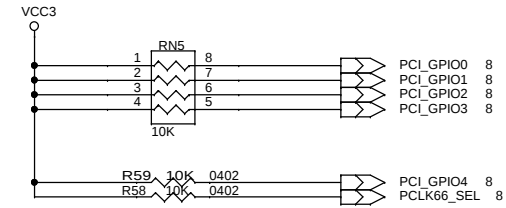
Title PCI Golden Finger		
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Bridge Power Filtering/Decoupling

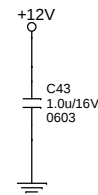
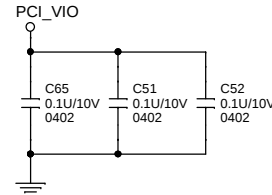
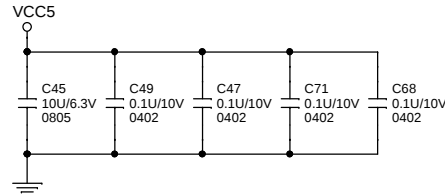
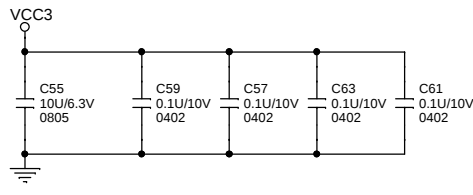
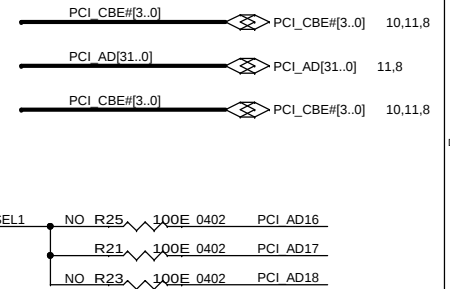
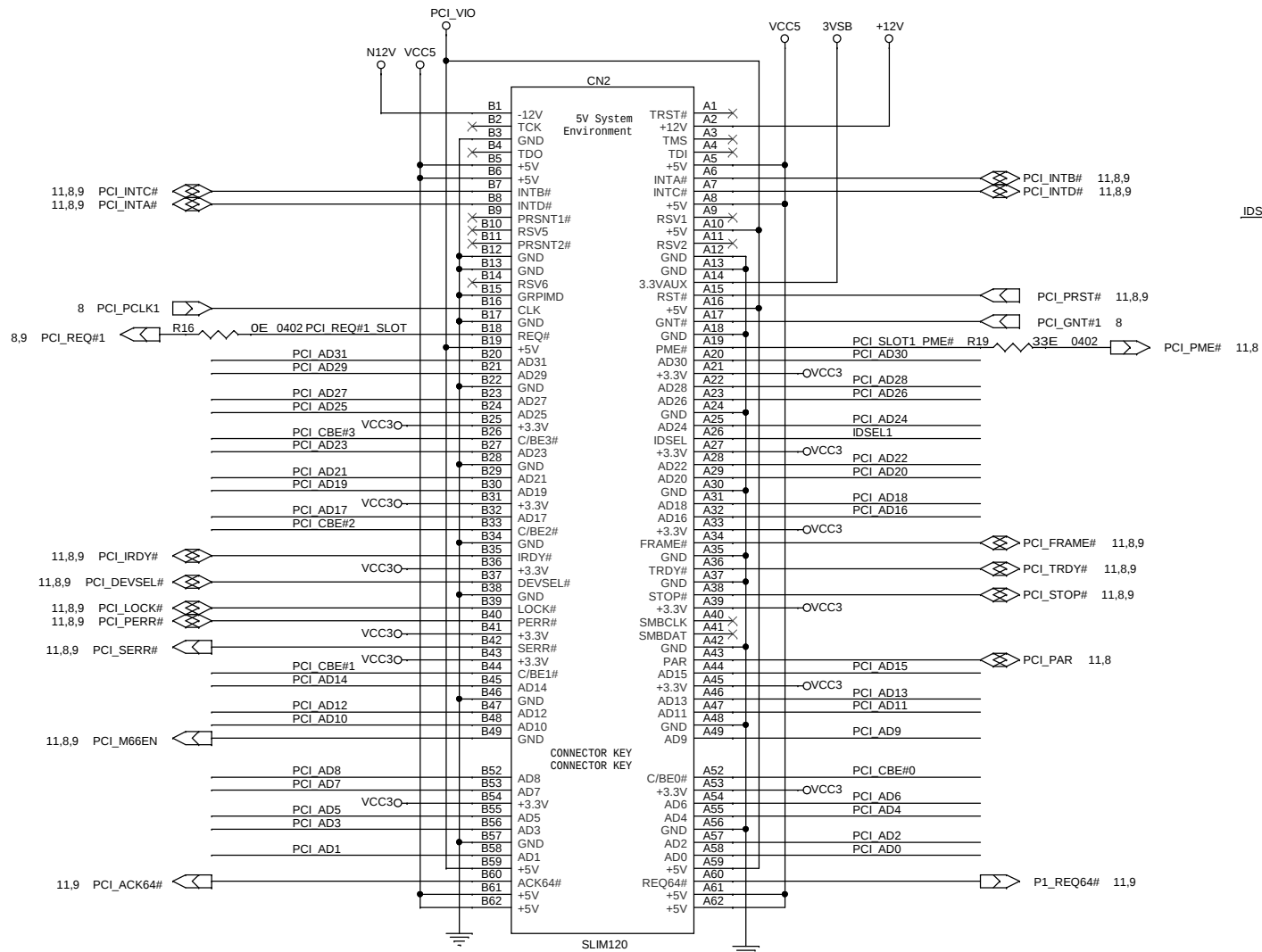
NOTE: Place decoupling
caps as close as
possible to associated
power terminals on U5.



Pull Ups/Pull Downs



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		Title PCI Damping	
Size B	Document Number NISK36P2(4BJK36P2B1X10)	Rev 20AX	
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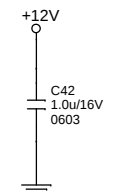
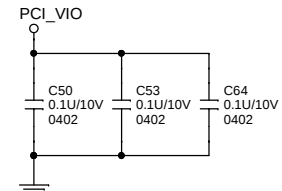
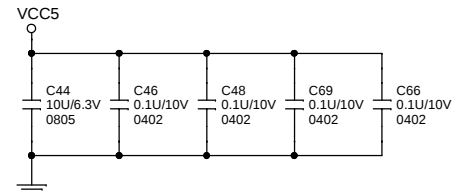
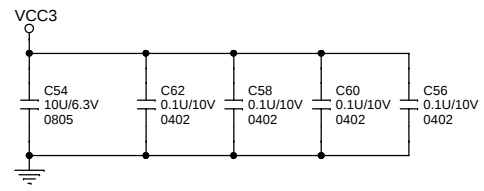
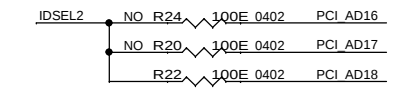
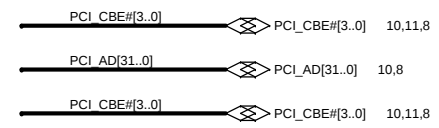
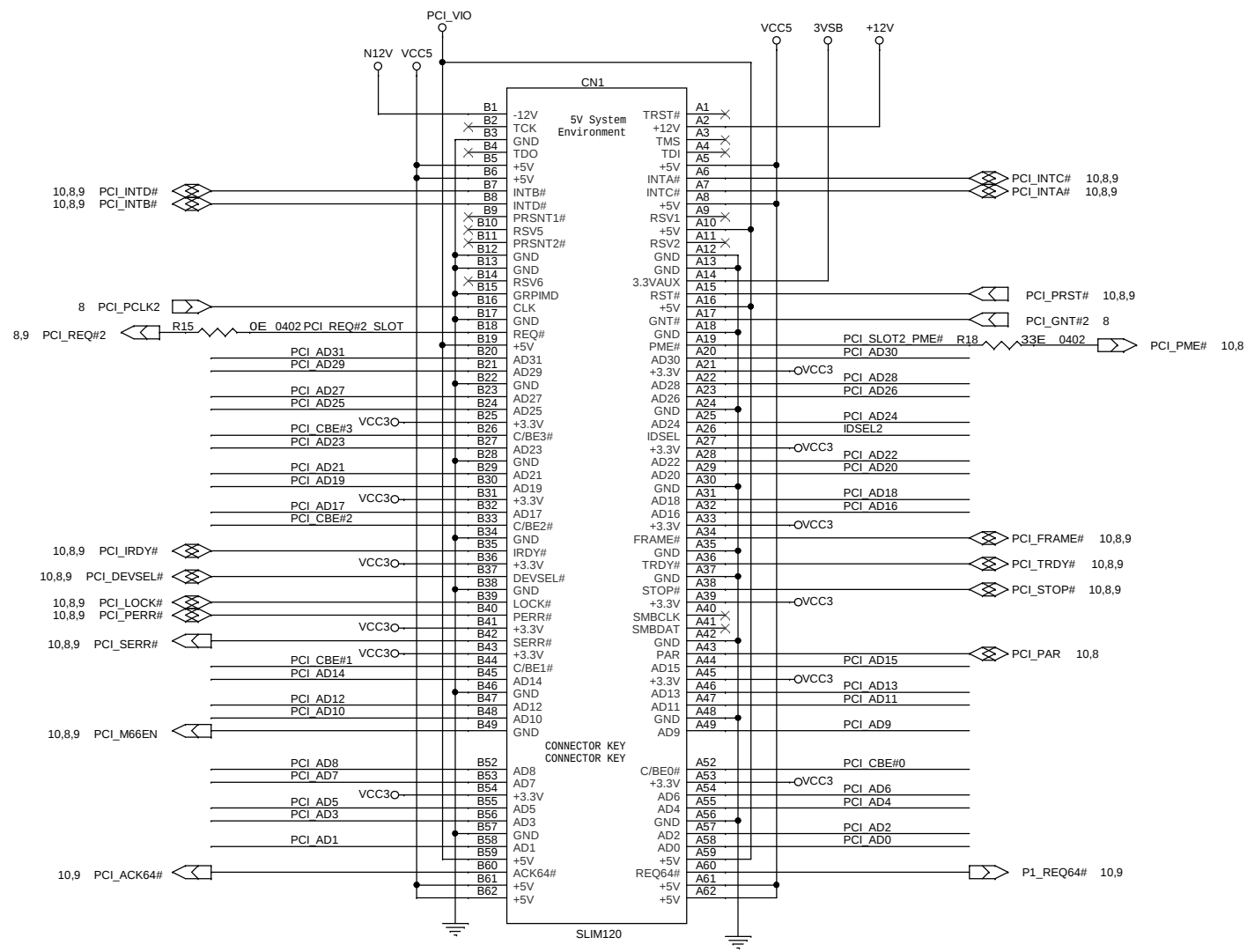


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Title			PCI SLOT1
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Title: **PCI Slot2**

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