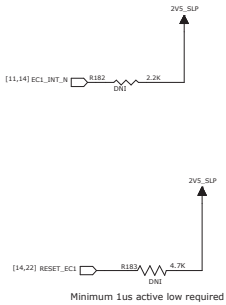
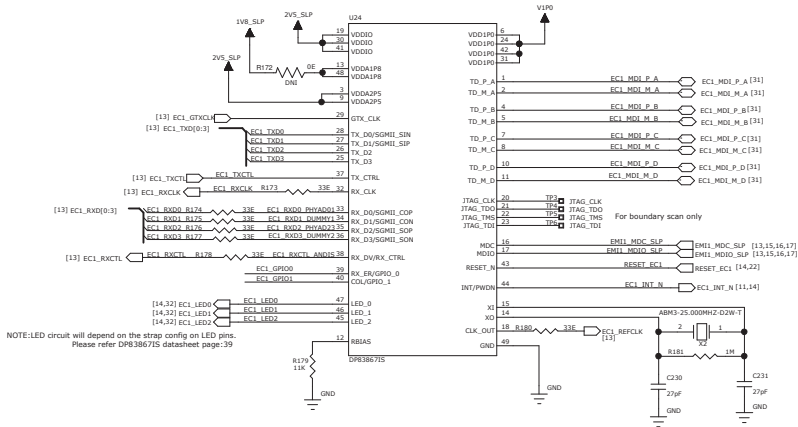
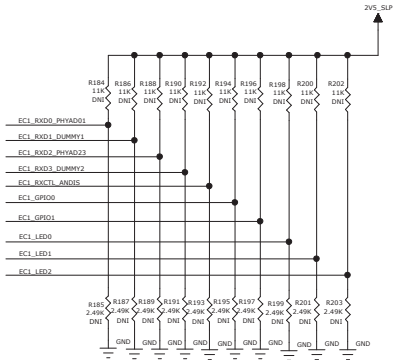


EC1 PHY : ETHERCAT



NOTE: reset should be controlled by the CPLD
TBD GPIO can be configured as a Wake on LAN indicator

STRAP CONFIGURATION



STRAP FUNCTION	VALUE
PHY ADDRESS (48BIT)	0000
AUTO NEGOTIATION	DISABLED
RGMI1 CLOCK SKEW (RX[2] RX[1] RX[0])	000 (2ns)
RGMI1 CLOCK SKEW (TX[2] TX[1] TX[0])	000 (2ns)
SPEED SEL	0 (10/100/1000)
SGMI1	DISABLED
MDI MIRRORING	DISABLED

DECAPS

