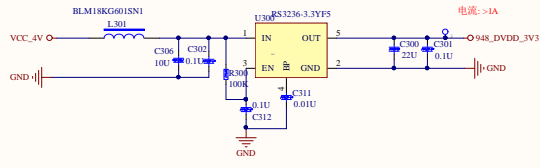
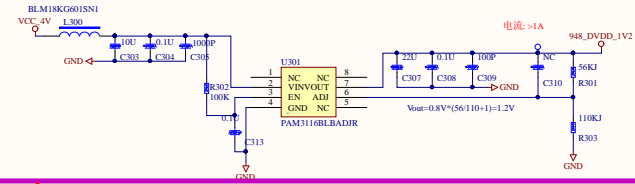


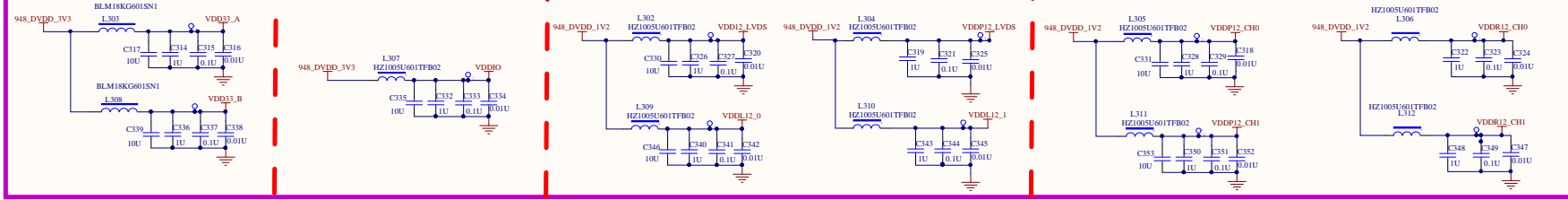
VDD3V3@300MA
Imax OUT=500MA



VDD1V2@250MA
948 1.2V供电: 956 1.1V供电
Imax OUT=600MA

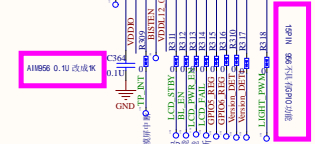
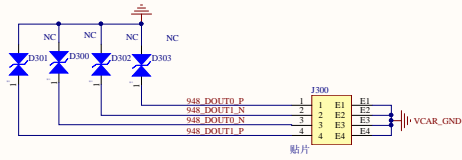
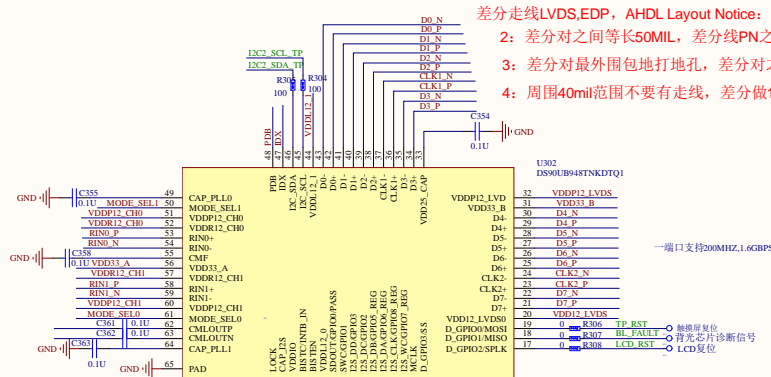
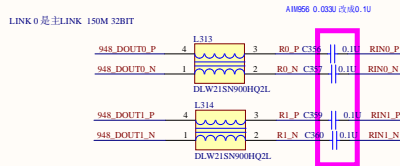


电容靠近电源引脚放置



- 差分走线LVDS,EDP, AHDL Layout Notice: 1:走线尽量短, 不要交叉, 最好不要走过孔, 有打过孔处打几个伴随地孔
2: 差分对之间等长50MIL, 差分线PN之间等长10MIL, 差分对之间控制3倍线宽间距
3: 差分对对外围包地打地孔, 差分对之间可以包地
4: 周围40mil范围不要有走线, 差分做100Q阻抗走线, 参考层尽量完整

100Ω阻抗匹配

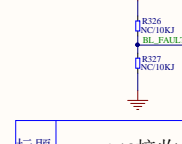
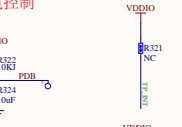
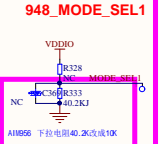
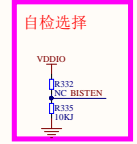
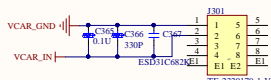


IIC 通信

TP中断

948下电控制

屏版本检测预留



标题	948接收	拟制		发行	
版本	V24.04.08	审核		部门	
页码	3/3	审核		签章	
定稿日期	24.04.08	标准化			