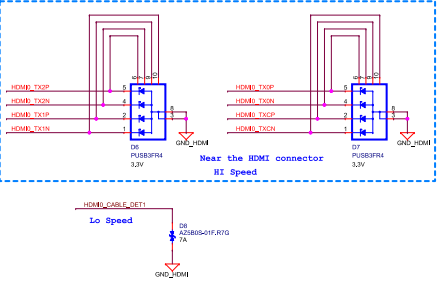
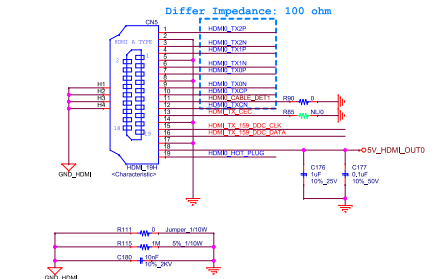


# HDMI-2.0\_TX



## 10.2 Layout Example

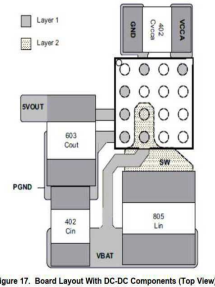
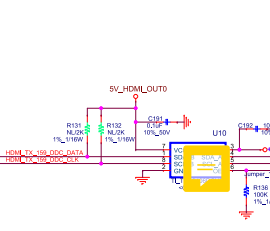
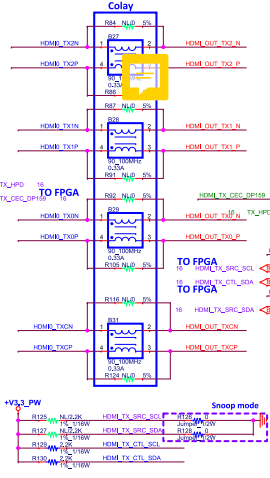
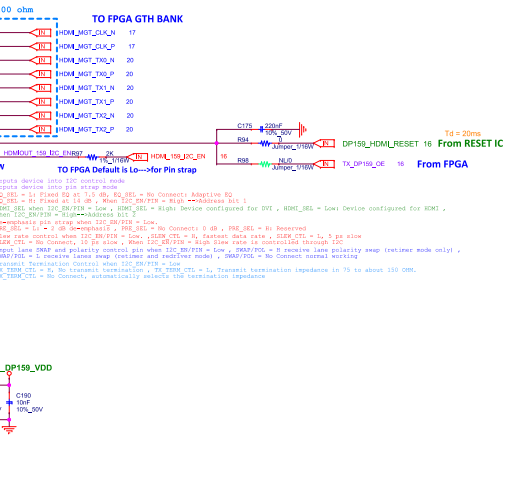
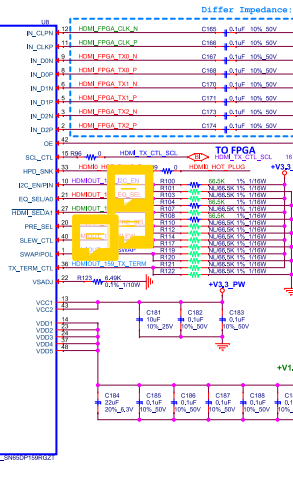
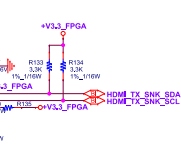
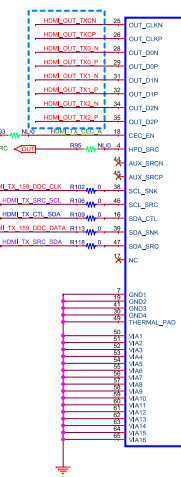


Figure 17. Board Layout With DC-DC Components (Top View)



## Differ Impedance: 100 ohm



## FPGA 3V3 BANK TO FPGA



## FPGA 3V3 BANK TO FPGA

