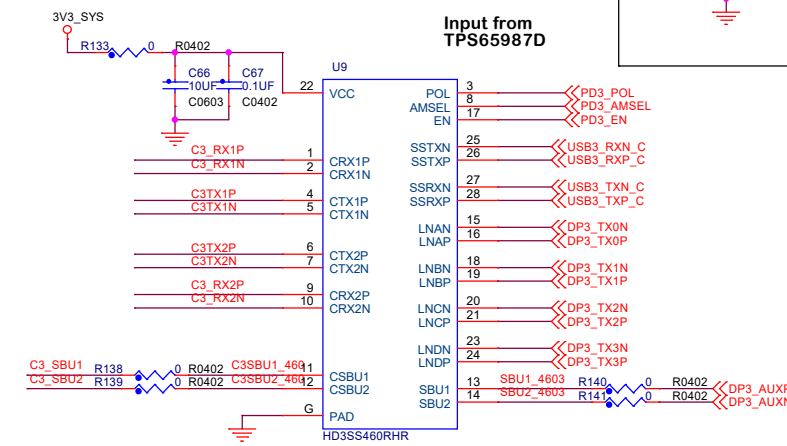


**TypeC Connector Pin Mapping**

| Signal | Pin | Signal | Pin |
|--------|-----|--------|-----|
| GND    | A1  | GND    | B12 |
| SSTXP1 | A2  | SSRXN1 | B11 |
| SSTXP2 | A3  | SSRXN2 | B10 |
| SSRXN1 | A4  | SSRXN1 | B9  |
| SSRXN2 | A5  | SSRXN2 | B8  |
| VBUS   | A6  | VBUS   | B7  |
| CC1    | A7  | CC2    | B6  |
| DP1    | A8  | DP2    | B5  |
| DN1    | A9  | DN2    | B4  |
| SBU1   | A10 | SBU2   | B3  |
| VBUS   | A11 | VBUS   | B2  |
| SSRXN2 | A12 | SSRXN2 | B1  |
| SSRXN1 | A1  | SSRXN1 | B12 |
| SSRXN2 | A2  | SSRXN2 | B11 |
| SSRXN1 | A3  | SSRXN1 | B10 |
| SSRXN2 | A4  | SSRXN2 | B9  |
| SSRXN1 | A5  | SSRXN1 | B8  |
| SSRXN2 | A6  | SSRXN2 | B7  |
| SSRXN1 | A7  | SSRXN1 | B6  |
| SSRXN2 | A8  | SSRXN2 | B5  |
| SSRXN1 | A9  | SSRXN1 | B4  |
| SSRXN2 | A10 | SSRXN2 | B3  |
| SSRXN1 | A11 | SSRXN1 | B2  |
| SSRXN2 | A12 | SSRXN2 | B1  |



| FLIP | POL | CTL0 | AMSEL | CTL1 | EN | Mux Operation                |
|------|-----|------|-------|------|----|------------------------------|
| L    | L   | L    | H     | H    | H  | 2CH USBSS + 2CH AM (Normal)  |
| H    | L   | L    | H     | H    | H  | 2CH USBSS + 2CH AM (Flipped) |
| L    | H   | L    | H     | H    | H  | 4CH AM (Normal)              |
| H    | H   | L    | H     | H    | H  | 4CH AM (Flipped)             |
| L    | M   | L    | H     | H    | H  | 2CH USBSS (Normal)           |
| H    | M   | L    | H     | H    | H  | 2CH USBSS (Flipped)          |
| L    | M   | M    | H     | H    | H  | 2CH USBSS + 2CH AM (Normal)  |

