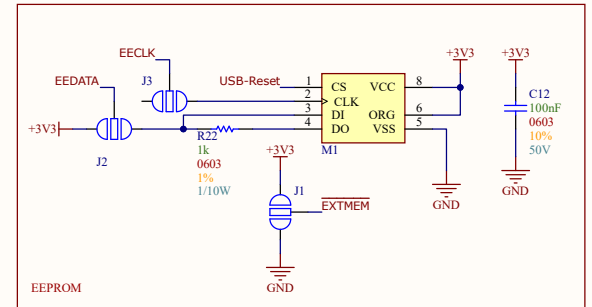
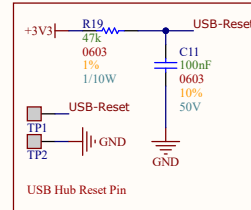
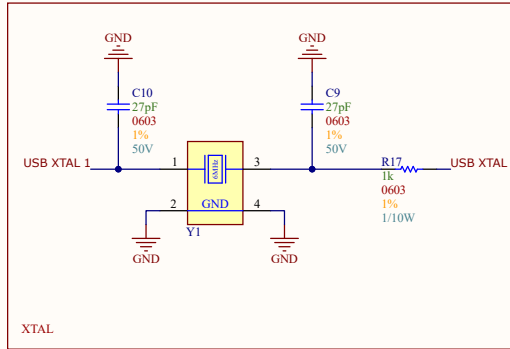
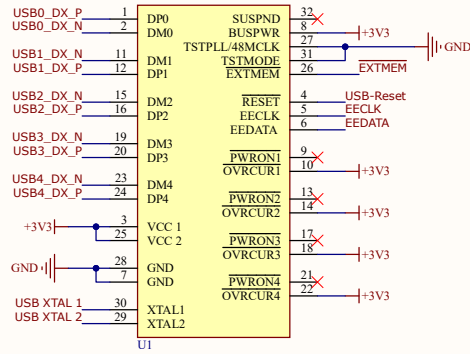
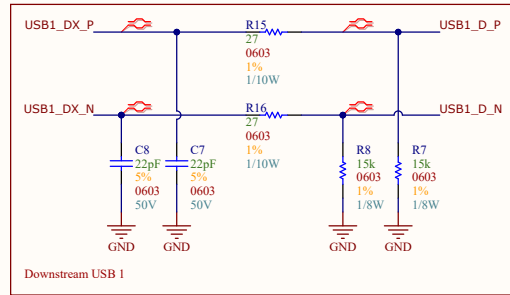
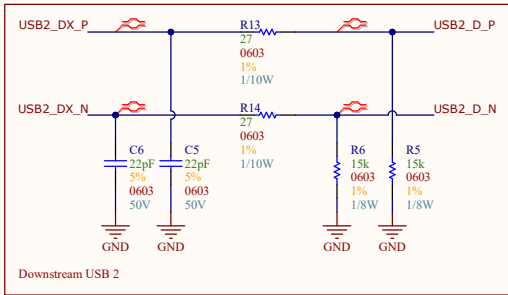
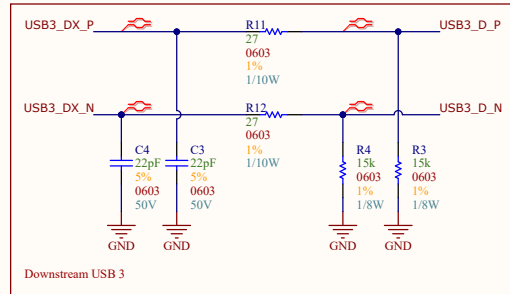
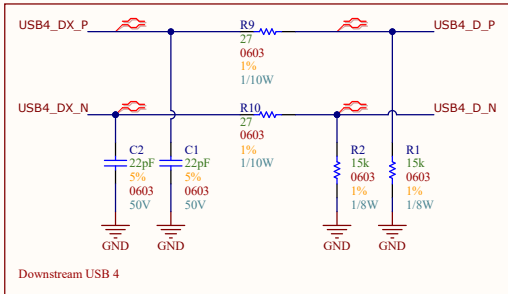
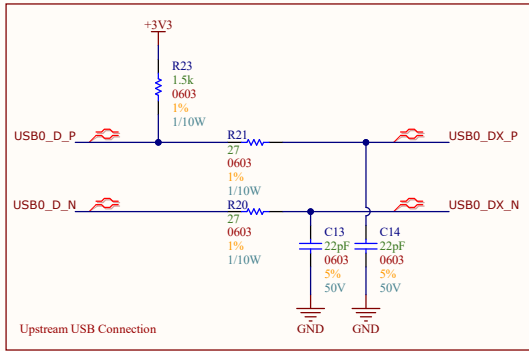
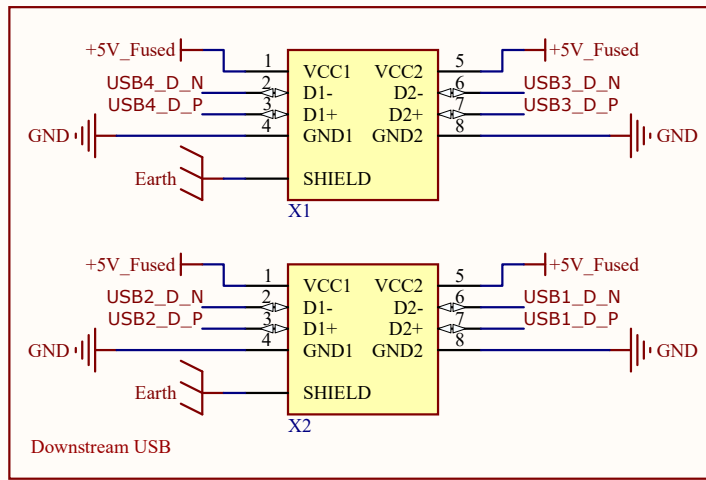
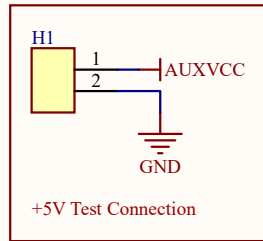
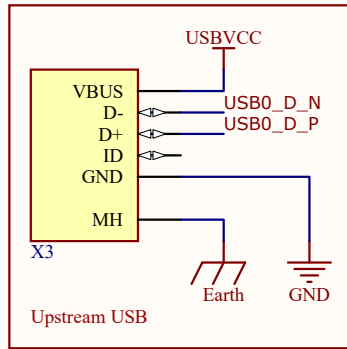
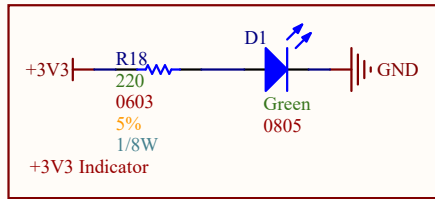
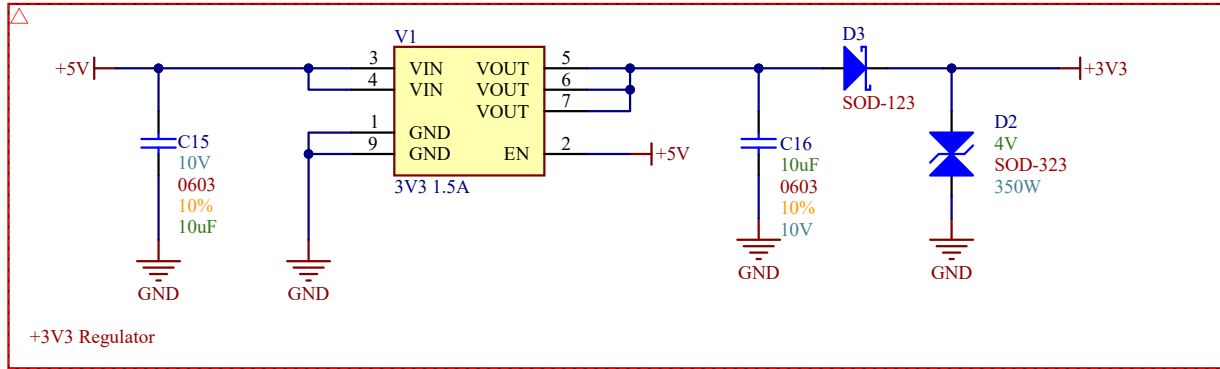
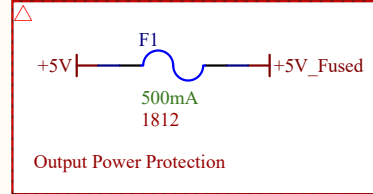
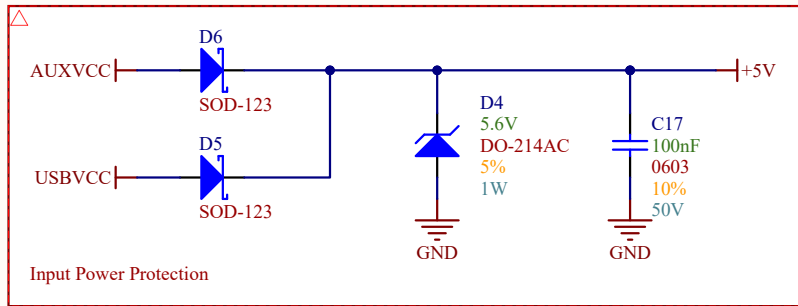




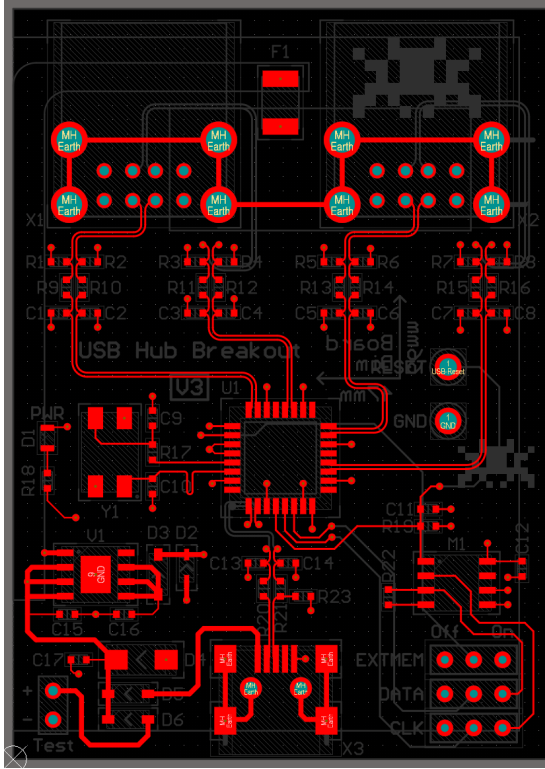
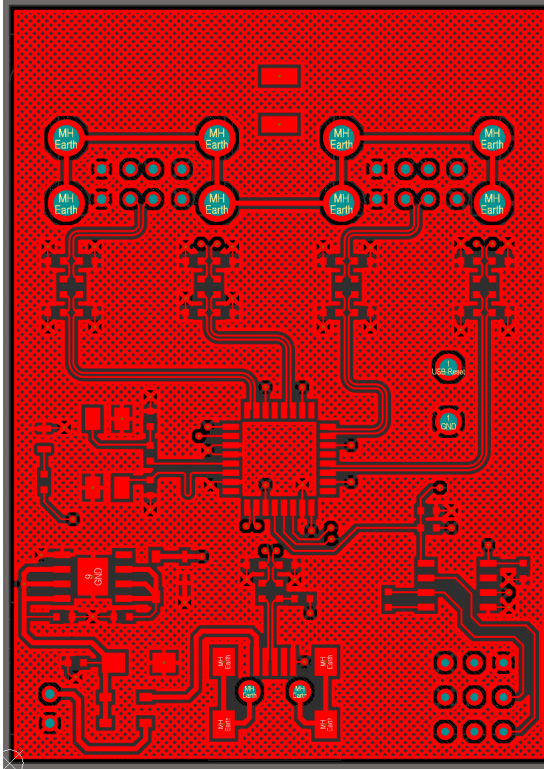
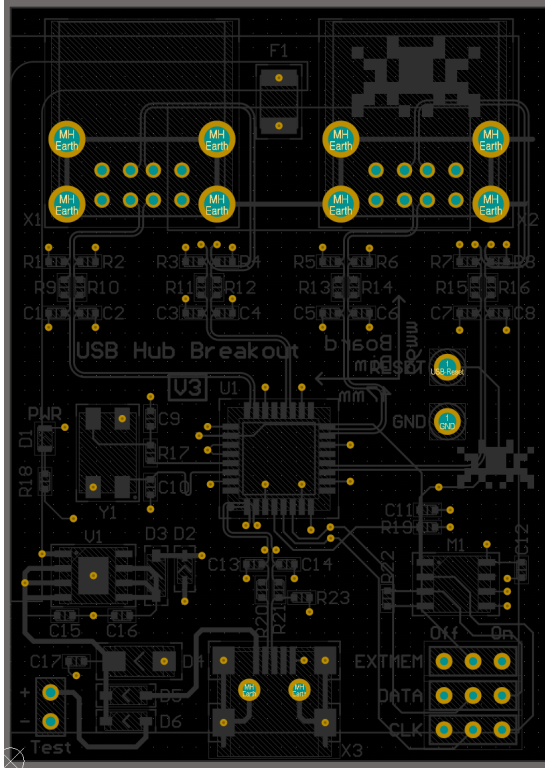
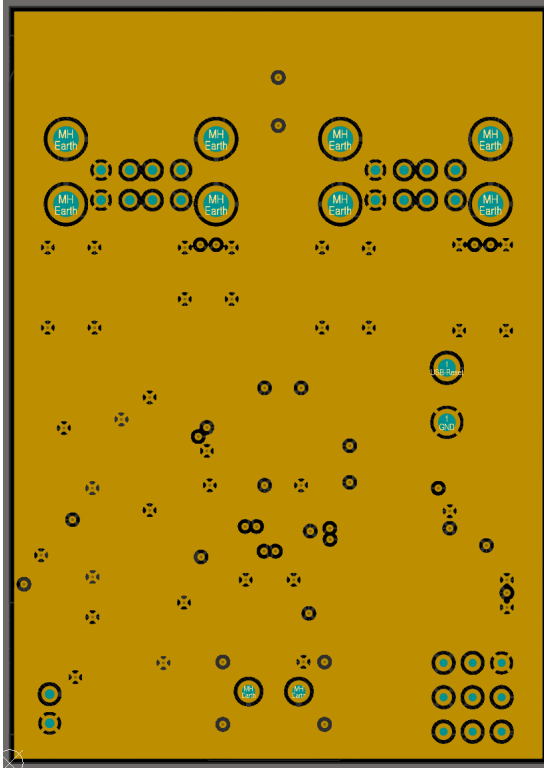
Non-EEPROM Operation:

- Connect EEDATA to 3V3
- Leave EECLK floating
- Connect EXTMEM to 3V3

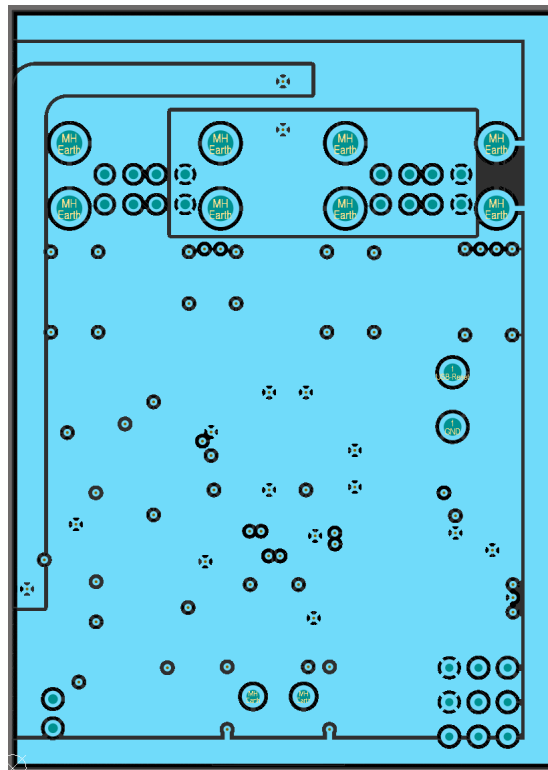
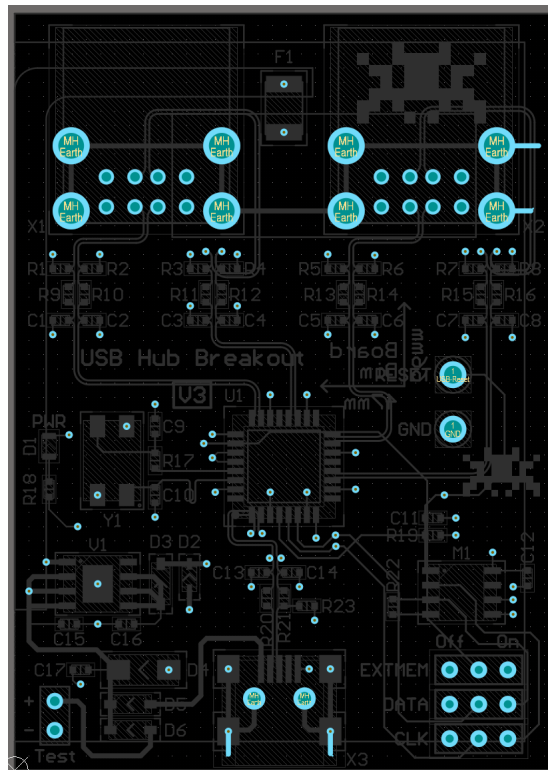




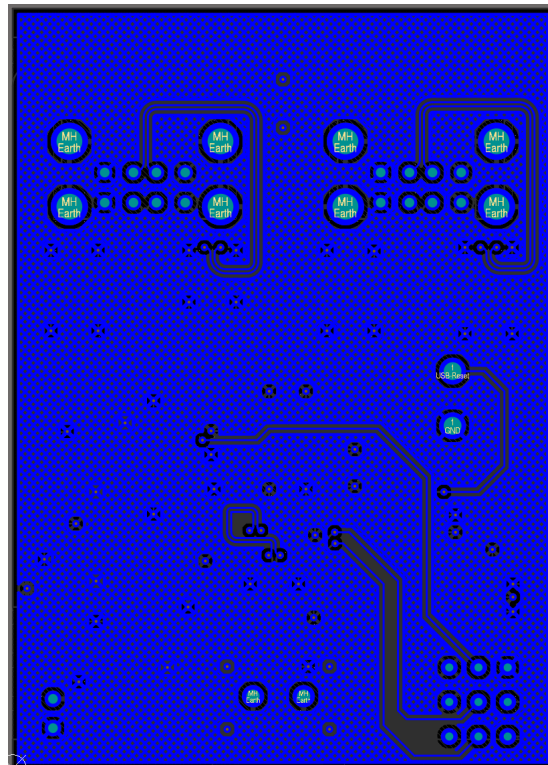
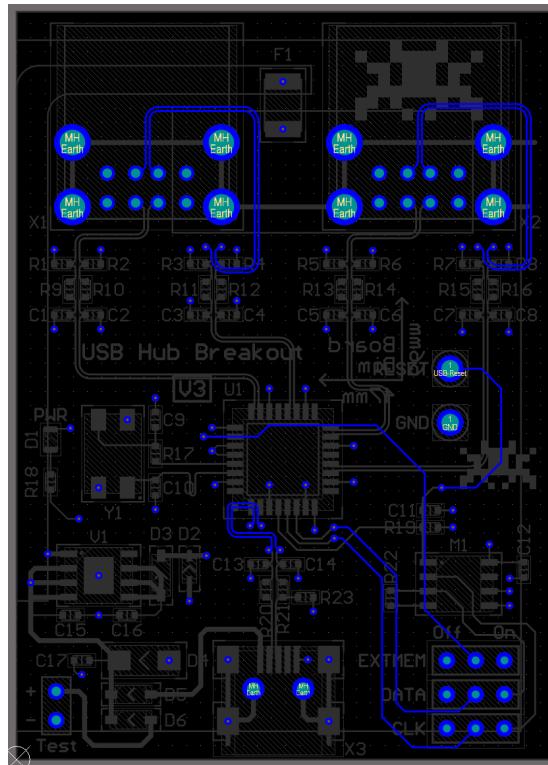
PCB Layout

Layer	Shelved Polygon Pours	Unshelved Polygon Pours (final layout)
Top	 The image shows the top layer of a PCB layout with a dark grey background. Red traces represent the circuitry, including a USB Hub Breakout section with various components like resistors (R1-R18), capacitors (C1-C17), and integrated circuits (U1, U3). The traces are connected to various pins and components, with red circles indicating connection points. The layout is organized with components labeled with their respective values and part numbers.	 The image shows the top layer of a PCB layout with a red background. The circuitry is represented by red traces and components, including a USB Hub Breakout section. The layout is organized with components labeled with their respective values and part numbers. The red background indicates the unshelved polygon pours.
Middle 1 (GND)	 The image shows the Middle 1 (GND) layer of a PCB layout with a dark grey background. Yellow traces represent the ground plane, including a USB Hub Breakout section with various components like resistors (R1-R18), capacitors (C1-C17), and integrated circuits (U1, U3). The traces are connected to various pins and components, with yellow circles indicating connection points. The layout is organized with components labeled with their respective values and part numbers.	 The image shows the Middle 1 (GND) layer of a PCB layout with a yellow background. The circuitry is represented by yellow traces and components, including a USB Hub Breakout section. The layout is organized with components labeled with their respective values and part numbers. The yellow background indicates the unshelved polygon pours.

Middle 2 (PWR)



Bottom



Example Error Messages

