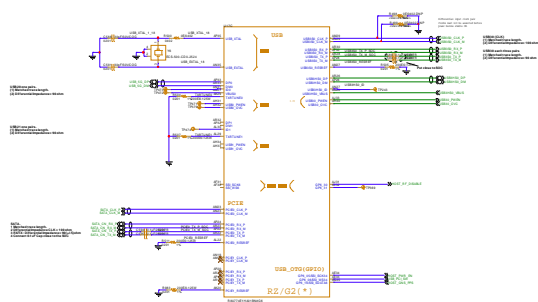
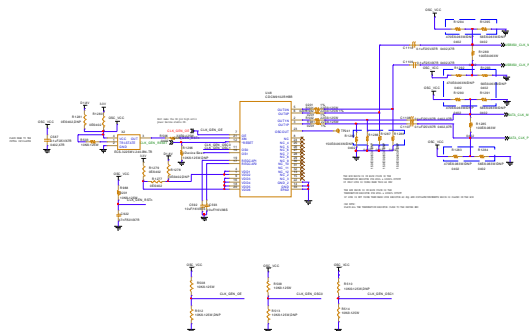
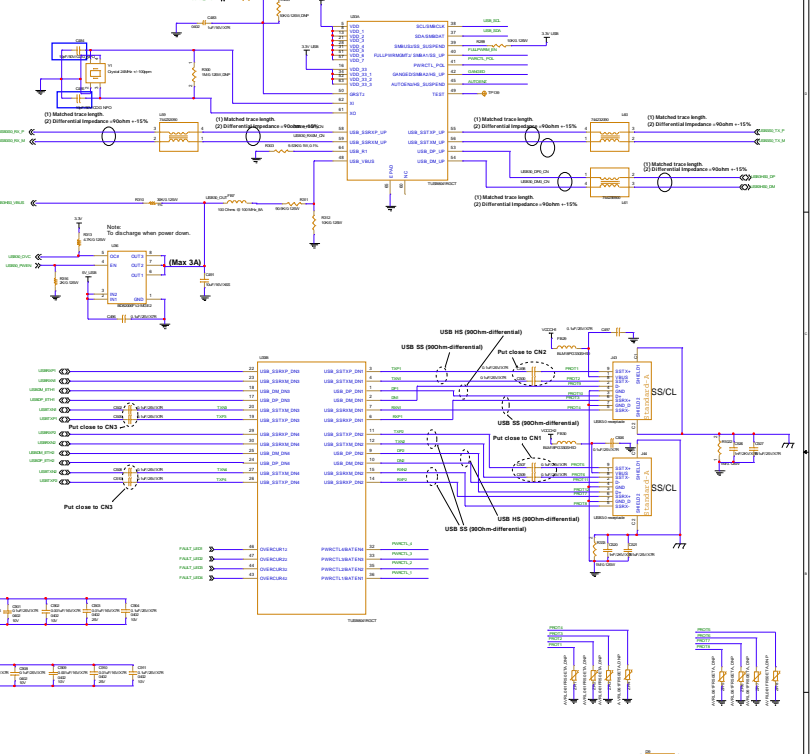
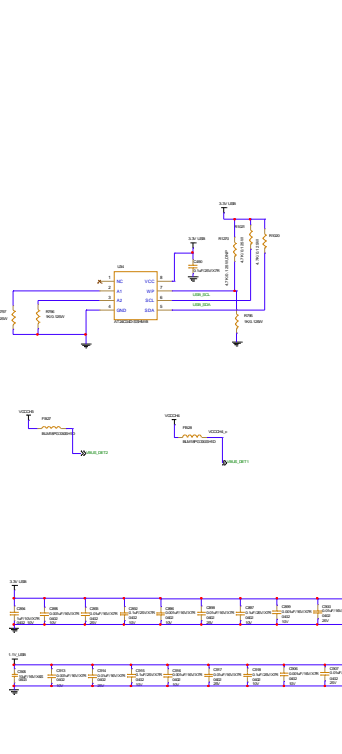
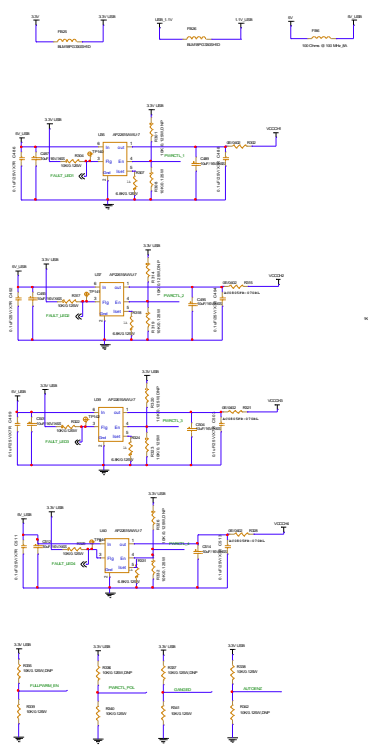


SOC COMMUNICATION INTERFACE 1



crystal need to choose and need to be alter design as per chosen crystal AND POWER NEED TO CHOOSE



CONVERTER NOTE

1. Power traces (1.8V, 3.3V, 5.0V, 1V, VCCIOV1) should be broad.
2. Every high speed signal trace (USB 2.0/3.0) should be wired as shortly as possible.
3. Every high speed signal trace (USB 2.0/3.0) should be wired as broadly as possible.
4. For signal traces, routing priority is as follows:

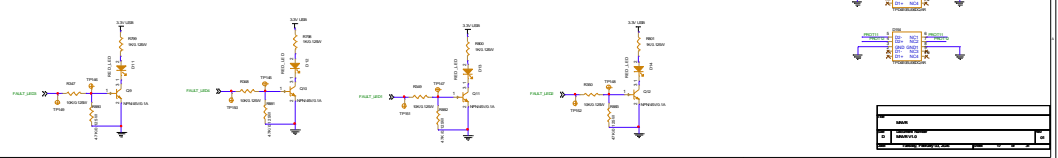
USB SS > USB HS > Others

5. All signal traces for every power source should be put.
6. Follow the basic of transmission trace pair when routing any signal trace.
7. Remove any equipment or discontinuity.
8. Keep same width and spacing.
9. Keep same length for each trace.
10. Keep same length for each trace.

PCB Trace Impedance should be a non-continuous value by its design rules. The differential impedance (single) the copper value that can be manufactured at PCB. For more information please refer to "USB 2.0 Board Design Guide" in design kit.

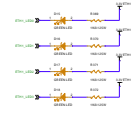
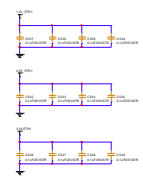
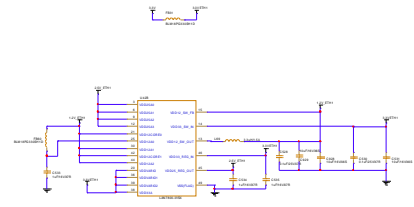
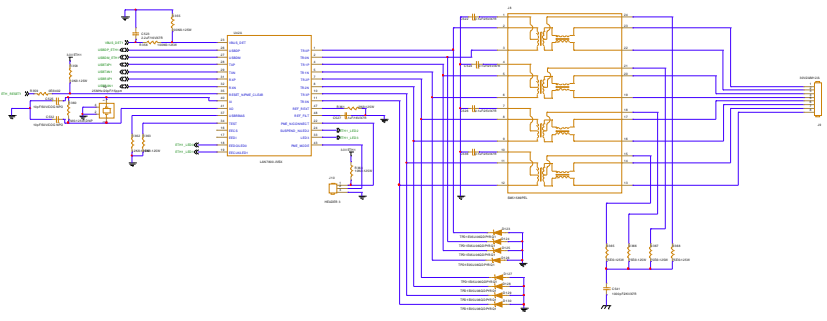
PCB Trace Impedance should be a non-continuous value by its design rules. The differential impedance (single) the copper value that can be manufactured at PCB. For more information please refer to "USB 2.0 Board Design Guide" in design kit.

12. It requires up to 500mA @ 5V



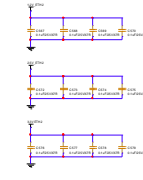
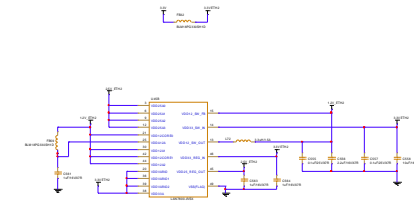
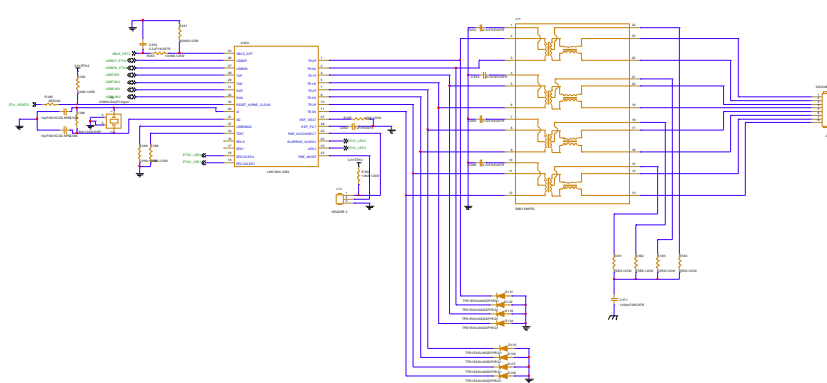
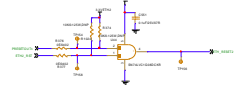
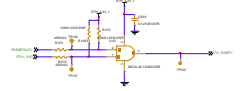
Pin	Signal	Color
1	D+	Blue
2	D-	White
3	GND	Green
4	VBUS	Red
5	D+	Blue
6	D-	White
7	D+	Blue
8	D-	White
9	SS1+	Orange
10	SS1-	Green
11	SS2+	Orange
12	SS2-	Green
13	GND	Green
14	VBUS	Red

USB 3.0 TO ETHERNET BRIDGE



ETHERNET 1 RESET

ETHERNET 2 RESET



REV	1.0
DATE	2014-01-01
DESIGNER	...
CHECKER	...