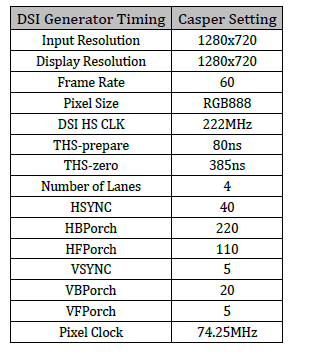
DSI timing



DP timing

