

Dialtronics requirements



Fig2: 2x15 2mm Male Burgstrip

This connector will be populated on Dialtronics Custom CM4 I/O Board.

Dialtronics uses the connector and cable as shown in Fig2, Fig3.

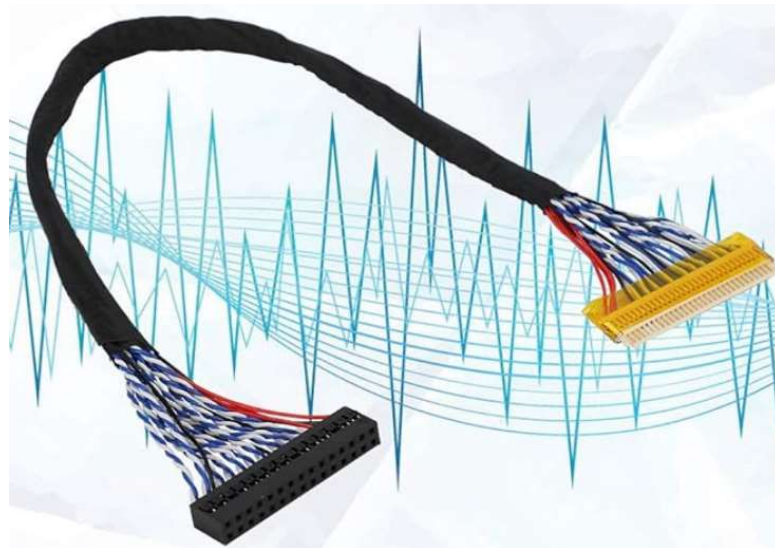
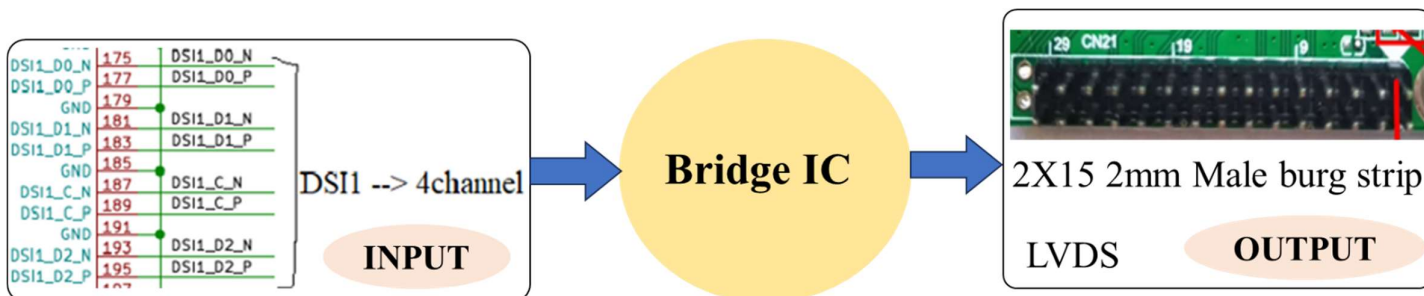
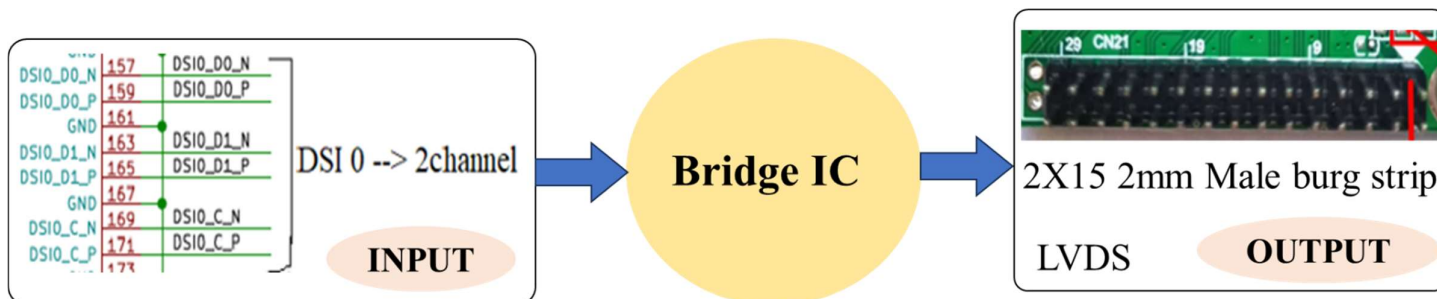


Fig3: 30Pin LVDS Cable

This cable will be connected between Dialtronics Custom CM4 I/O Board and the LVDS display.

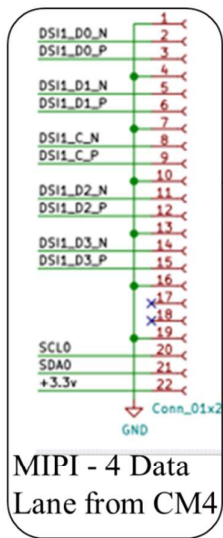


Suggest a Bridge IC that can convert 4 Channel MIPI DSI to LVDS.



Suggest a Bridge IC that can convert 2 Channel MIPI DSI to LVDS.

LVDS on board



Pin NO.	SYMBOL	DESCRIPTION
1	PVCC	Power Supply for board 板供电输入(4.5V-12.5V)
2	PVCC	
3	PVCC	
4	GND	Ground
5	GND	
6	GND	
7	RXO0-	LVDS ODD 0- Signal
8	RXO0+	LVDS ODD 0+ Signal
9	RXO1-	LVDS ODD 1- Signal
10	RXO1+	LVDS ODD 1+ Signal
11	RXO2-	LVDS ODD 2- Signal
12	RXO2+	LVDS ODD 2+ Signal
13	GND	Ground
14	GND	
15	RXOC-	LVDS ODD Clock- Signal
16	RXOC+	LVDS ODD Clock+ Signal
17	RXO3-	LVDS ODD 3- Signal
18	RXO3+	LVDS ODD 3+ Signal
19	RXE0-	LVDS EVEN 0- Signal
20	RXE0+	LVDS EVEN 0+ Signal
21	RXE1-	LVDS EVEN 1- Signal
22	RXE1+	LVDS EVEN 1+ Signal
23	RXE2-	LVDS EVEN 2- Signal
24	RXE2+	LVDS EVEN 2+ Signal
25	GND	Ground
26	GND	
27	RXEC-	LVDS EVEN Clock- Signal
28	RXEC+	LVDS EVEN Clock+ Signal
29	RXE3-	LVDS EVEN 3- Signal
30	RXE3+	LVDS EVEN 3+ Signal