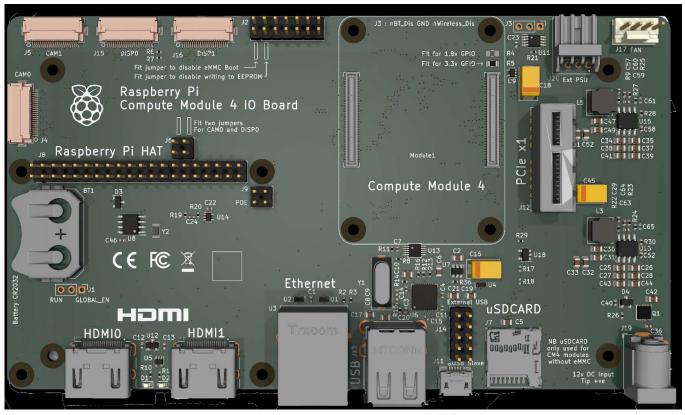
## CM4 I/O Board with LVDS Display





200 HDMIO\_SCL

## Dual DSI display connectors (22-pin 0.5mm pitch cable)

Both DSI interfaces (2-channel and 4channel) are brought out to separate 22way 0.5mm pitch connectors. These

connectors are the same as on the existing CMIO board; note that they are different from Raspberry Pi 4 Model B. If the

DSI0 interface (2-channel) is used, then the two jumpers on J6 must be fitted to route the I2C bus to the connector.

CM4 has 2 display interfaces.

DSI0 → 2Channel

DSI1 → 4 Channel

USB\_OTG\_ID 101 USB2\_N USB2\_P GND 107 ×104 Reserved ×106 Reserved GND PCIe\_CLK\_nREQ CIE\_CLK\_nREQD-USBOTG ID ⇒USB2\_N ⇒USB2\_P 110 PCIe\_CLK\_P 112 PCIe\_CLK\_N 114 GND PCIE CLK PC DPCIE\_nRST GND 113 CAM1\_DO\_N 117 CAM1\_DO\_P 117 CAM1\_DO\_N CAM1\_DO\_P 116 PCIe\_RX\_P PCIE RX PD 118 PCIe\_RX\_N CAM1\_DD\_P 117
GND 119
CAM1\_D1\_N 121
CAM1\_D1\_P 123
GND 125
CAM1\_C\_N 127
CAM1\_C\_P 129
GND 131
CAM1\_D2\_N 133
CAM1\_D2\_P 135
CAM1\_D3\_P 141
LNDTDILE 143 PCIE\_RX\_ND 120 GND 122 PCIe\_TX\_P 124 PCIe\_TX\_N PCIE\_TX\_PC PCIE\_TX\_N C-126 GND CAMO\_DO\_N 128 CAMO\_DO\_N 130 CAMO\_DO\_P 132 GND CAMO\_D1\_N CAMO\_D1\_P 134 CAMO\_D1\_N 136 CAMO\_D1\_P 138 GND CAM O\_C\_ CAM1 D3 N 140 CAMO C N CAMO\_C CAM1\_D3\_P HDMI1\_HOTPLUG 142 CAMO\_C\_F CAM1\_D3\_P 144 GND 146 HDMI1\_TX2\_P 148 HDMI1\_TX2\_N HDMI1\_HOTPLUG 143 HDMI1\_SDA 145 HDMI1\_SCL 147 HDMI1\_HOTPLUG | 1-19
HDMI1\_SDA 14-5
HDMI1\_SDA 14-5
HDMI0\_CEC 15-1
HDMI0\_CEC 15-1
HDMI0\_HOTPLUG 15-3
DSI0\_DO\_N 15-7
DSI0\_DO\_N 15-7
DSI0\_DO\_N 16-3
DSI0\_DI\_N 16-3
DSI0\_DI\_N 16-3
DSI0\_DI\_N 16-5
DSI0\_DI\_N 16-5
DSI0\_DI\_N 16-5
DSI0\_DI\_N 16-5
DSI0\_DI\_N 16-7
DSI0\_CN 16-9
DSI0\_CN 17-7
GND 18-1
DSI1\_DI\_N 18-1
DSI1\_DI\_N 18-1
DSI1\_CN 18-1
DSI1\_DN 18-1
DN 18 HDMI1\_SDA HDMI1\_SCL HDMI1\_CEC High Speed Serial HDMI1\_D1\_P 152 HDMI1\_TX1\_P HDMIO\_HOTPLUG 154 HDMI1\_TX1\_N 158 HDMI1\_TX0\_P 160 HDMI1\_TX0\_N 162 GND 164 HDMI1 CLK P DSI 0 --> 2channel 166 HDMI1\_CLK\_N 168 GND 170 172 174 HDMIO\_TX2\_N GND 176 HDMIO\_TX1\_P 178 HDMI0\_TX1\_N 180 GND 182 HDMIO\_TXO\_P HDMIO\_DO\_P DSI1\_D1\_N 184 HDMIO\_TXO\_N 186 GND DSI1 --> 4channel 188 HDMIO\_CLK\_P 190 HDMIO\_CLK\_N DSI1\_C\_P GND 191 DSI1\_D2\_N 193 DSI1\_D2\_P 195 GND 197 HDMIO\_SDA 199 192 194 DSI1\_D3\_N 196 DSI1\_D3\_P 198 GND

HDMIO\_SDA

ComputeModule4 Hirose 2off DF40C-100DS-0.4V

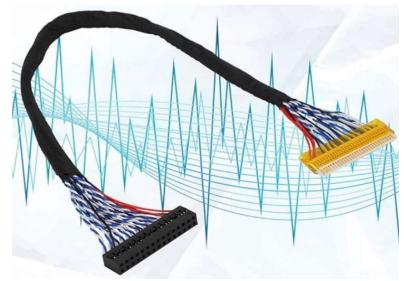
## **Dialtronics requirements**



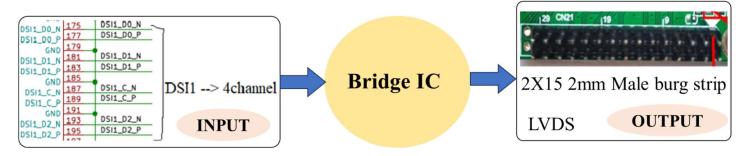
Fig2: 2x15 2mm Male Burgstrip

This connector will be populated on Dialtronics Custom CM4 I/O Board.

Dialtronics uses the connector and cable as shown in Fig2, Fig3.



**Fig3: 30Pin LVDS Cable**This cable will be connected between
Dialtronics Custom CM4 I/O Board and the
LVDS display.



Suggest a Bridge IC that can converts 4 Channel MIPI DSI to LVDS.



Suggest a Bridge IC that can converts 2 Channel MIPI DSI to LVDS.

