

Placed LVDS termination resistors as close as possible to Deserializer inputs



1. Serial Control Bus Addresses: 0x2C (7'b)
2. MAPSEL=1; MSBs on TXOUT3+/-
3. MODE_SEL: REPEAT Mode,B ackwards Compatibility Mode,
I2S & Long Cable Mode are not active

SHEET ONE (1) MUST BE SUPPLIED

Schematic1		PROJECT NO 9 OF 9	DATE A	NAME
DATE A1	SCALE NA			

* DO NOT PLACE COMPONENT
@ PLACE CLOSE TO PIN.

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