

```
#define UB954_ID      0x60
#define UB953_ID      0x30
#define UB953_ALIAS_ID  0x18
#define OV2740_ID      0x6C
#define OV2740_ALIAS_ID 0x4C
#define UB954_READ_CMD 0x61
#define UB953_READ_CMD 0x19
#define SENSOR_READ_CMD 0x6D
```

```
UB954_ID, 0x01, 0x03
UB954_ID, 0x4C, 0x01
UB954_ID, 0x58, 0x5E
UB954_ID, 0x5B, UB953_ID
UB954_ID, 0x5C, UB953_ALIAS_ID
UB954_ID, 0x5D, OV2740_ID
UB954_ID, 0x65, OV2740_ALIAS_ID
UB954_ID, 0x6D, 0x7C
UB954_ID, 0x32, 0x01
UB954_ID, 0x33, 0x21
UB954_ID, 0x21, 0x04
UB954_ID, 0x20, 0x20 /* FWD_CTL1 Register: enable RX Port 0 */
UB954_ID, 0x1F, 0x02
UB954_ID, 0x72, 0x00
UB954_ID, 0x0A, 0x7C
UB954_ID, 0x0B, 0x7C
UB954_ID, 0x0C, 0x81
```

```
UB953_ALIAS_ID, 0x01, 0x03
UB953_ALIAS_ID, 0x02, 0x33
UB953_ALIAS_ID, 0x32, 0x49
UB953_ALIAS_ID, 0x08, 0xFE
UB953_ALIAS_ID, 0x03, 0x48
UB953_ALIAS_ID, 0x0B, 0x7C
UB953_ALIAS_ID, 0x0C, 0x7C
UB953_ALIAS_ID, 0x06, 0x41 /* Set output clock: 24MHZ. */
UB953_ALIAS_ID, 0x07, 0x28
```