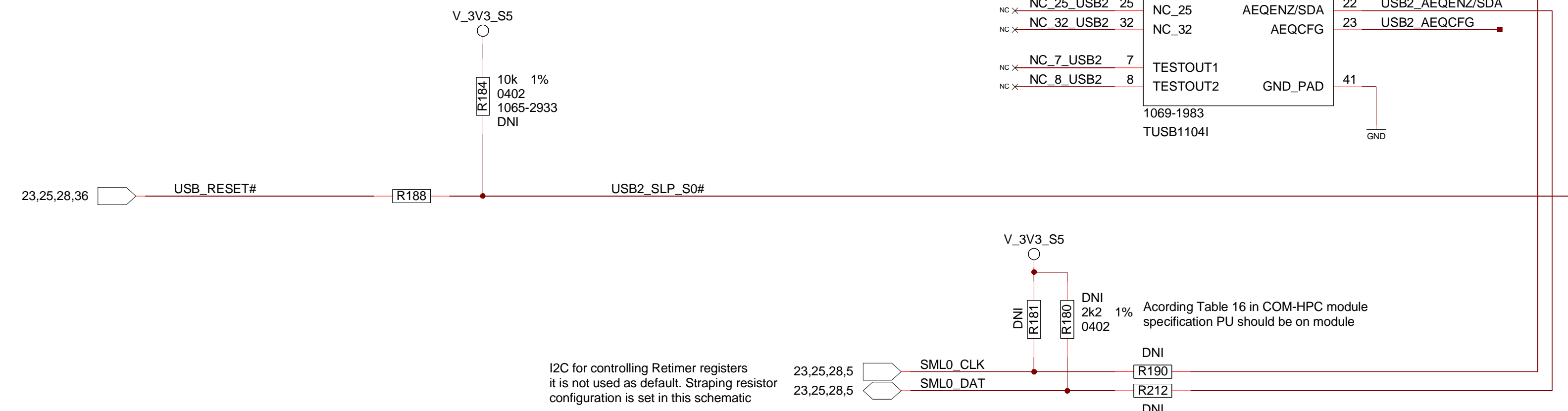


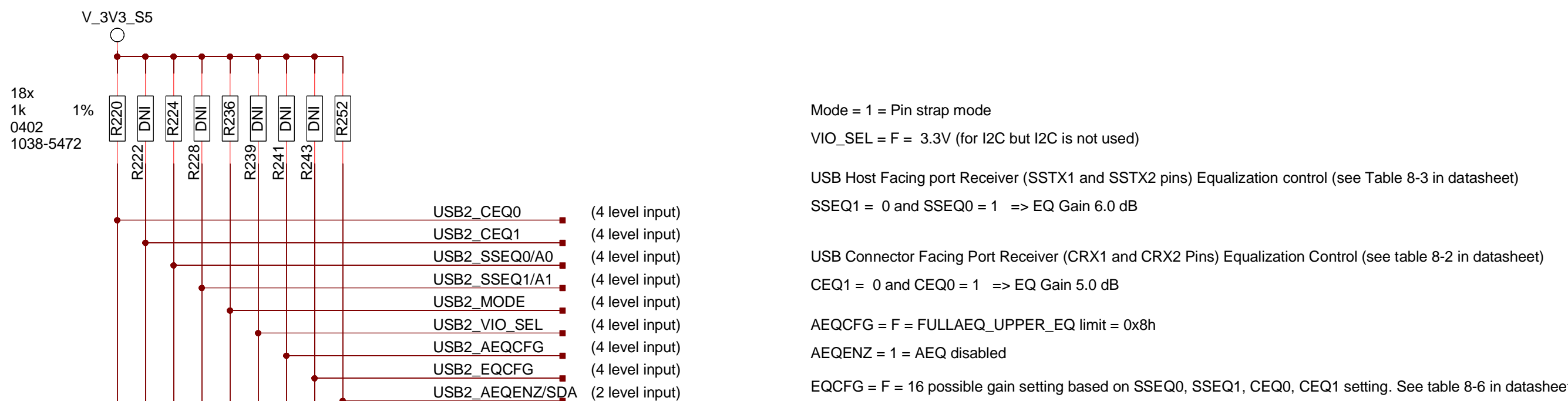
USB-C Port 2

USB 3.2x2

Note: It is possible to use alternatively TUSB1142 (with MUX/DEMUX) instead of TUSB1104I with these differences:
 MODE (pin 17)
 TUSB1142: LOW -> pin-strap mode / FLOAT -> I2C mode
 TUSB1104I: HIGH -> pin-strap mode / FLOAT -> I2C mode
 TEST1 (pin 27)
 TUSB1142: TI Test1. Under normal operations, this pin shall be connected directly or pull-down to GND
 TUSB1104I: TI Test1. Under normal operations this pin shall be connected directly or pulled up to VCC

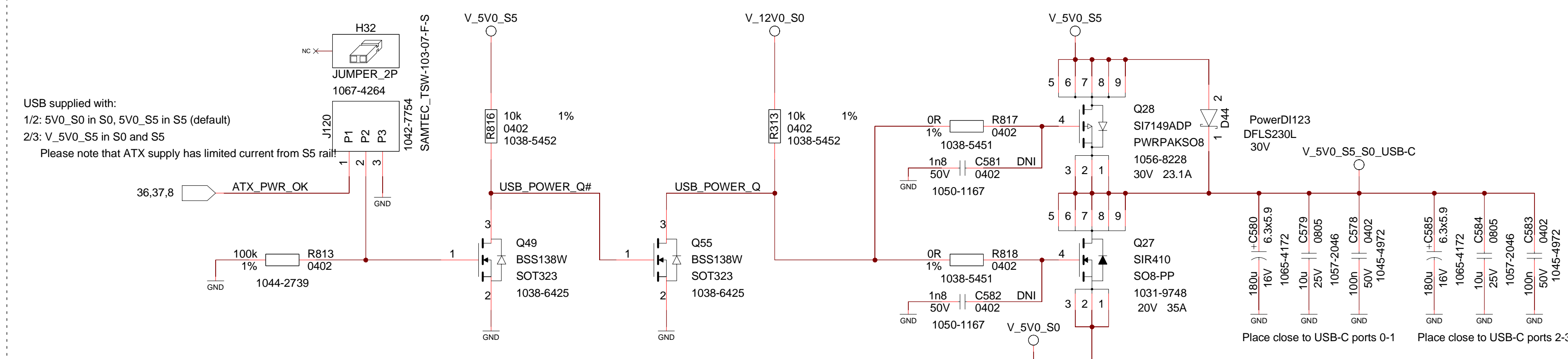


HW strapping redriver configuration

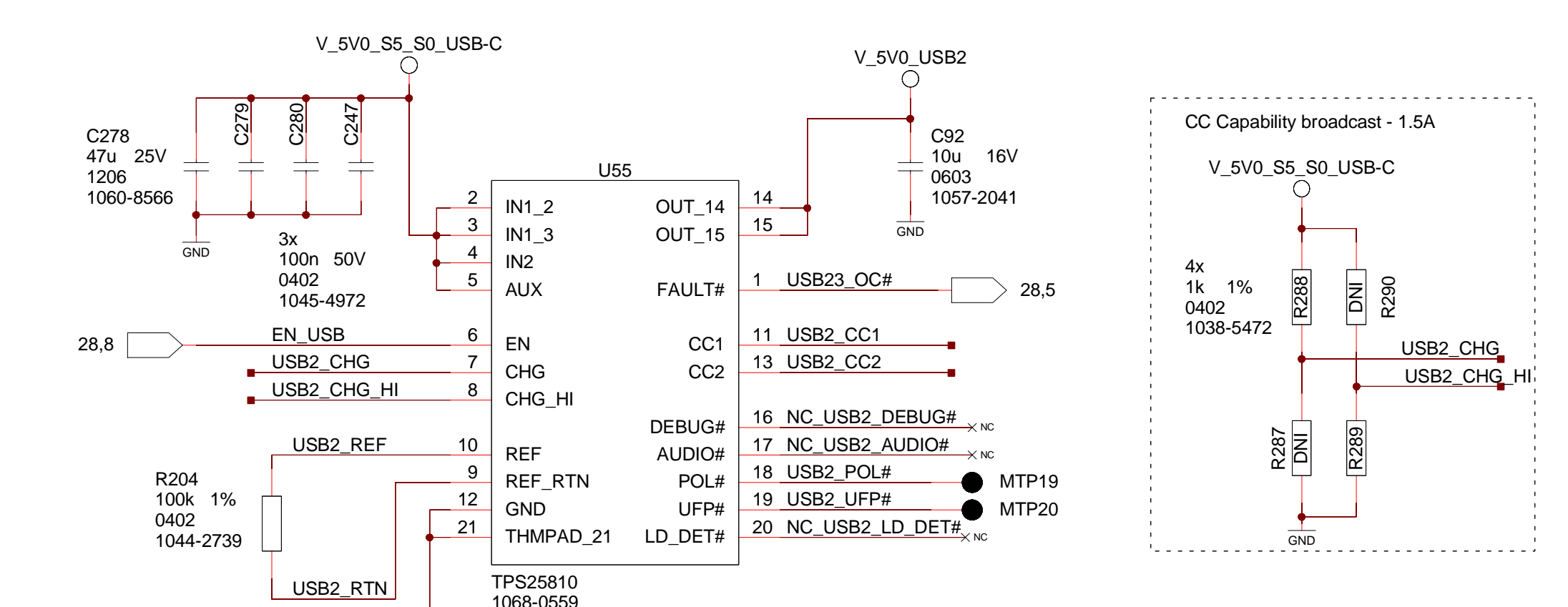
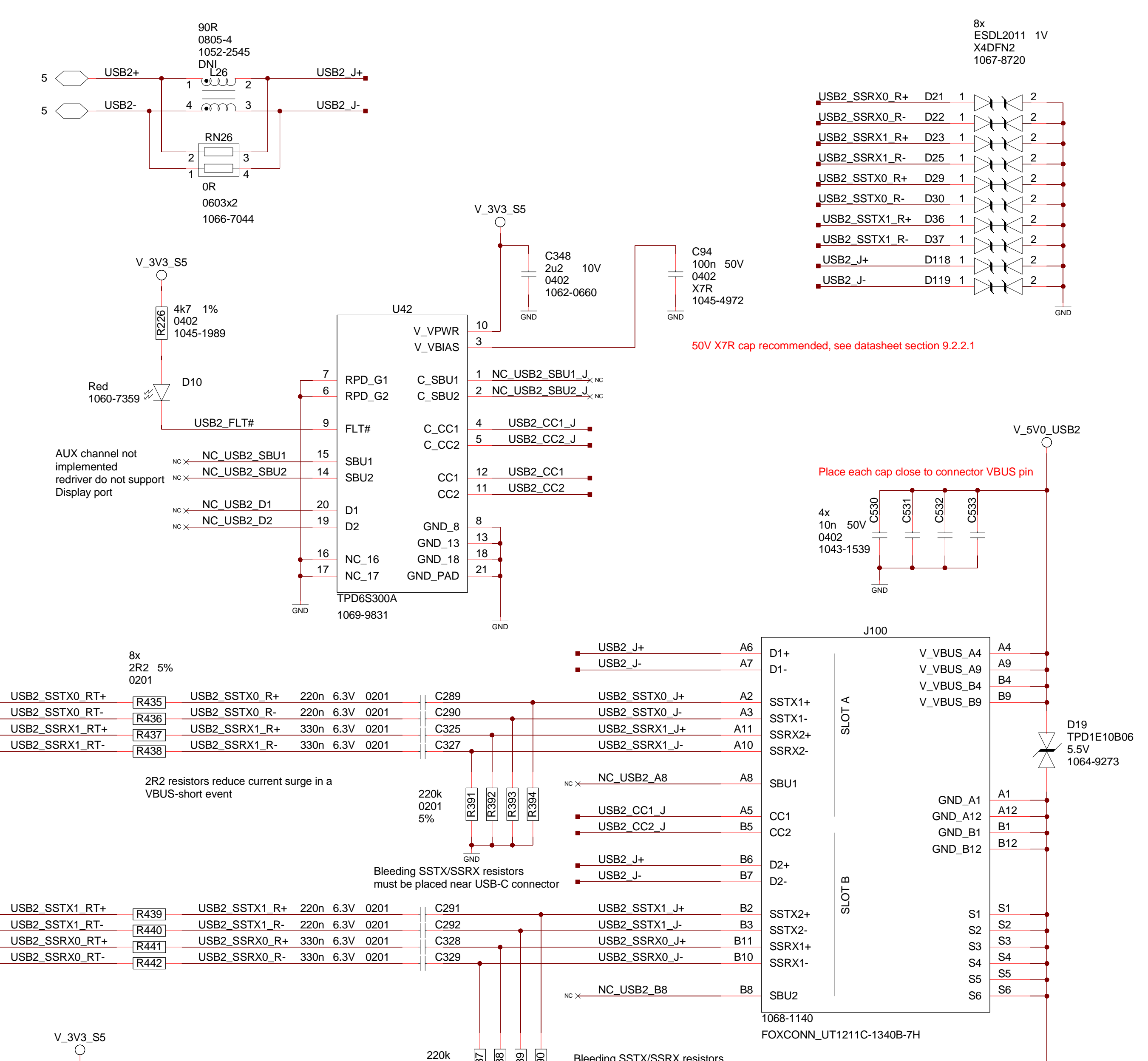


Mode = 1 = Pin strap mode
 VIO_SEL = F = 3.3V (for I2C but I2C is not used)
 USB Host Facing port Receiver (SSTX1 and SSTX2 pins) Equalization control (see Table 8-3 in datasheet)
 SSEQ1 = 0 and SSEQ0 = 1 => EQ Gain 6.0 dB
 USB Connector Facing Port Receiver (CRX1 and CRX2 Pins) Equalization Control (see table 8-2 in datasheet)
 CEQ1 = 0 and CEQ0 = 1 => EQ Gain 5.0 dB
 AEQCFG = F = FULLAEQ_UPPER_EQ limit = 0x8h
 AEQENZ = 1 = AEQ disabled
 EQCFG = F = 16 possible gain setting based on SSEQ0, SSEQ1, CEQ0, CEQ1 setting. See table 8-6 in datasheet

USB-C 5V S5/S0 switch



USB supplied with:
 1/2: 5V0_S0 in S0, 5V0_S5 in S5 (default)
 2/3: V_5V0_S5 in S0 and S5
 Please note that ATX supply has limited current from S5 rail!



PROJECT	60-60220		
	ADHC COM-HPC Client Carrier		
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SHEET	27 - USB-C Port2		CREATED: KCZ PD, MV 2021-09-29
PCB ID	PCB INDEX	LAST MODIFIED	VERIFIED: KCZ TBD TBD
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PAGE		27 OF 40	DRAWING ID REV
			ADHC111 B0