Hello,

we had some problems with the MIPI connection between a camera and the Serializer 953. We see with an analyzer a delay on lane 2.

How critical is it for the DS90UB953, if as shown in the following picture, the SOT Synq Pattern on MIPI CSI-2 lane 2 (if Lane\_n, n = 0,1,2,3) has an delay of a clock period?

Ein Bild, das Text, Screenshot enthält.

Automatisch generierte Beschreibung