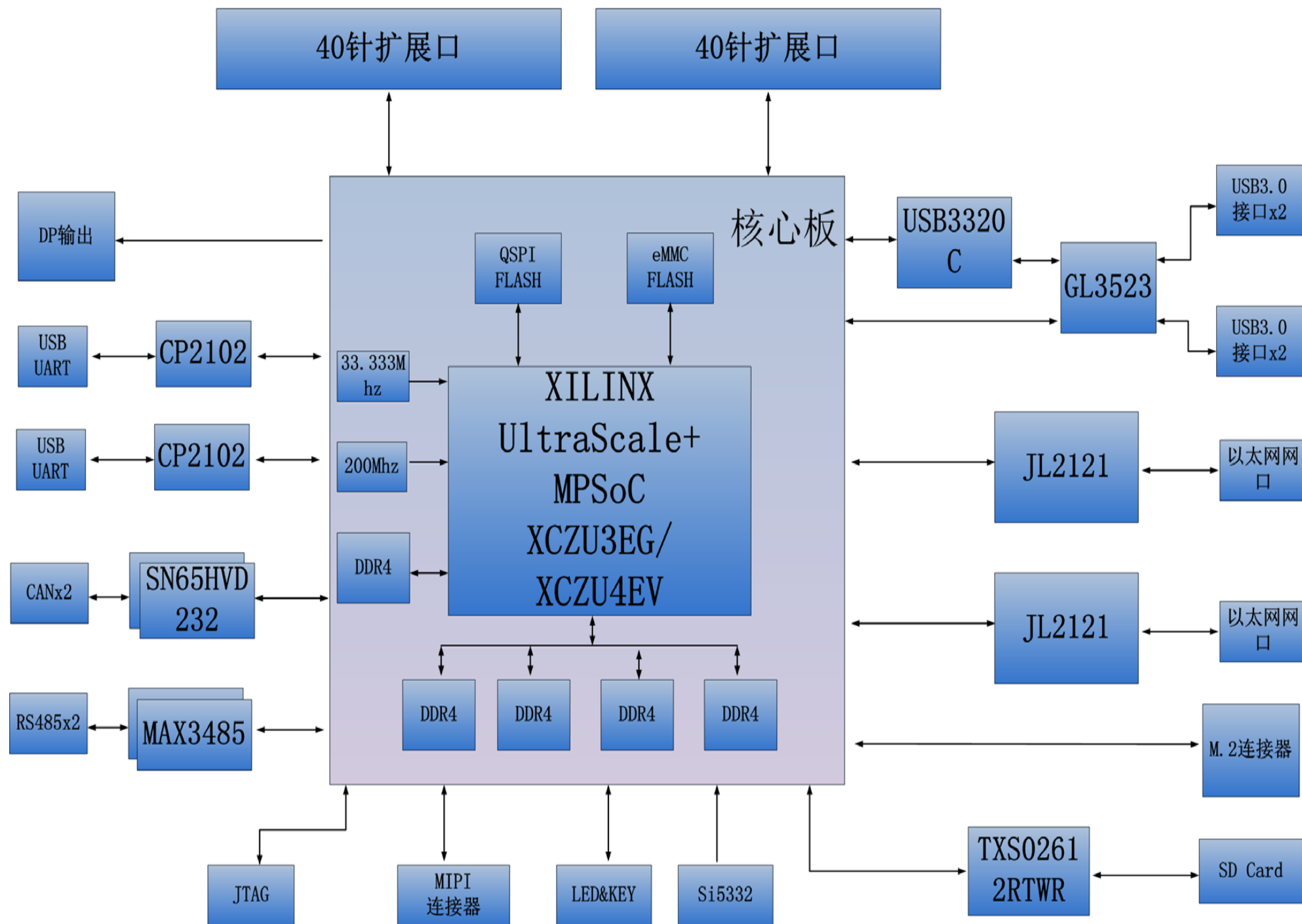
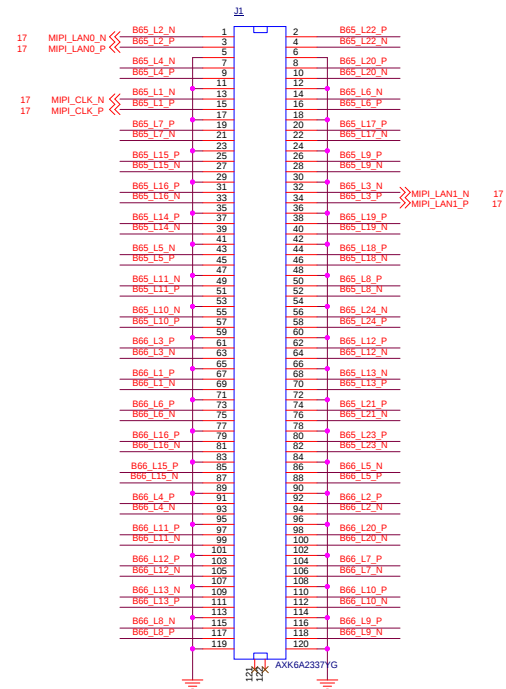
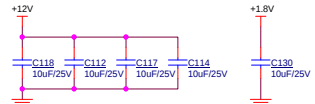
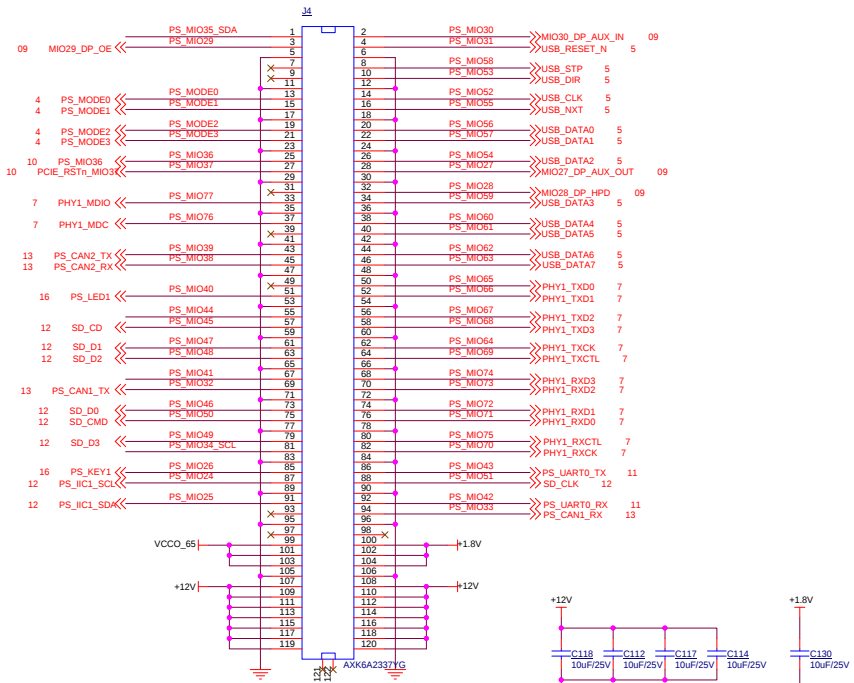
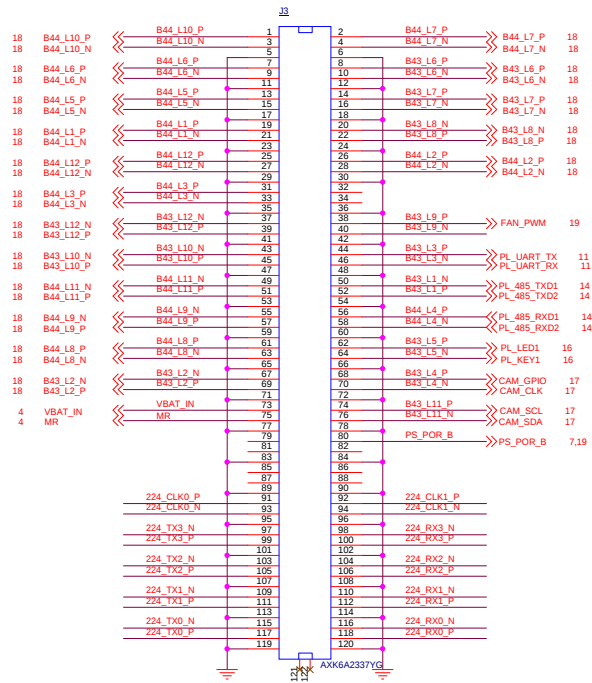
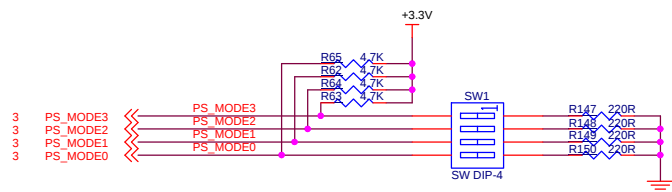




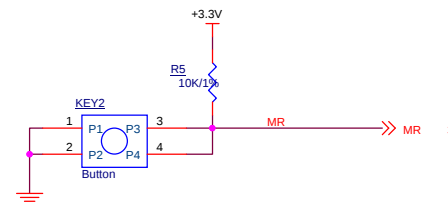
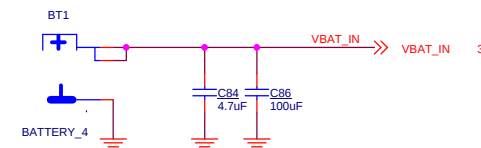
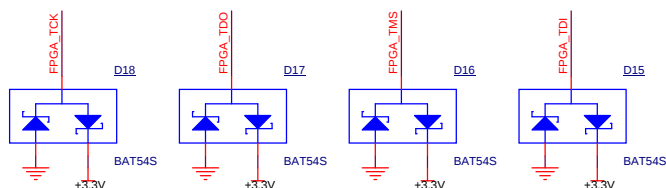
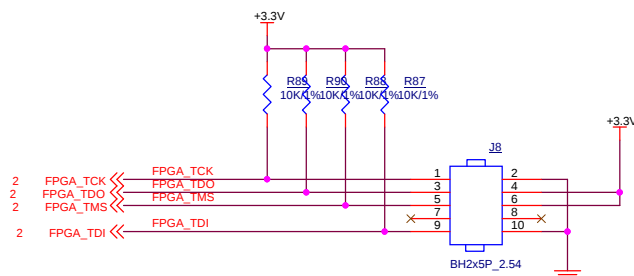


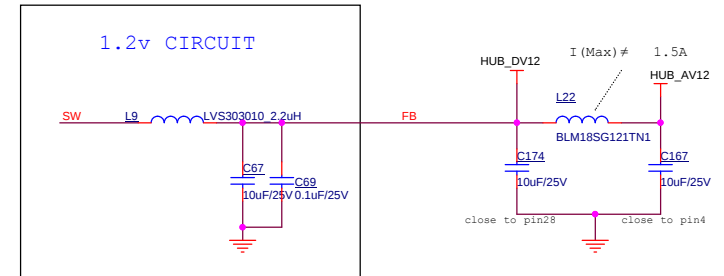
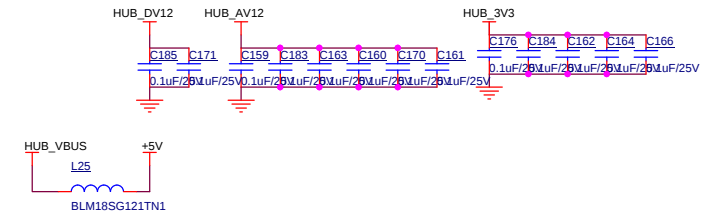
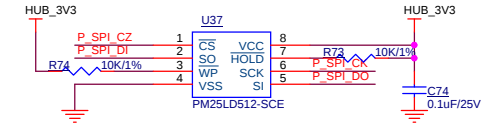


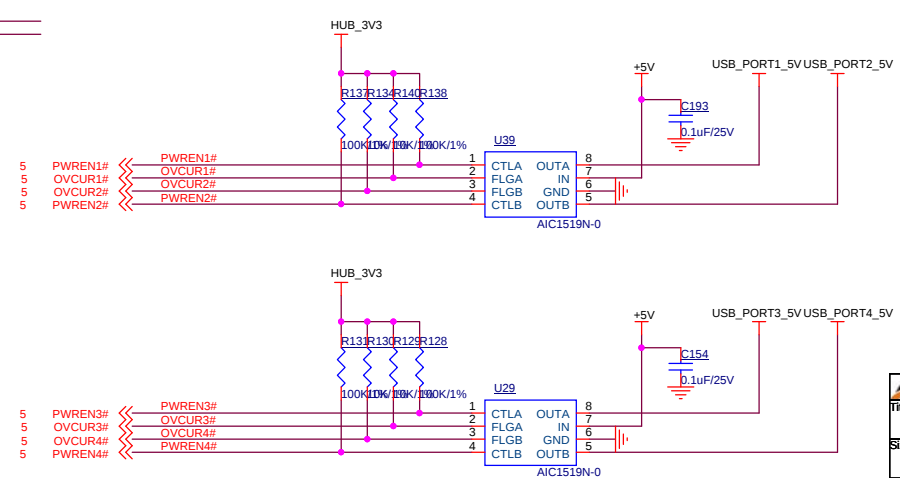
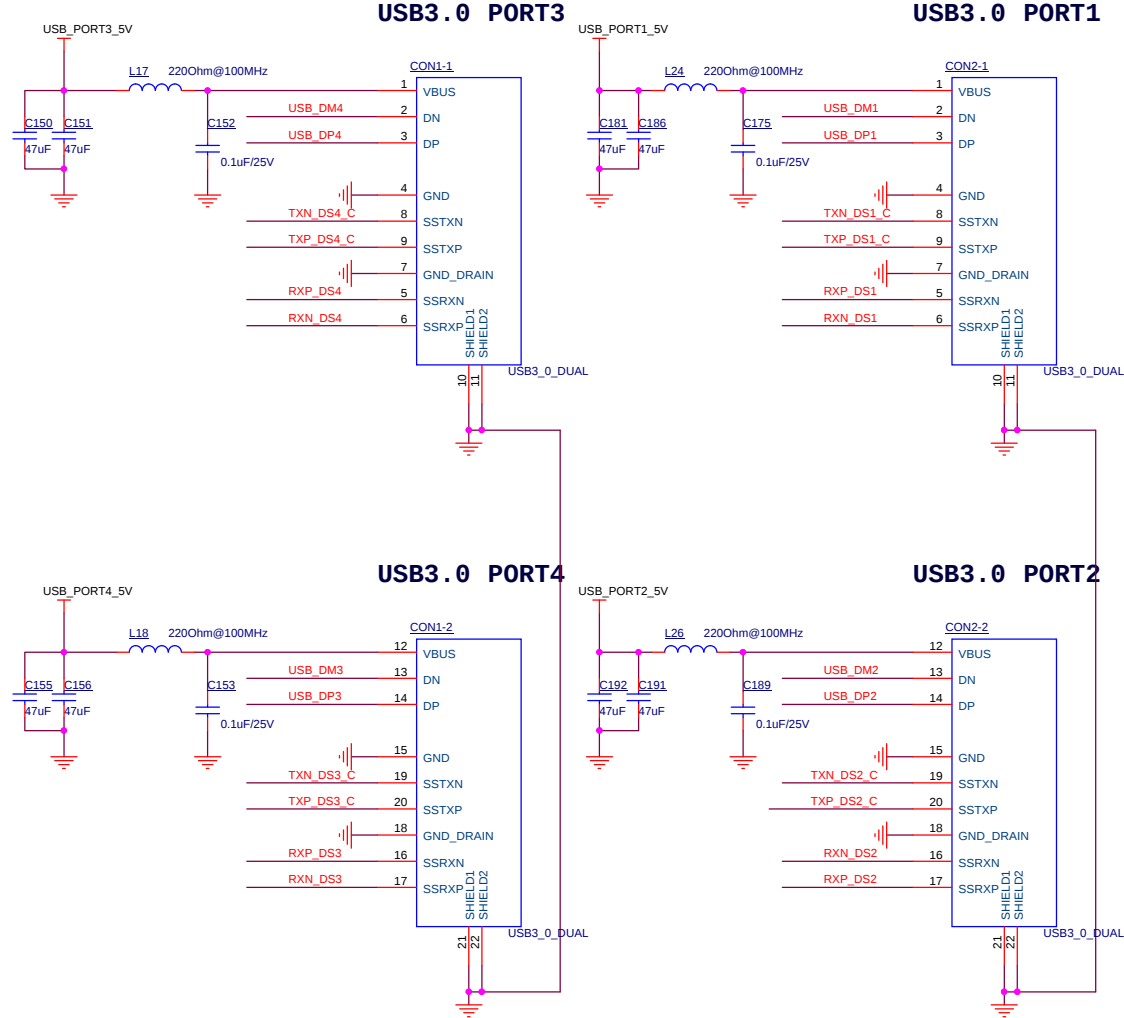
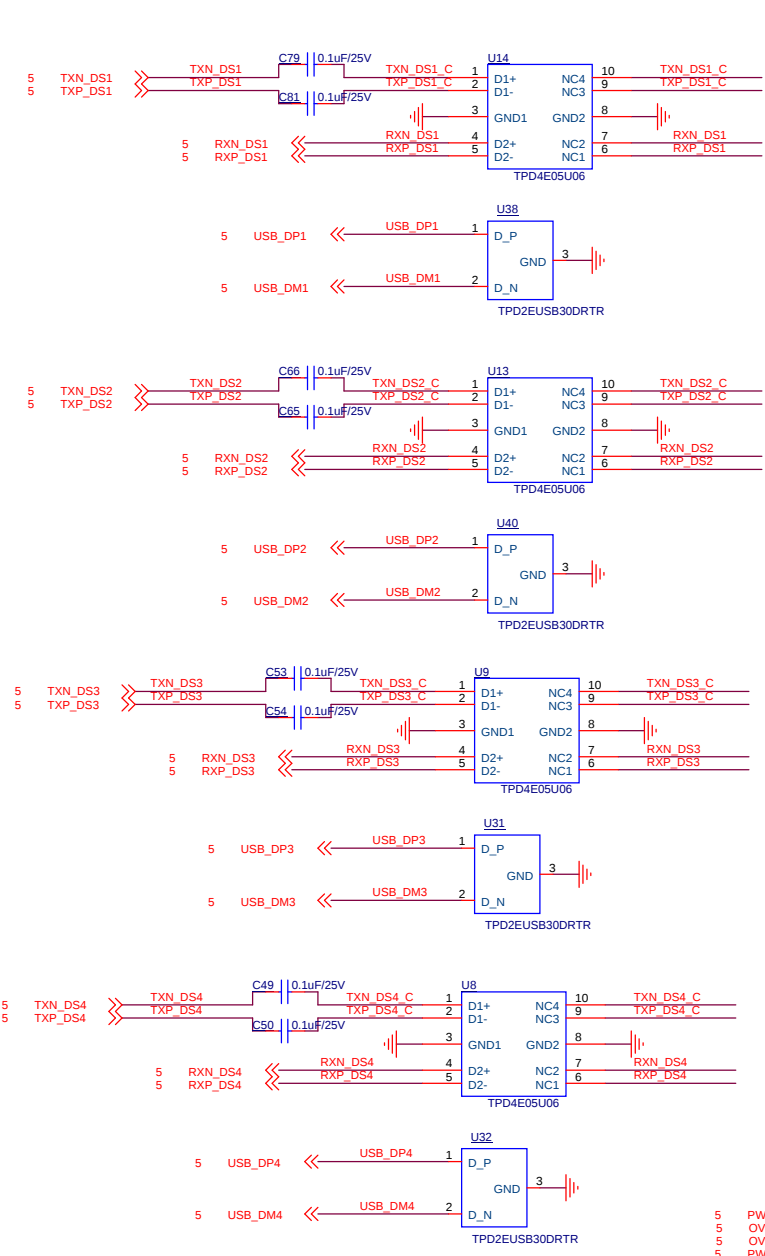


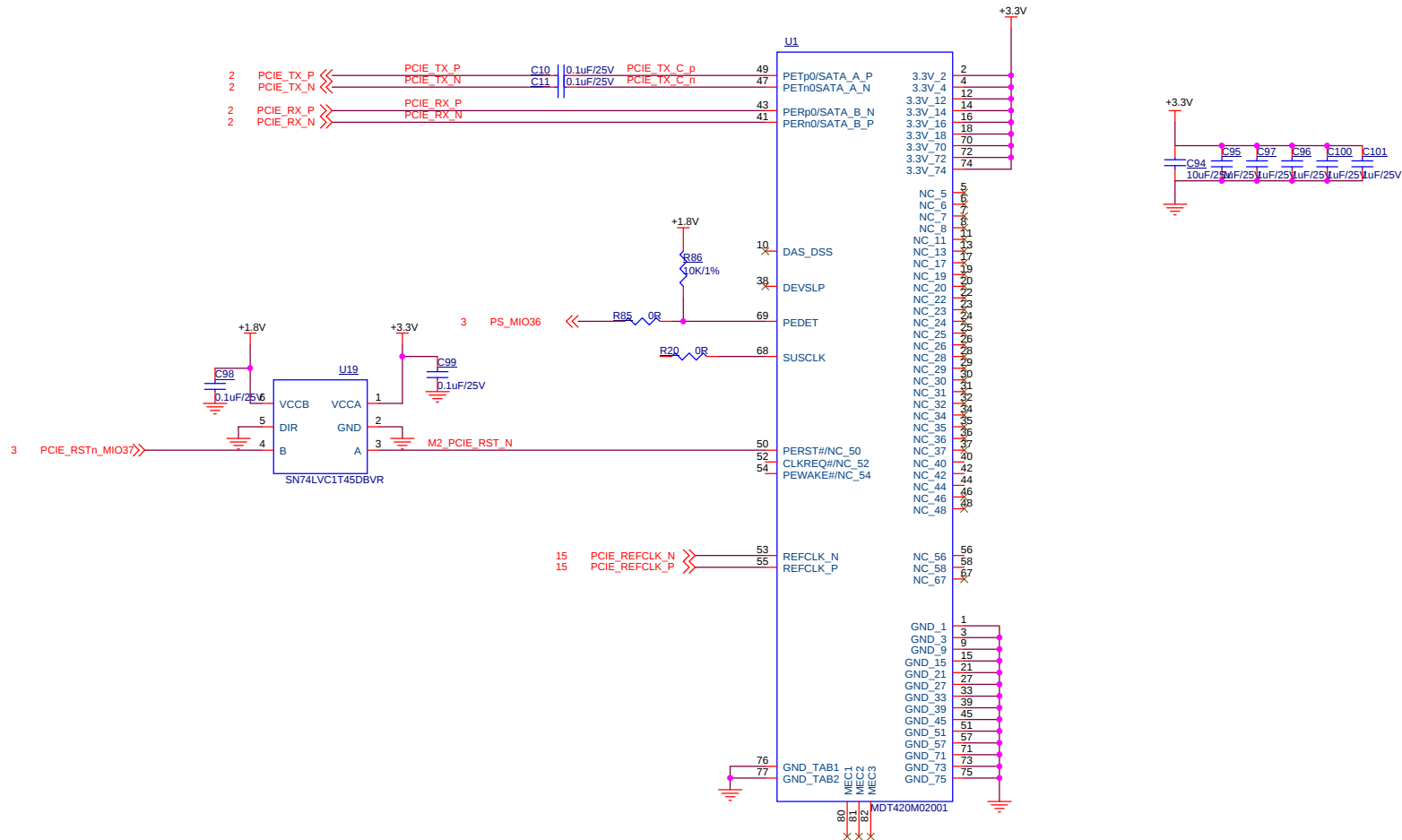
MODE[3:0]	BOOT MODE	Description
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

JTAG Connector

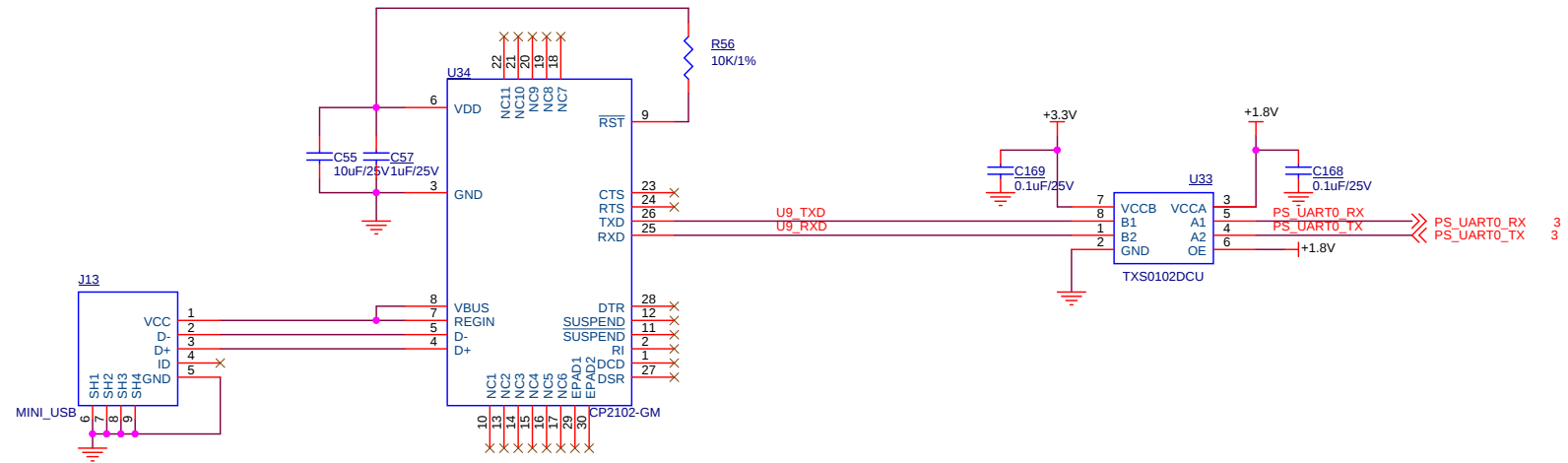




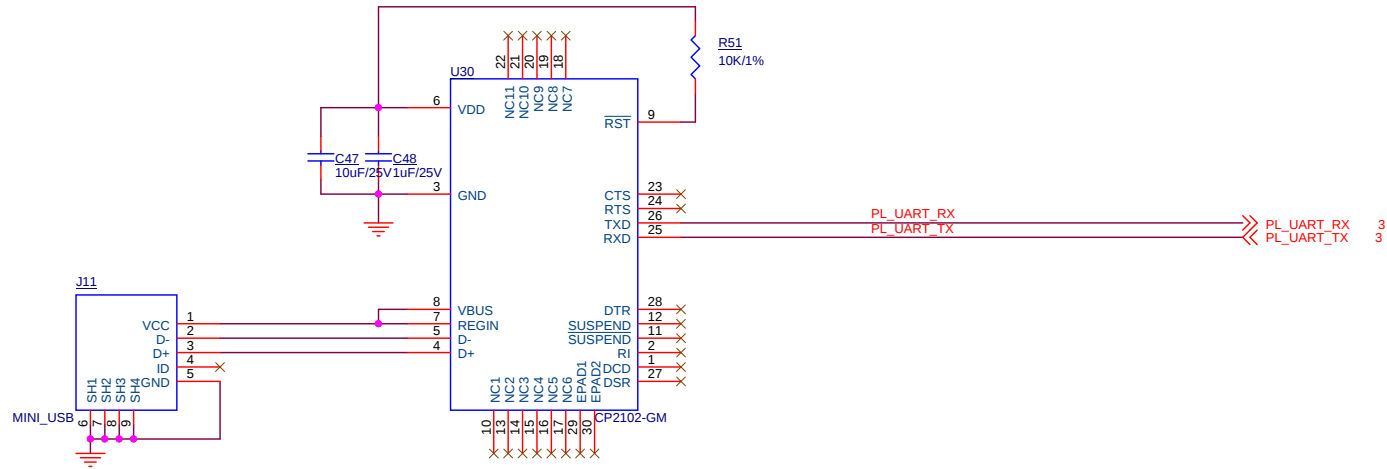




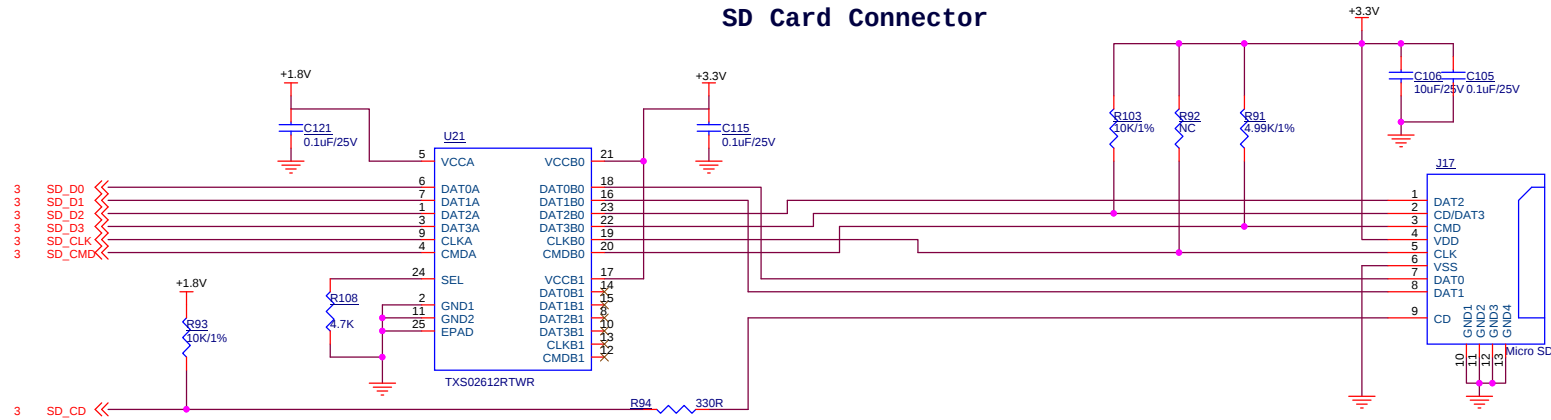
PS UART PORT



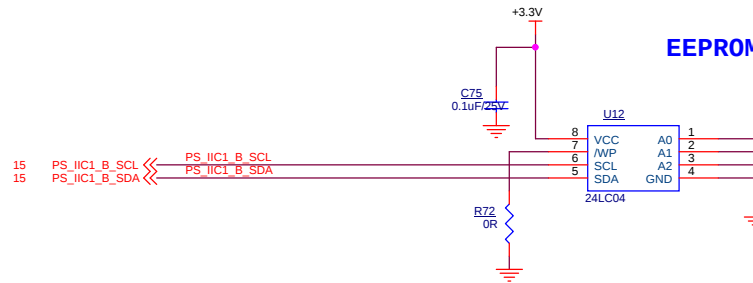
PL UART PORT



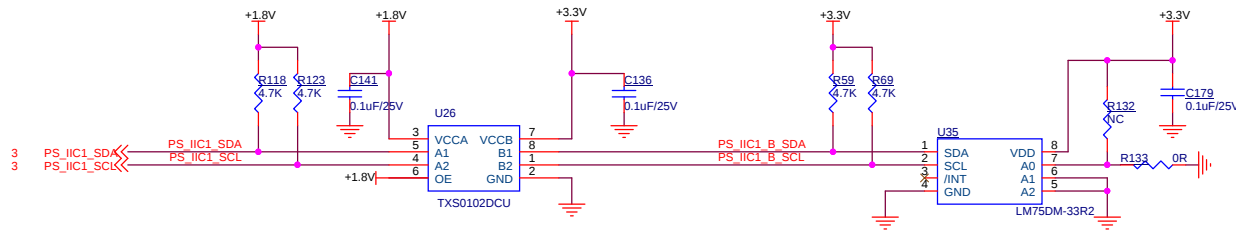
SD Card Connector

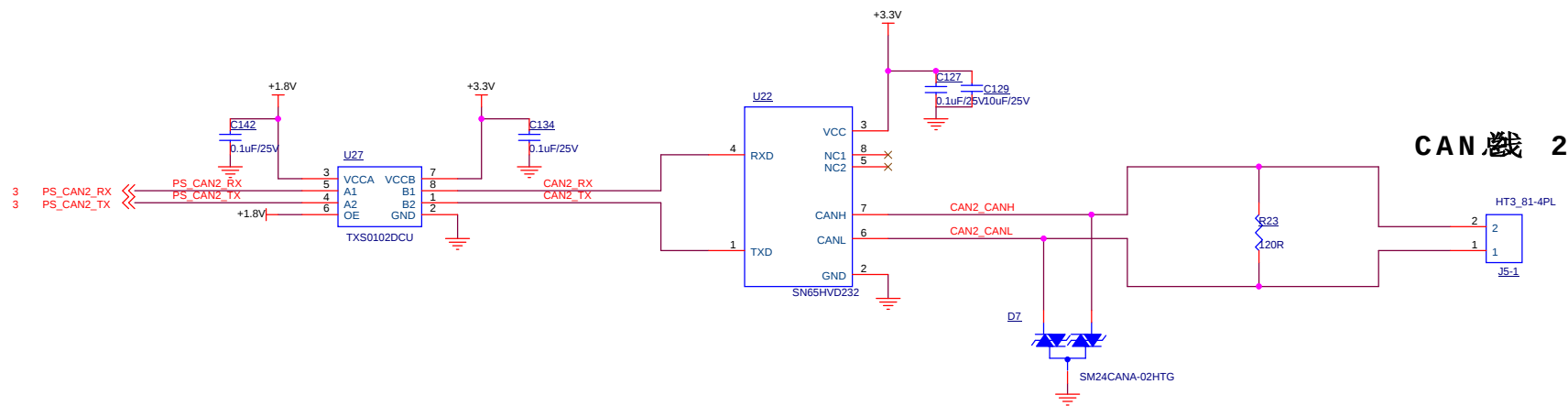
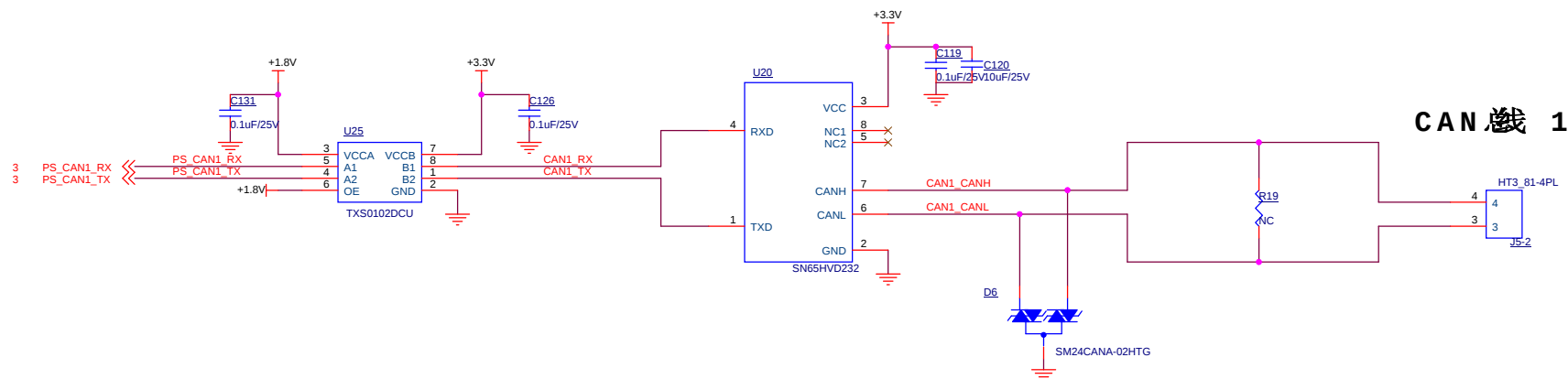


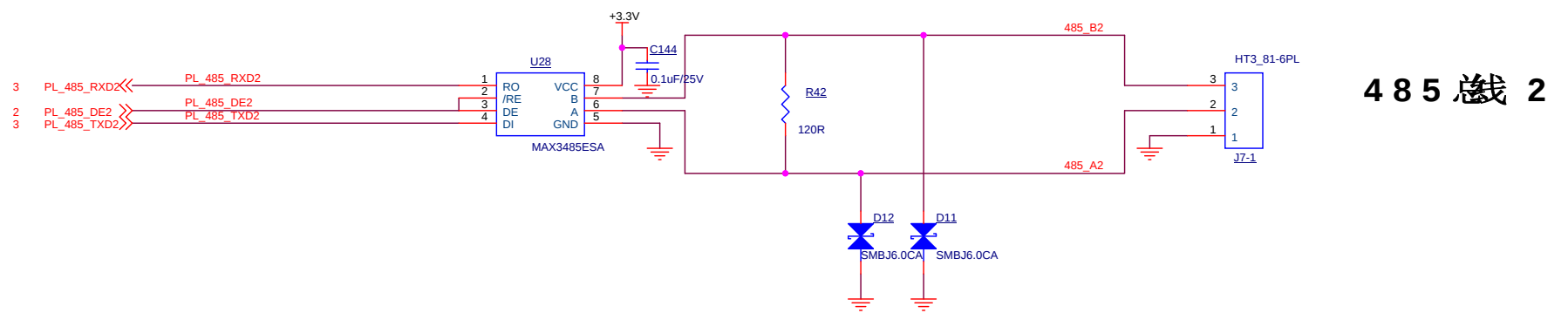
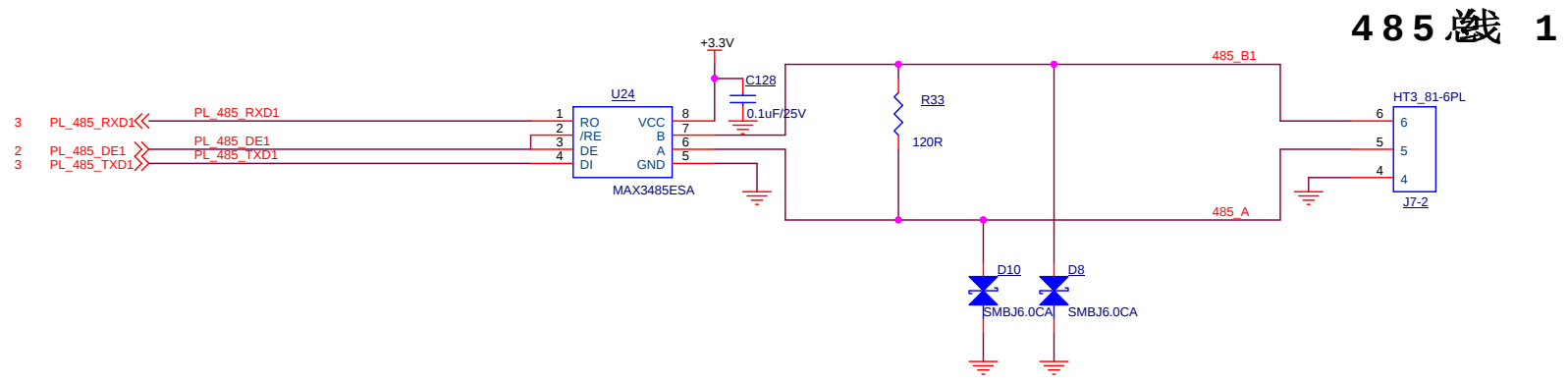
EEPROM

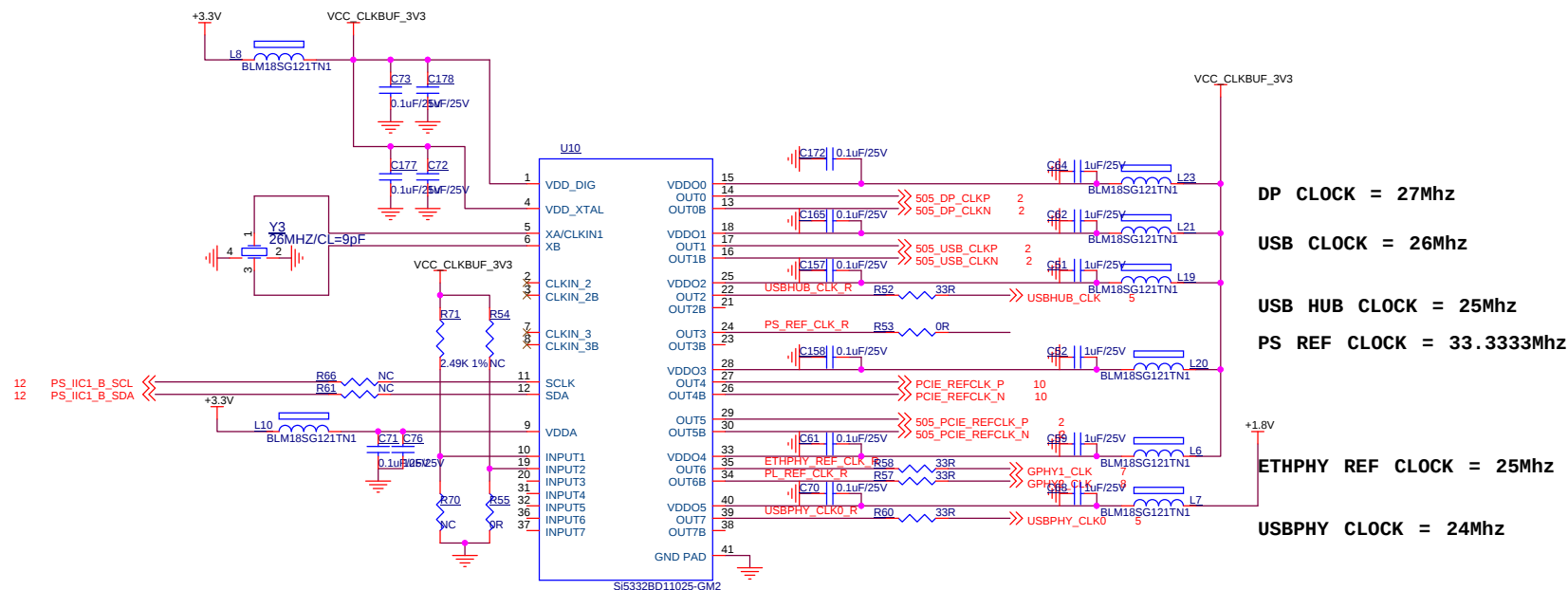


SENSOR

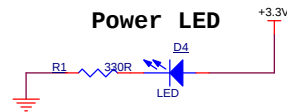
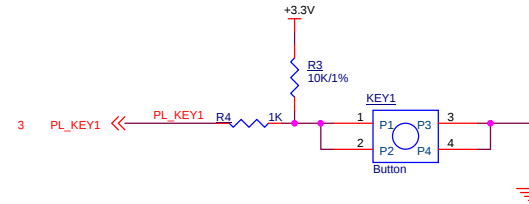
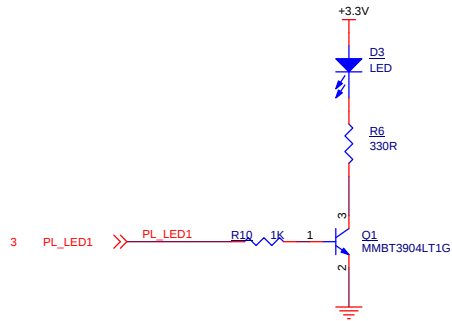
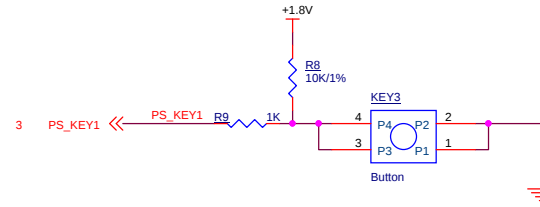
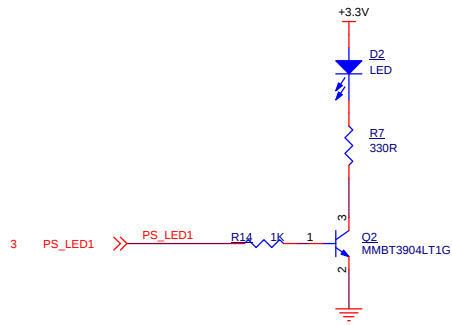


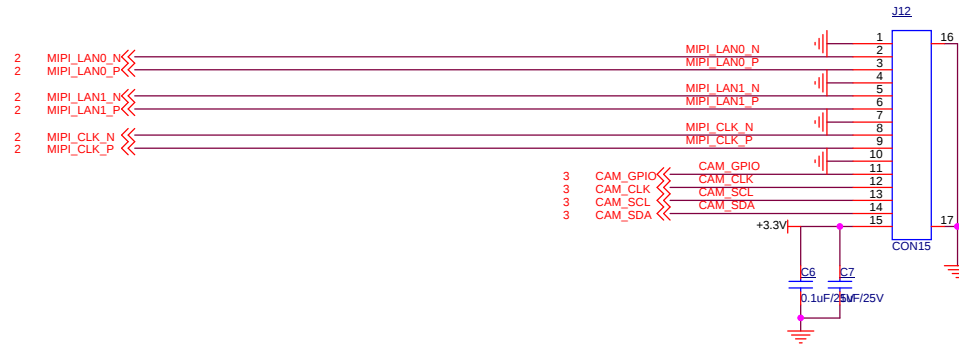
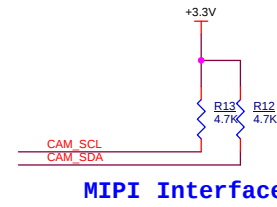


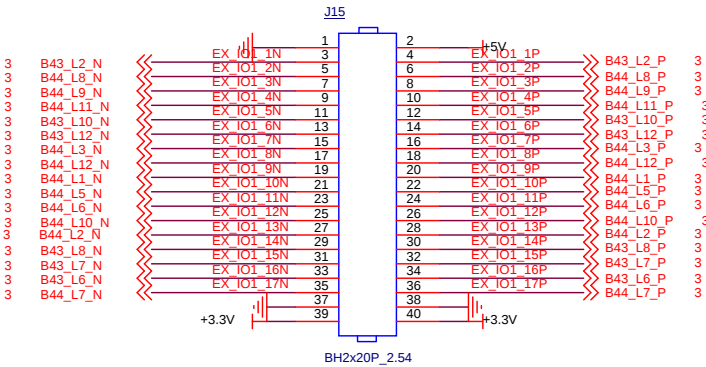
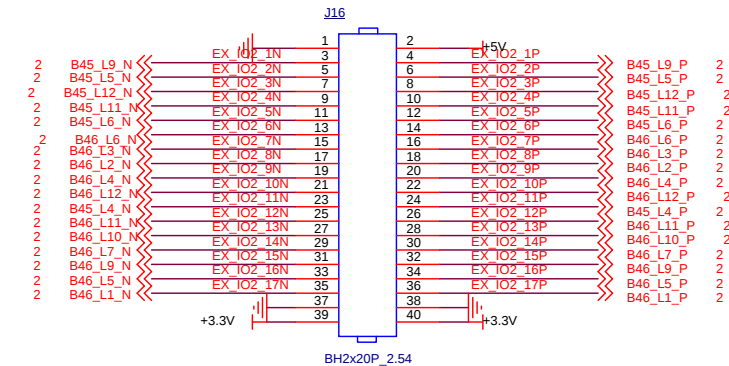




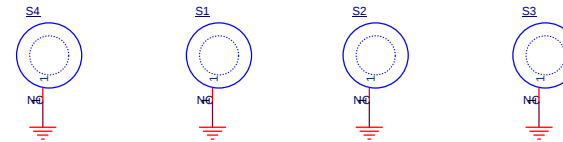
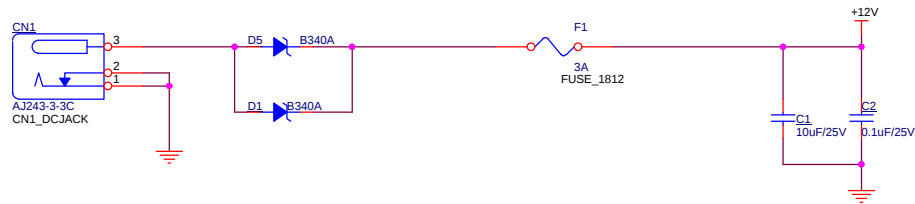
PS_IIC1: IIC ADDRESS IS 0x76



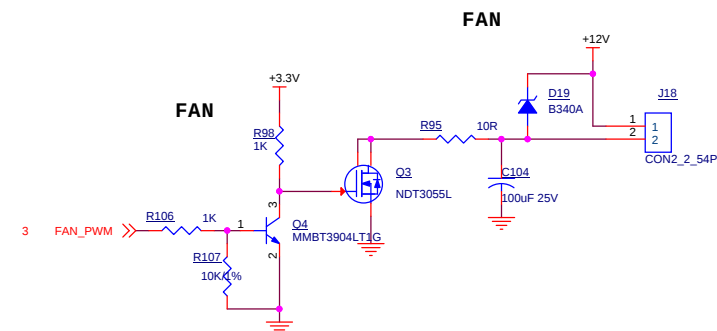
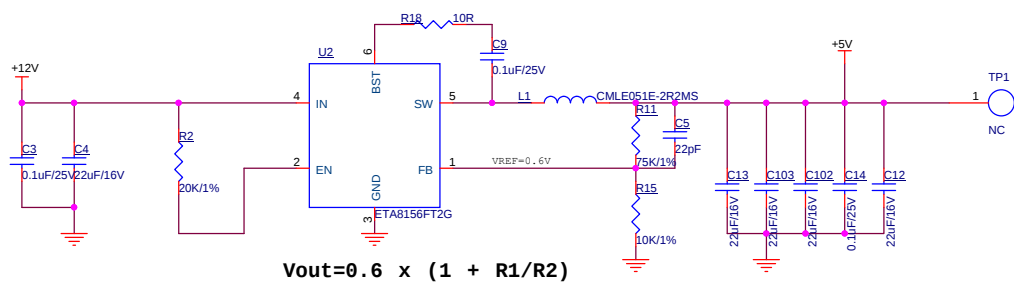




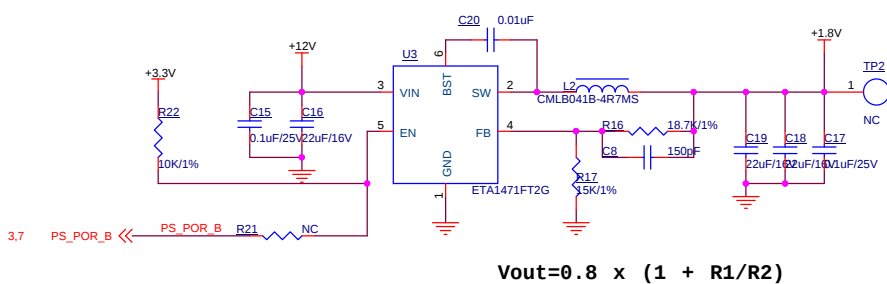
12V/3A AC Adapter



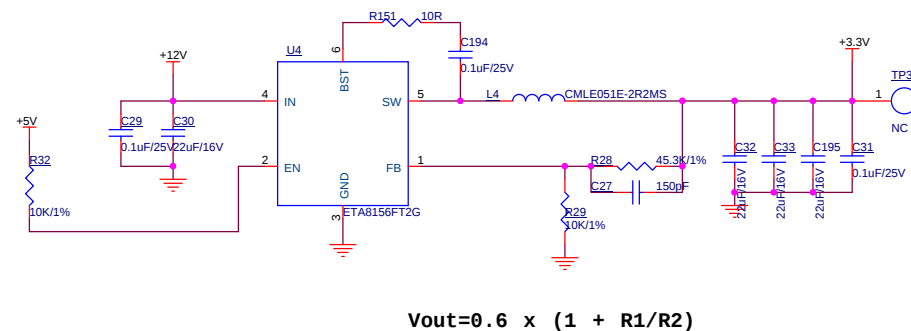
+5V POWER 6A



+1.8V POWER



+3.3V POWER 6A



500mA

