

Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

A

B

C

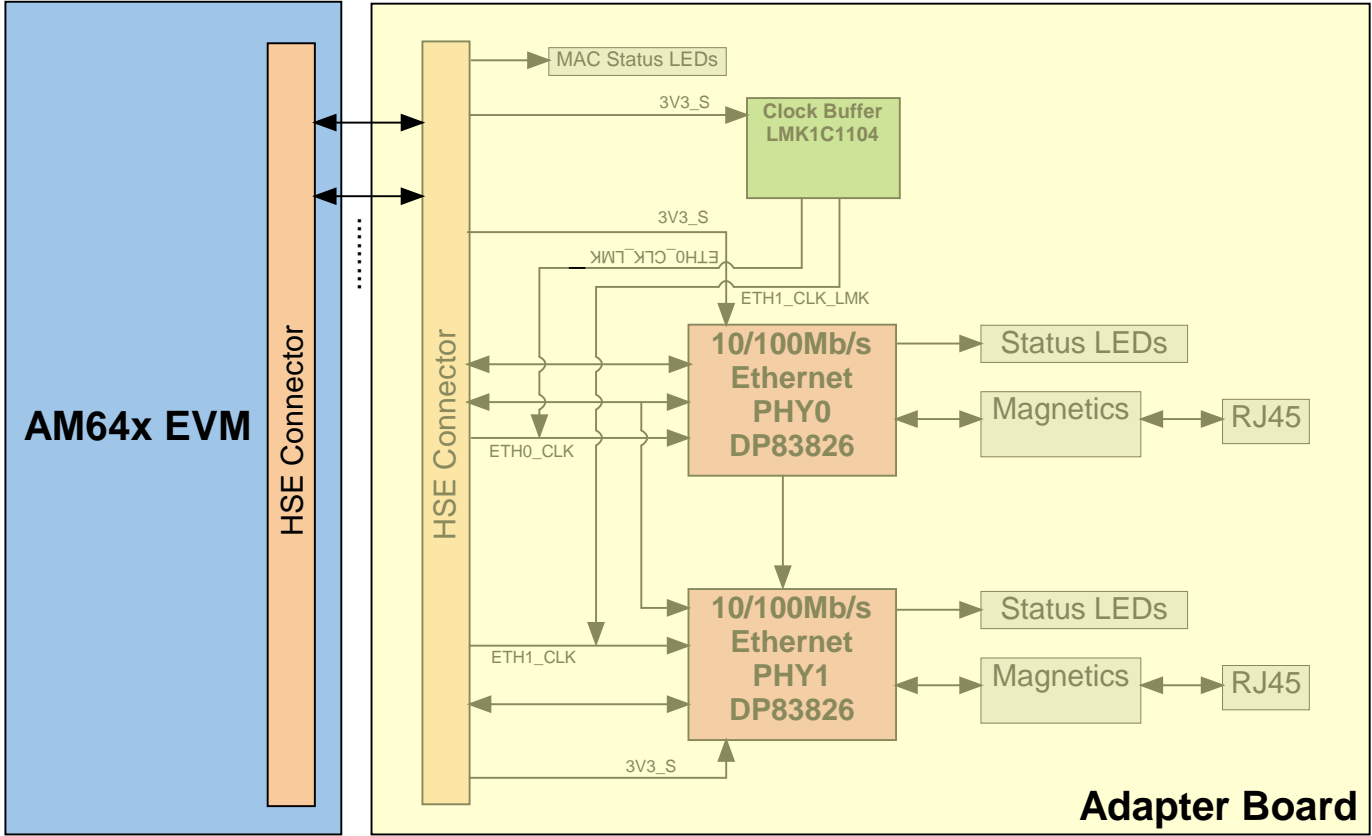
D

A

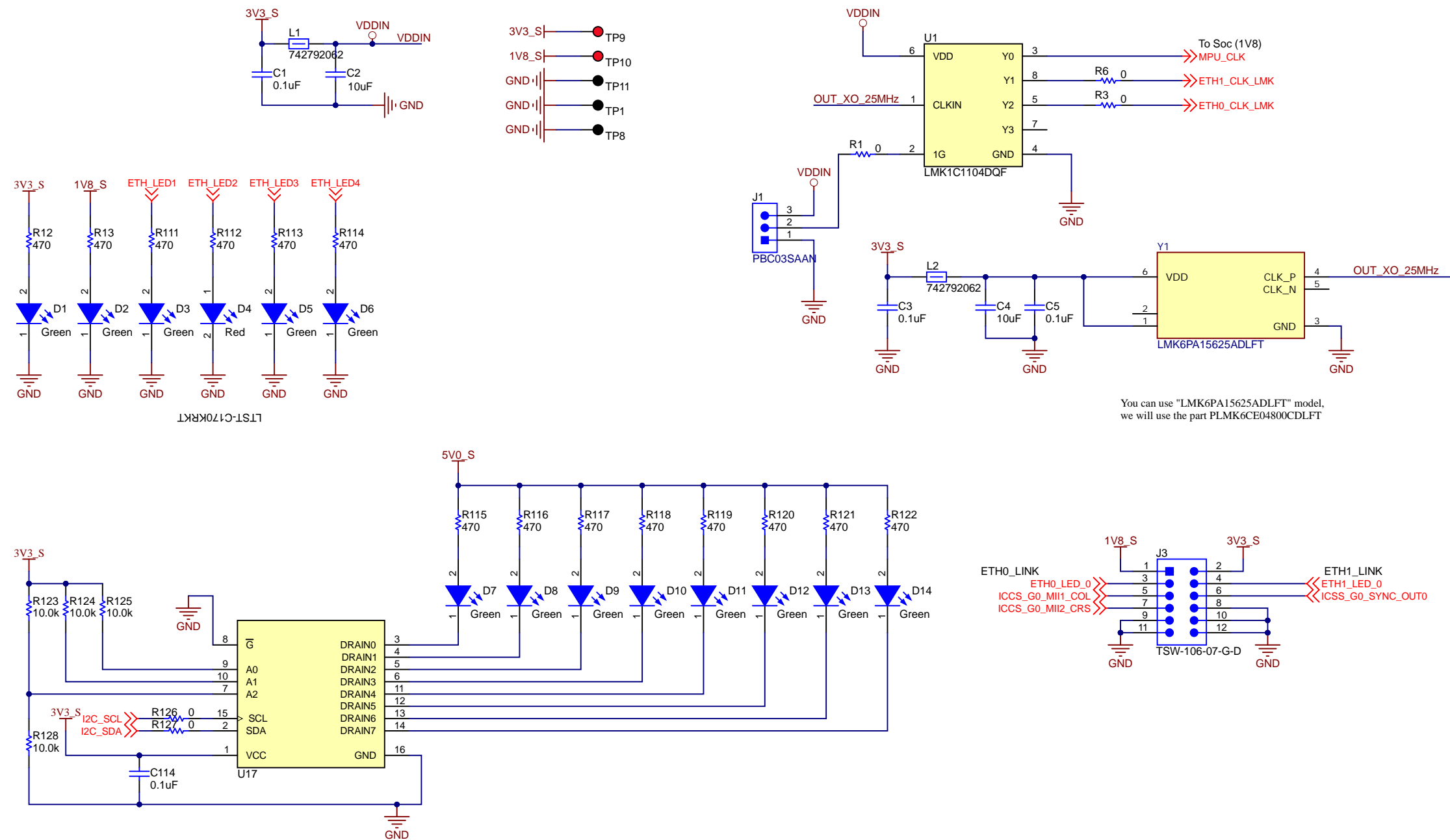
B


C

D

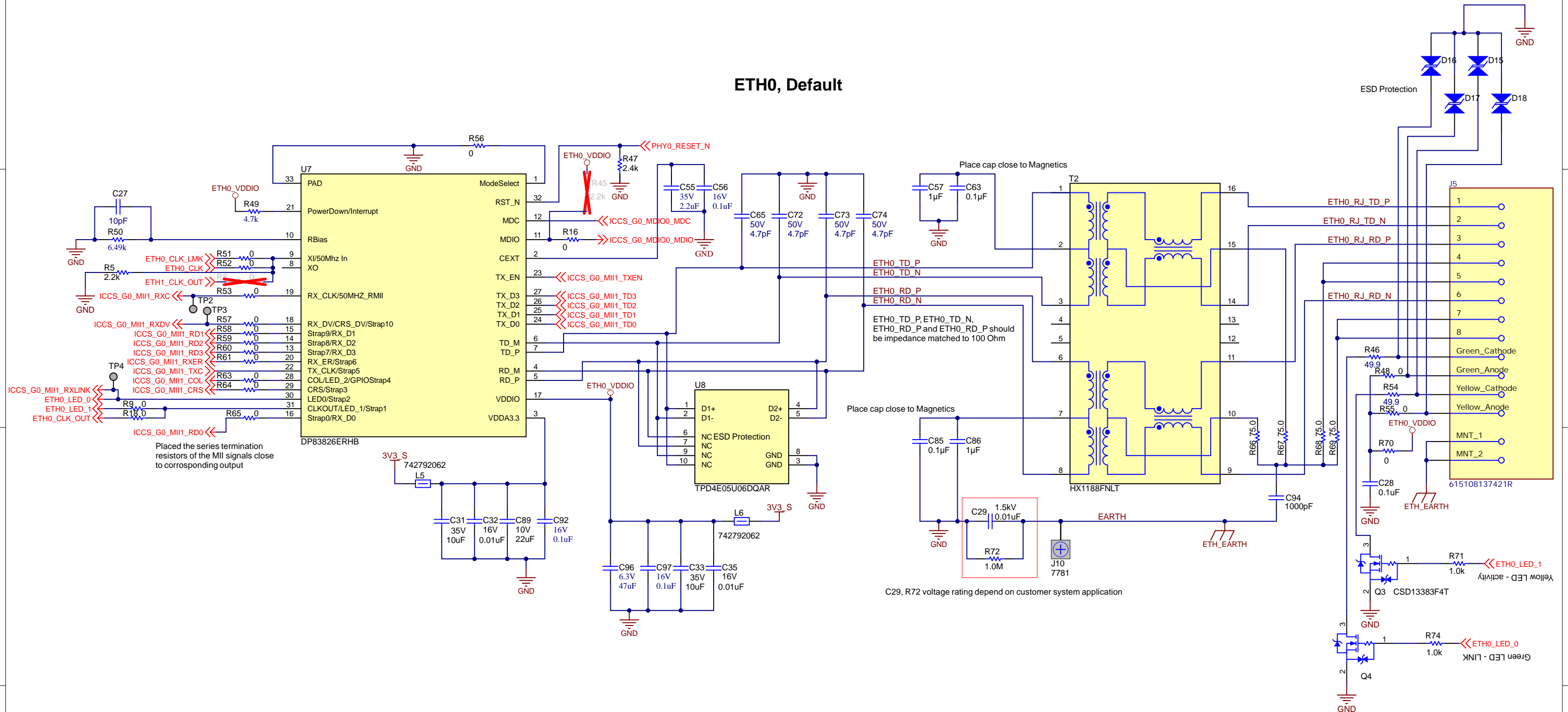



Block Diagram



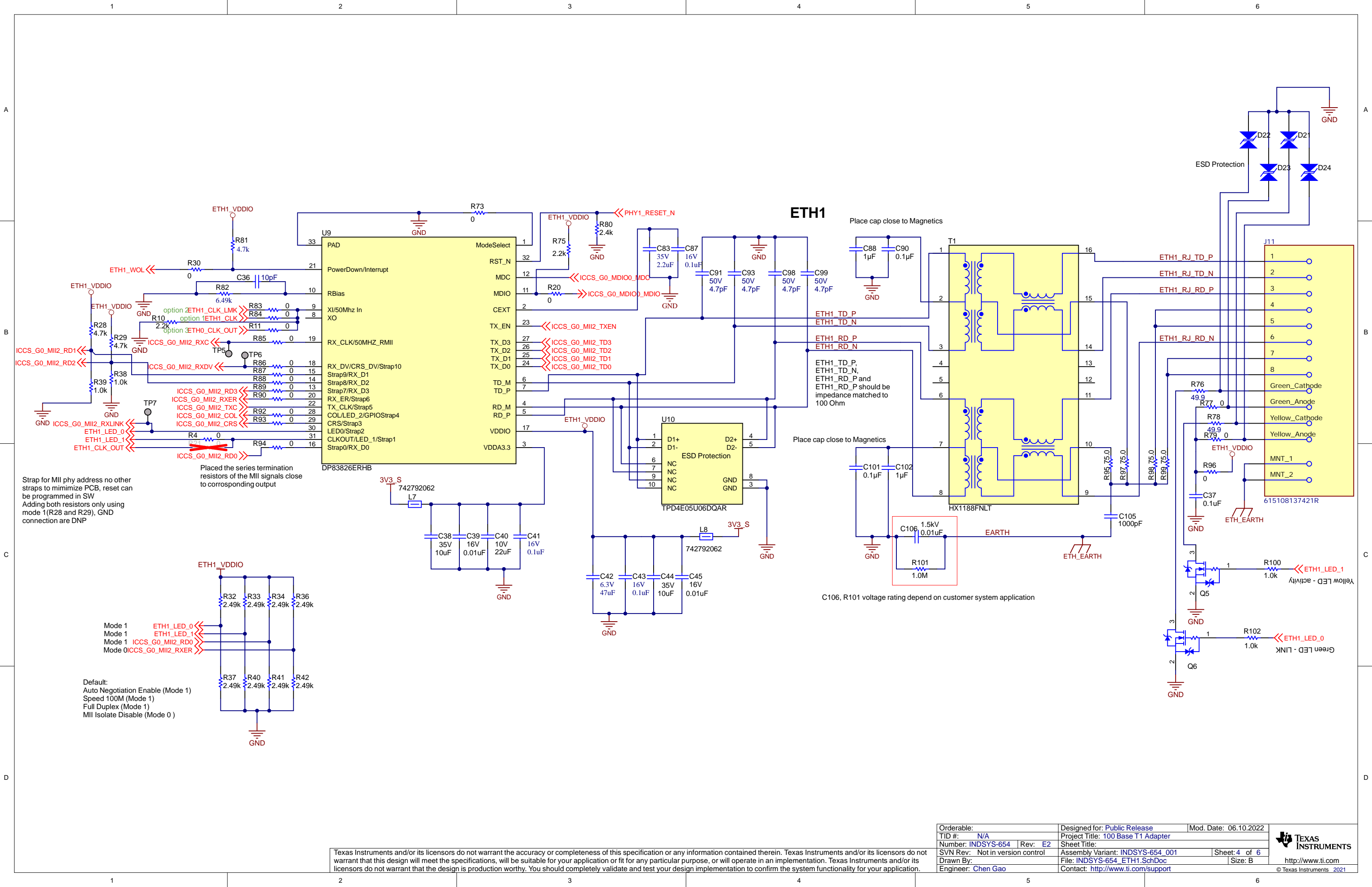
Orderable:	Designed for: Public Release	Mod. Date: 06.10.2022	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2021
TID #: N/A	Project Title: 100 Base T1 Adapter		
Number: INDSYS-654	Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: INDSYS-654_001	Sheet: 2 of 6	
Drawn By:	File: INDSYS-654_CLK_CFG.SchDoc	Size: B	
Engineer: Chen Gao	Contact: http://www.ti.com/support		

ETH0, Default



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Number: INDSYS-654 Rev: E2	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: INDSYS-654_001	Sheet: 3 of 6	
Drawn By:	File: INDSYS-654_ETH0.SchDoc	Size: B	
Engineer: Chen Gao	Contact: http://www.ti.com/support		

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Strap for MII phy address no other straps to minimize PCB, reset can be programmed in SW
Adding both resistors only using mode 1(R28 and R29), GND connection are DNP

Placed the series termination resistors of the MII signals close to corresponding output

Mode 1
Mode 1
Mode 1
Mode 0

Default:
Auto Negotiation Enable (Mode 1)
Speed 100M (Mode 1)
Full Duplex (Mode 1)
MII Isolate Disable (Mode 0)

C106, R101 voltage rating depend on customer system application

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