

When separating GRST from externally controlled signals

Signal description

CH1 : VDD33 (+3.3VD)

Power supply input to VDD33 pin of TUSB7320.

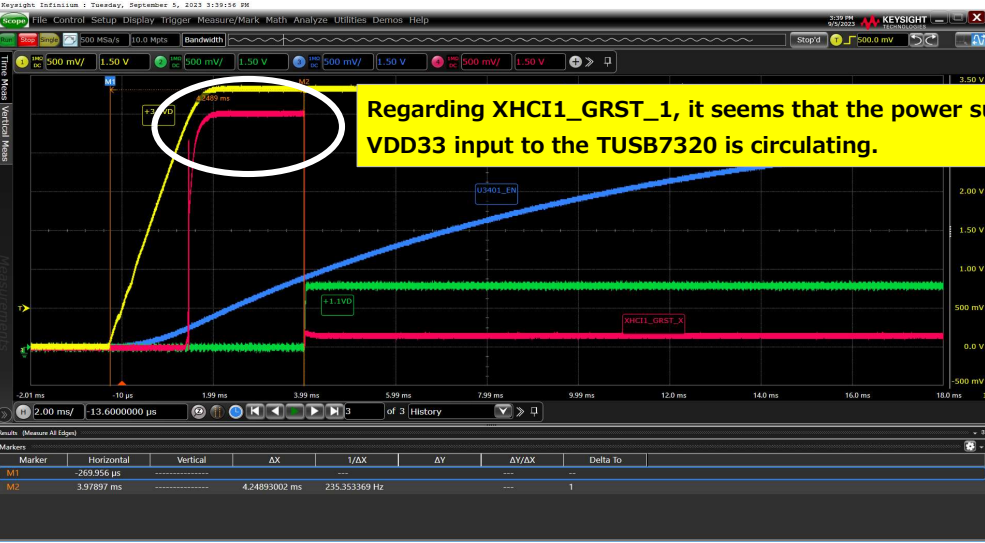
CH2 : VDD11 (+1.1VD_U1)

Power supply input to VDD11 pin of TUSB7320.

CH3 :

CH4 : XHCI1_GRST_X

Signal input to the GRST pin of the TUSB7320.



Enlarged waveform



measuring point

In order to check the influence of TUSB7320, the connection of XHCI1_GRST_X is left unconnected.

