

After correction (VDD33→VDD11)

Signal description

CH1 : VDD33 (+3.3VD)

Power supply input to VDD33 pin of TUSB7320.

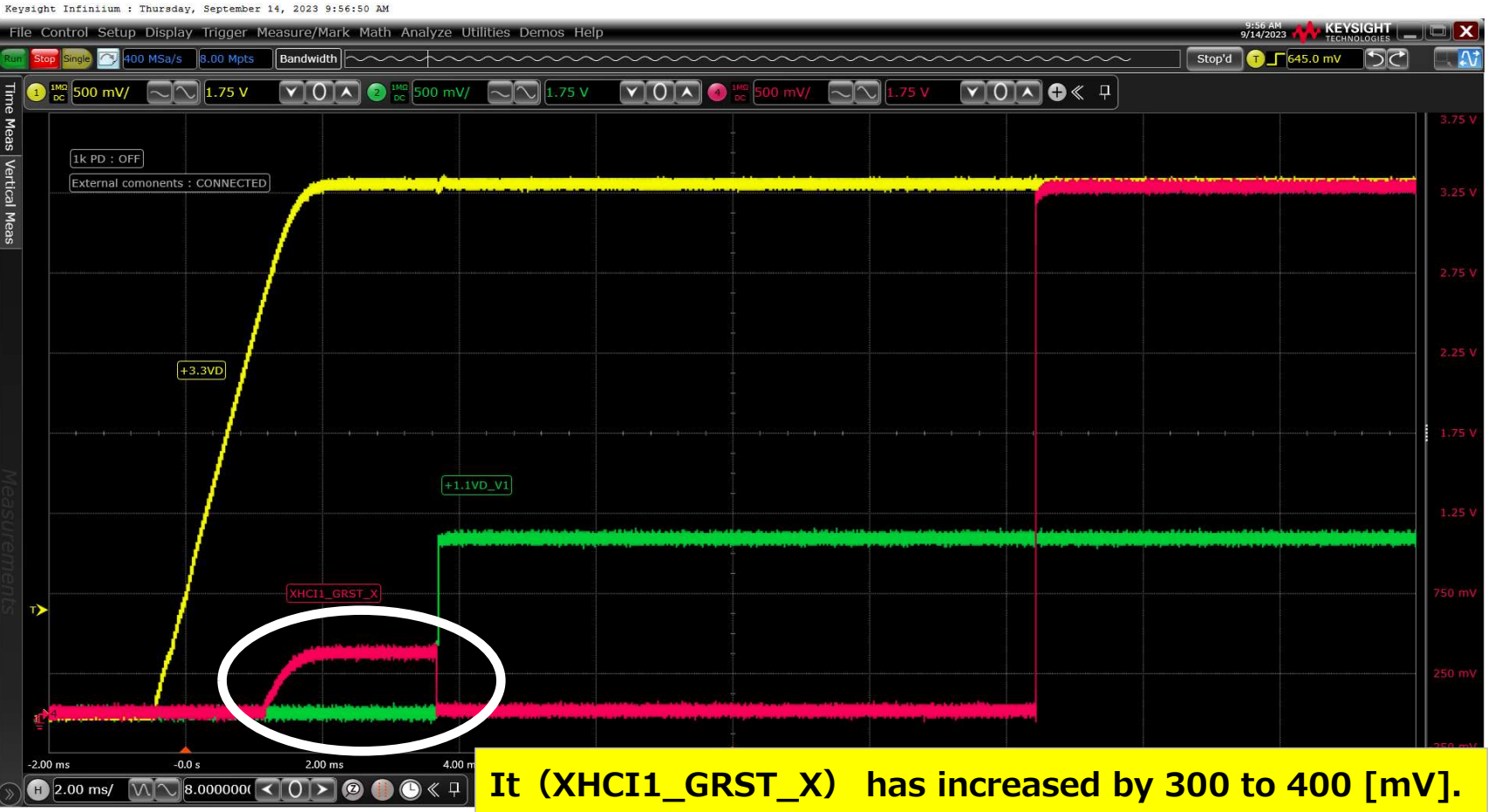
CH2 : VDD11 (+1.1VD_U1)

Power supply input to VDD11 pin of TUSB7320.

CH3 :

CH4 : XHCI1_GRST_X

Signal input to the GRST pin of the TUSB7320.



Connection diagram



GRST is controlled by an external controller.
At startup, it outputs "L" (0[V]).