

Signal description

- CH1 : VDD33 (+3.3VD)

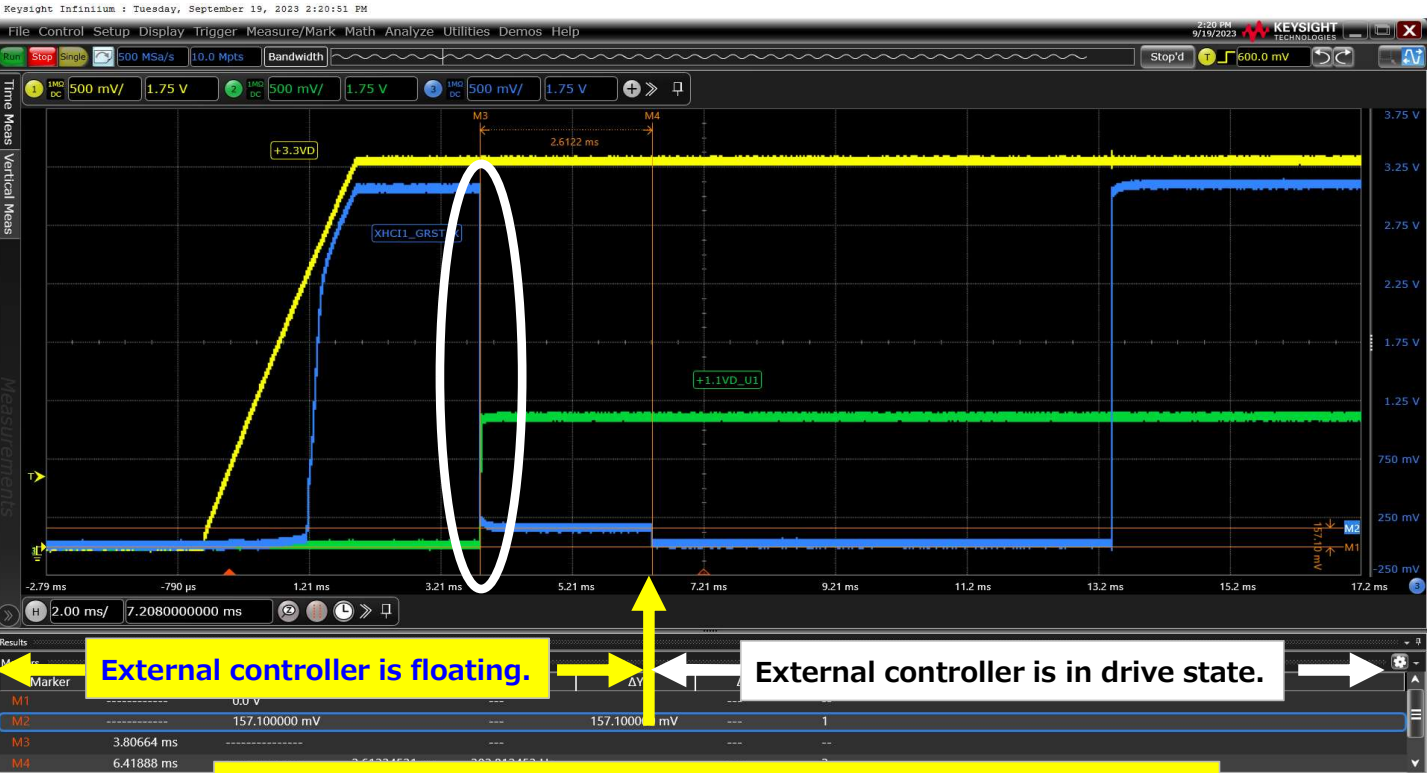
CH2 : VDD11 (+1.1VD_U1)

CH3 : XHCI1_GRST_X

CH4 :
- Power supply input to VDD33 pin of TUSB7320.

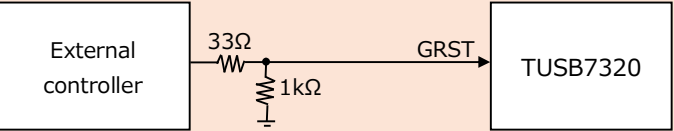
Power supply input to VDD11 pin of TUSB7320.

Signal input to the GRST pin of the TUSB7320.



[doubt]
I am suspicious that the state of GRST is changing triggered by +1.1V power supply activation even though the external controller is in a floating state.
I assume that the following blocks exist inside the TUSB7320.
Could you please comment?

Connection diagram



GRST is controlled by an external controller.
The external controller shall be floating until the power is turned on.

